## Original Article



# Emulating the impact of additional proton–proton interactions in the ATLAS simulation by presampling sets of inelastic Monte Carlo events

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Abstract The accurate simulation of additional interactions at the ATLAS experiment for the analysis of protonproton collisions delivered by the Large Hadron Collider presents a significant challenge to the computing resources. During the LHC Run 2 (2015-2018), there were up to 70 inelastic interactions per bunch crossing, which need to be accounted for in Monte Carlo (MC) production. In this document, a new method to account for these additional interactions in the simulation chain is described. Instead of sampling the inelastic interactions and adding their energy deposits to a hard-scatter interaction one-by-one, the inelastic interactions are presampled, independent of the hard scatter, and stored as combined events. Consequently, for each hard-scatter interaction, only one such presampled event needs to be added as part of the simulation chain. For the Run 2 simulation chain, with an average of 35 interactions per bunch crossing, this new method provides a substantial reduction in MC production CPU needs of around 20%, while reproducing the properties of the reconstructed quantities relevant for physics analyses with good accuracy.

#### **Contents**

1	Introduction	1
2	ATLAS detector	3
3	Overview of simulation chain	4
4	Computing performance comparison	5
5	Inner detector	8
	5.1 Detector readout	8
	5.1.1 Silicon pixel detector	8
	5.1.2 Silicon microstrip detector (SCT)	9
	5.1.3 Transition radiation tracker (TRT)	9
	5.2 Overlay procedure	9
	5.2.1 Pixel detector	9
	5.2.2 SCT detector	9

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5.2.3 TRT detector		
5.3 Validation results		
6 Calorimeters		
6.1 Detector readout		
6.2 Overlay procedure		
6.3 Validation results		
7 Muon spectrometer		
7.1 Detector readout and overlay procedure 14		
7.1.1 Monitored drift tubes (MDT) 14		
7.1.2 Cathode strip chambers (CSC) 14		
7.1.3 Resistive plate chambers (RPC) 14		
7.1.4 Thin-gap chambers (TGC) 15		
7.2 Validation results		
8 Trigger		
8.1 L1 calorimeter trigger simulation 18		
8.2 HLT simulation and performance		
9 Conclusions		
References		

## 1 Introduction

The excellent performance of the Large Hadron Collider (LHC) creates a challenging environment for the ATLAS and CMS experiments. In addition to the hard-scatter proton–proton (pp) interaction which is of interest for a given physics analysis, a large number of inelastic proton–proton collisions occur simultaneously. These are collectively known as pile-up. The mean number of these inelastic pp interactions per bunch crossing,  $\mu$ , also known as the pile-up parameter, characterises the instantaneous luminosity at any given time  $^1$ .

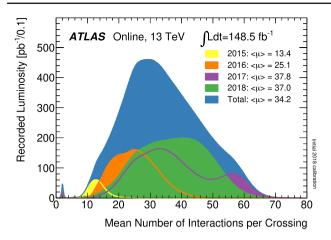
For physics analyses, pile-up is conceptually similar to a noise contribution that needs to be accounted for as it is unrelated to the hard-scatter event that is of interest for the analysis. Since nearly all analyses rely on Monte Carlo (MC)

 $<sup>^1\,</sup>$  Hereafter, the approximation that  $\mu$  is the same for all colliding bunch pairs is made.



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3 Page 2 of 35 Comput Softw Big Sci (2022) 6:3

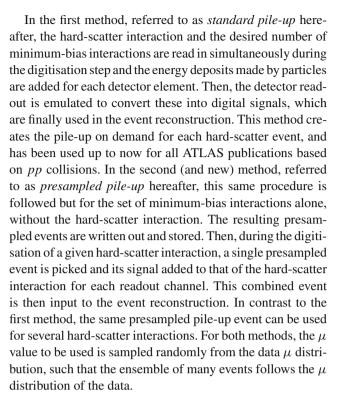


**Fig. 1** The  $\mu$  distribution observed for the ATLAS Run 2 data, for each year (2015–2018) separately and for the sum of all years [4]

simulation to predict the detector response to the physics process, it is crucial that the pile-up is modelled correctly as part of that simulation. The goal of the ATLAS MC simulation chain is to accurately reproduce the pile-up, such that it can be accounted for in physics analyses.

Within ATLAS, the pile-up is emulated by overlaying soft inelastic pp interactions, in the following called minimumbias interactions, generated with an MC generator, normally PYTHIA [1], according to the pile-up profile for a given datataking period. Figure 1 shows the  $\mu$  distribution for each year during Run 2 (2015–2018) and the sum of all years. The mean value is 34.2, but the distribution is broad and generally covers values between 10 and 70. The small peak at  $\mu \sim 2$  arises from special running periods with rather low luminosity. At the High Luminosity LHC (HL-LHC),  $\mu$  is expected to increase to about 200 [2]. The inelastic interactions include non-diffractive and diffractive interactions based on the Donnachie–Landshoff [3] model for the cross sections of the individual processes.

The simulation chain for MC events contains several steps, starting from the generation of the interactions with an MC generator (e.g., PYTHIA, SHERPA [5]). The interactions of the generated particles with the ATLAS detector are simulated using a GEANT4-based [6] simulation framework [7]. This is performed separately for the hard-scatter interactions of interest and a large number of minimum-bias interactions. Next, the readout of the detector is emulated via a process known as digitisation, which takes into account both the hardscatter and any overlapping minimum-bias interactions. In this article, two methods of performing the digitisation are compared. The goal of the new method, described below, is to reduce the computing resources required by creating a large set of pile-up events only once for an MC production campaign and then reusing these events for different hardscatter events. A similar method has been explored by the CMS collaboration [8].



If the detector signals were read out without any information loss, the two methods would give identical results. However, in reality, some information loss occurs due to readout thresholds applied or custom compression algorithms designed to reduce the data volume. This can lead to differences in the reconstructed quantities used in physics analyses. While in most cases for ATLAS, these differences were found to be negligible, in some cases, corrections were derived to reduce the impact on physics analyses, as is discussed in Sects. 5–8.

Within the ATLAS Collaboration, a significant validation effort took place to ensure that this presampled pile-up simulation chain reproduces the results from the standard pile-up simulation chain accurately, so that there is no impact on physics analyses whether one or the other is used. To this end, thousands of distributions were compared between the presampled and standard pile-up simulation chains. In this article, a representative subset of relevant distributions is shown. Only comparisons between the two methods are shown in this article; detailed comparisons of data with simulation can be found in various performance papers; see, e.g., Refs. [9–14].

The motivation for using the presampled pile-up simulation chain in the future is that it uses significantly less CPU time than the standard pile-up simulation chain. As is discussed in Ref. [15], savings in CPU, memory, and disk space requirements are pivotal for the future running of the ATLAS experiment. Additionally, the presampled pile-up simulation chain can also be seen as a step towards using minimum-bias data, instead of presampled simulated events, for emulating



Comput Softw Big Sci (2022) 6:3 Page 3 of 35 3

the pile-up, which could potentially improve the accuracy of the modelling of the pile-up interactions. However, the pileup emulation with data is not yet validated and not the subject of this article.

The article is organised as follows. A description of the ATLAS detector is given in Sect. 2, highlighting the aspects that are most relevant for the pile-up emulation. Section 3 describes both the standard and presampled pile-up simulation chain, and Sect. 4 compares their CPU and memory performances. In Sects. 5–8, the challenges in the inner detector, calorimeters, muon system, and trigger are described and comparisons of the impact of the old and new methods are shown. For these comparisons, a variety of different MC samples are used, based on what is most appropriate for the validation of a given object within the particular detector subsystem.

For all studies presented in this article, unless otherwise stated, the distribution of the average number of events per bunch crossing follows the distribution observed in the ATLAS data in 2017, with an average  $\mu$  value of 37.8 (see Fig. 1). The ATLAS detector configuration corresponds to that of Run 2. As the detector configuration evolves in the future, the new presampled pile-up method will need to be validated for those new detector elements.

#### 2 ATLAS detector

The ATLAS detector [16] at the LHC covers nearly the entire solid angle around the collision point. It consists of an inner tracking detector surrounded by a thin superconducting solenoid, electromagnetic and hadronic calorimeters, and a muon spectrometer incorporating three large superconducting toroidal magnets. A two-level trigger system is used to select interesting events [17]. The first-level (L1) trigger is implemented in hardware and uses a subset of detector information to reduce the event rate from 40 MHz to 100 kHz. This is followed by a software-based high-level trigger (HLT) which reduces the event rate to an average of 1 kHz.

At the LHC, typically 2400 bunches from each of the two proton beams cross each other at the ATLAS interaction point per beam revolution, with one bunch crossing (BC) taking place every 25ns. In each BC, several *pp* interactions may occur. Whenever an L1 trigger signal is received for a given BC, the entire detector is read out and processed in the HLT to decide whether the event is stored for further analysis.

The inner detector (ID) is immersed in a 2 T axial magnetic field and provides charged-particle tracking in the pseudorapidity<sup>2</sup> range  $|\eta|$  < 2.5. The high-granularity silicon

pixel detector (Pixel), including an insertable B-layer (IBL) [18,19] added in 2014 as a new innermost layer, covers the vertex region and typically provides four measurements per track; the first hit normally being in the innermost layer. It is followed by the silicon microstrip tracker (SCT) which usually provides four two-dimensional measurement points per track. These silicon detectors are complemented by a straw tracker (transition radiation tracker, TRT), which enables radially extended track reconstruction with an average of  $\sim$  30 hits per track up to  $|\eta|=2.0$ . Additionally, the transition radiation capability provides separation power between electrons and charged pions.

The calorimeter system covers the pseudorapidity range  $|\eta| < 4.9$ . Within the region  $|\eta| < 3.2$ , electromagnetic (EM) calorimetry is provided by barrel (EMB) and endcap (EMEC) high-granularity lead/liquid-argon (LAr) electromagnetic calorimeters, with an additional thin LAr presampler covering  $|\eta| < 1.8$  to correct for energy loss in material upstream of the calorimeters. Hadronic calorimetry is provided by the steel/scintillator-tile (Tile) calorimeter, segmented into three barrel structures within  $|\eta| < 1.7$ , and two copper/LAr hadronic endcap calorimeters (HEC). The solid angle coverage is completed with forward copper/LAr and tungsten/LAr calorimeter (FCAL) modules optimised for electromagnetic and hadronic measurements, respectively.

The muon spectrometer (MS) comprises separate trigger and high-precision tracking chambers measuring the deflection of muons in a toroidal magnetic field generated by the superconducting air-core magnets. The field integral of the toroids ranges between 2.0 and 6.0 T across most of the detector. A set of precision chambers covers the region  $|\eta| < 2.7$  with three stations of monitored drift tubes (MDTs), complemented by cathode strip chambers (CSCs) in the forward region, where the background is highest. The muon trigger system covers the range  $|\eta| < 2.4$  with resistive plate chambers (RPCs) in the barrel, and thin-gap chambers (TGCs) in the endcap regions.

The integration times of the different subdetectors vary significantly, mostly due to the charge drift times depending on the material and geometry of the respective detector system. In most cases, the integration time exceeds 25 ns, i.e., the time between two BCs. In such cases, the signal from events that occurred in previous BCs contaminates the signal in the triggered BC. This is often referred to as out-of-time pile-up and needs to be considered for the simulation, in addition to the in-time pile-up which accounts for signals generated

the LHC ring, and the *y*-axis points upwards. Cylindrical coordinates  $(r, \phi)$  are used in the transverse plane,  $\phi$  being the azimuthal angle around the *z*-axis. The pseudorapidity is defined in terms of the polar angle  $\theta$  as  $\eta = -\ln\tan(\theta/2)$ . Angular distance is measured in units of  $\Delta R \equiv \sqrt{(\Delta \eta)^2 + (\Delta \phi)^2}$ .



 $<sup>^2</sup>$  ATLAS uses a right-handed coordinate system with its origin at the nominal interaction point (IP) in the centre of the detector and the *z*-axis along the beam pipe. The *x*-axis points from the IP to the centre of

3 Page 4 of 35 Comput Softw Big Sci (2022) 6:3

by interactions occurring inside the BC corresponding to the hard-scatter event.

Figure 2 shows the readout windows considered for the simulation of each of the detector systems. The MDTs have the longest integration time, 750 ns, with 32 BCs prior to the trigger and 6 BCs after the trigger being considered. For the LAr calorimeter, it is only slightly shorter. For the inner detector (Pixel, SCT, and TRT), the integration time is much shorter, and only the 1–2 BCs before and after the trigger need to be considered.

## 3 Overview of simulation chain

As is described above, the ATLAS simulation chain [7], used to produce MC samples to be used in physics and performance studies, is divided into three steps: generation of the event and immediate decays, particle tracking and physics interactions in the detector, based on GEANT4 (G4), and digitisation of the energy deposited in the sensitive regions of the detector into voltages and currents to emulate the readout of the ATLAS detector. This simulation chain is integrated into the ATLAS software framework, ATHENA [20]. Finally, a series of reconstruction algorithms is applied in the same way as for the data, where final physics objects such as jets, muons, and electrons are reconstructed [16]. Each step can be run as an individual task, but to save disk space, the digitisation step is usually performed in the same task as the reconstruction step, such that the intermediate output format from the digitisation step only needs to be stored locally on the computing node and can be discarded after the reconstruction step is finished.

The G4 simulation step is run by itself and, since it is independent of the detector readout configuration, the trigger, and the pile-up, it is often run significantly earlier than the digitisation and reconstruction, which depend on these aspects. The G4 simulation is the most CPU intensive, and thus, it is desirable to run this as rarely as possible.

The ATLAS digitisation software converts the energy deposits (HITS) produced by the G4 simulation in the sensitive elements into detector response objects, known as *digits*. A digit is produced when the voltage or current of a particular readout channel rises above a preconfigured threshold within a particular time window. Some of the subdetectors read out just the triggered BC, while others read out several bunch crossings, creating digits for each. For each digit, some subdetectors (e.g., SCT) record only the fact that a given threshold has been exceeded, while others (e.g., Pixel or LAr) also retain information related to the amplitude. The digits of each subdetector are written out as Raw Data Objects (RDOs), which contain information about the readout channel identifier and the raw data that are sent from the detector front-end electronics.

For any given hard-scatter interaction, the additional pileup interactions must be included in a realistic model of the detector response. For this purpose, minimum-bias events are generated using the PYTHIA event generator with the NNPDF2.3LO [21] parton distribution function and the A3 [22] set of tuned parameters, and then simulated and stored in separate files. To avoid potential issues due to low statistics of relatively hard minimum-bias events, containing objects with high transverse momentum  $(p_T)$ , the sampled events are in fact split into two distinct equal-sized samples: a high- $p_T$ sample composed of events containing jets or photons with  $p_{\rm T} > 35 \, {\rm GeV}$  and a low- $p_{\rm T}$  sample composed of the remaining events. Events are then selected from each sample based on their relative cross-section to avoid duplicating distinctive hard events, which may give rise to visible features during analysis.

In the current standard pile-up simulation chain, the simulation files of both the hard-scatter event and the desired number of minimum-bias events are read in concurrently at the digitisation step and the HITS are combined. For each hard-scatter event, a value of  $\mu$  is assigned by randomly sampling the  $\mu$  distribution corresponding to the relevant datataking period. Most subdetector responses are affected by interactions from neighbouring bunch crossings: as is shown in Fig. 2, up to 32 BCs before and 6 BCs after the triggering BC may contribute signal to the trigger BC. For the average  $\mu$  value of 37.8 during 2017 data taking, this implies that simulating the impact of pile-up on any given hard-scatter event requires approximately  $(32 + 1 + 6) \times 38 = 1482$ minimum-bias events on average to be selected at random (from the simulated event files) and processed as part of the digitisation step. Each of these bunch crossings is taken to have the same value of  $\mu$  as the trigger bunch crossing<sup>3</sup>. The number of minimum-bias events (N) to include for each bunch crossing is drawn at random from a Poisson distribution with a mean of the  $\mu$  value for that bunch crossing. After the energy deposits in the trigger BC due to all contributing BCs have been combined, the detector response is emulated. This workflow is illustrated in Fig. 3.

The new presampled pile-up simulation chain is illustrated in Fig. 4. Rather than digitising the minimum-bias interactions each time a hard-scatter event is produced, a large sample of pile-up events is produced by pre-combining the simulated pile-up interactions, according to the  $\mu$  distribution of the data campaign, during a separate digitisation step, termed presampling<sup>4</sup>. Here, the sampling is done exactly as for the standard pile-up, the only difference being that there



<sup>&</sup>lt;sup>3</sup> In data, there are variations between adjacent bunches of order 10% [23], but this is not emulated in the MC simulation.

<sup>&</sup>lt;sup>4</sup> For the calorimeters and the SCT, the digitised output stored in the presampled events is amended, so that the presampled pile-up can be applied accurately.

Comput Softw Big Sci (2022) 6:3Page 5 of 35

Fig. 2 The time windows considered for the simulation of each subdetector. The dark blue BCs are those where a signal in that BC can contaminate the signal in the triggered BC (i.e., BC 0), while the light blue-coloured BCs cannot affect the triggered BC

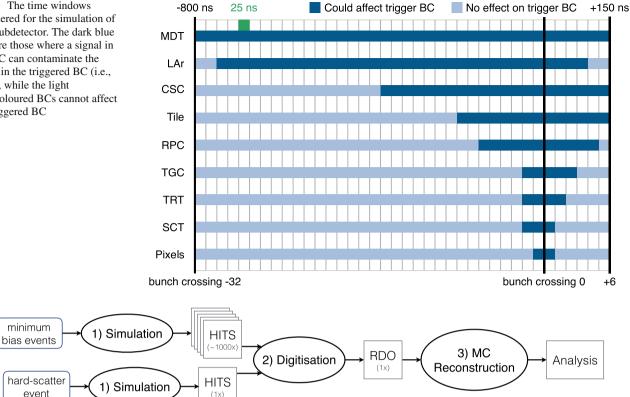


Fig. 3 Current workflow diagram from simulation to physics analysis. The oval steps represent an action, while the boxes represent data files of a given format. The final box is the reconstructed data in analysis format

is no hard-scatter event. These presampled pile-up events are written out in RDO format as pile-up RDO datasets and typically contain several million events. Each simulated hardscatter interaction is then digitised and combined with an event sampled from these pile-up datasets (step 3 in Fig. 4, called overlay). Here, instead of HITS for each channel, the signals of the RDO or digit (depending on the subdetector) in the hard-scatter event and the presampled event are overlaid. To avoid double-counting, instrumental noise associated with the detector electronics is included solely in the presampled RDOs and not included in the subsequent hard-scatter digitisation. Since the digitisation, presampling, and reconstruction steps are typically combined into a single task in the production workflow, the output is written locally to an RDO file that is then input to the reconstruction software; this local RDO file is subsequently discarded. The pile-up RDO datasets necessary for a given digitisation task are about five times smaller than the many minimum-bias HITS required in the standard pile-up simulation chain.

The main benefit of the presampled pile-up simulation chain is that the CPU and I/O requirements of the digitisation are significantly lower and have a much smaller dependence on  $\mu$ , as is discussed in Sect. 4. However, if a threshold or compression has been applied to the signal when writing the RDO/digit, this results in some loss of information and thereby could reduce the accuracy of the simulation when using the presampled pile-up method, as is discussed in Sects. 5–8.

For all the comparisons shown in these sections, the hardscatter events are identical for the two methods, but the pileup events are different. This makes the estimation of the uncertainties difficult as the hard-scatter is fully correlated, while the pile-up is not. As most of the quantities are selected to be sensitive to pile-up, the uncertainties are calculated assuming the two samples are uncorrelated, but in some distributions, this leads to an overestimate of the uncertainties, e.g., in the reconstruction efficiencies of tracks and leptons and in the trigger efficiencies.

## 4 Computing performance comparison

In this section, the performances of the two simulation chains are compared in terms of CPU time, memory usage, and I/O. The validation in terms of physics performance is presented in subsequent sections.

The main computing performance benefit of the presampled pile-up simulation chain stems from the fact a pile-up dataset is only created once per MC production campaign, and then, the individual events within that dataset are used for



3 Page 6 of 35 Comput Softw Big Sci (2022) 6:3



Fig. 4 The presampled pile-up workflow schema. The oval steps represent an action, while the boxes represent data files of a given format. The final box is the reconstructed data in analysis format

multiple hard-scatter MC samples, as opposed to being created on demand independently for each MC sample. An MC production campaign happens typically once per data-taking period and comprises billions (B) of hard-scatter events and thousands of individual samples. A sample is defined as a set of MC events generated using the same input parameters, e.g., a sample of  $t\bar{t}$  events produced by a certain MC generator with a given set of input parameters. The same presampled pile-up event can thus be overlaid on many different hard-scatter events from different MC samples. In doing so, care needs to be taken to ensure that no undesirable effects on physics analyses occur due to reusing the same pile-up events, as is discussed below.

In ATLAS, typically 70% of the CPU resources are devoted to MC production via the simulation chain; the remainder is used for data processing and user analyses. At present, with the Run 2 pile-up profile, the simulation chain CPU usage is broken down into about 15% for event generation, 45% for G4 simulation, 20% for digitisation, and 20% for other tasks (reconstruction, trigger, and event writing). The presampled pile-up scheme decreases the digitisation time to a negligible level by reusing the pile-up events more efficiently and thus reduces the overall CPU resources required for MC production by about 20%, as is discussed below.

The average CPU time per event in the standard and presampled pile-up simulation chains as a function of  $\mu$  is shown in Fig. 5. As can be seen, both depend linearly on  $\mu$ , but the slope is about 50 times larger for the standard pile-up than for the presampled pile-up simulation chain. For the standard pile-up simulation chain, the CPU time required at  $\mu=70$  is 7.5 times larger than for  $\mu=10$ , while for the presampled pile-up method, the corresponding increase in CPU time is only a factor of 1.2. Extrapolating this to  $\mu=200$ , the CPU time is 20 times greater than for  $\mu=10$  for the standard method and < 2 times higher for the presampled pile-up method. However, this comparison does not account for the CPU time required for the production of the presampled pileup dataset, which is needed to assess the overall CPU benefit in a realistic campaign, as is discussed below.

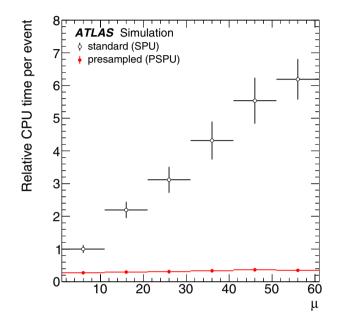


Fig. 5 Comparison of the average CPU time per event in the standard pile-up (SPU) digitisation (black open circles) and the presampled pile-up (PSPU) digitisation (red filled circles) as a function of the number of pp collisions per bunch crossing ( $\mu$ ). The CPU time is normalised to the time taken for the standard pile-up for the lowest  $\mu$  bin. For this measurement,  $t\bar{t}$  events are used for the hard-scatter event. The average is taken over 1000 events and the vertical error bars represent the standard deviation of the separate CPU time measurements. For the standard pile-up digitisation, the slope of the relative CPU time per event versus  $\mu$  is 0.108, while for the presampled pile-up digitisation, it is 0.002

Figure 6 shows the memory used by the various steps as a function of time for the different production steps for the two simulation chains. The time estimate is based on running 2000 hard-scatter events for the 2017  $\mu$  distribution on the same CPU in all cases, so that the three scenarios can be directly compared. The absolute number, of course, depends on the CPU used and the  $\mu$  distribution. The presampling takes about 7 s per event. The standard digitisation takes about 8 s per event, while the hard-scatter digitisation and overlay of the presampled pile-up take about 0.5 s. The remaining steps, which are the same for the two simula-



Comput Softw Big Sci (2022) 6:3 Page 7 of 35 3

tion chains, take about 8 s and include the trigger emulation, reconstruction, and the writing of the analysis format to disk.

When comparing the required CPU time between the two chains, the following equations provide a good approximation. For the standard pile-up simulation chain, the time  $T_{\rm standard}$  required is simply given by the number of events in the campaign times the total time  $t_{\rm digi} + t_{\rm other}$ , where  $t_{\rm other}$  is the sum of the times needed for reconstruction, trigger, and writing the event to disk. Thus

$$T_{\text{standard}} = N_{\text{MC-campaign}} \times (t_{\text{digi}} + t_{\text{other}}),$$

where  $N_{\text{MC-campaign}}$  is the number of hard-scatter events produced in a given MC-campaign.

For the presampled pile-up simulation chain, the time  $T_{\rm presample}$  required is given by the number of events in the campaign times the time needed for the overlay step and other aspects plus the time required for the presampling. This last contribution is given by the total number of presampled pile-up events required  $(N_{\rm pp})$  multiplied by the event digitisation time, so that the required time is

$$T_{\text{presample}} = N_{\text{MC-campaign}} \times (t_{\text{overlay}} + t_{\text{other}}) + N_{\text{pp}} \times t_{\text{digi}}.$$

The time reduction factor of the presampled pile-up simulation chain compared to the standard is then given by

$$\begin{split} \frac{T_{\text{presample}}}{T_{\text{standard}}} &= \frac{N_{\text{MC-campaign}} \times (t_{\text{overlay}} + t_{\text{other}}) + N_{\text{pp}} \times t_{\text{digi}}}{N_{\text{MC-campaign}} \times (t_{\text{other}} + t_{\text{digi}})} \\ &\approx \frac{1}{t_{\text{other}} + t_{\text{digi}}} \left[ t_{\text{other}} + t_{\text{digi}} \times \frac{N_{\text{pp}}}{N_{\text{MC-campaign}}} \right], \end{split}$$

where the approximation  $t_{\text{overlay}} \ll t_{\text{other}}$  is made, based on the observations from Fig. 6.

It is immediately clear that the presampled pile-up simulation chain uses less CPU time than the standard pile-up simulation chain, since  $N_{\rm pp} < N_{\rm MC-campaign}$ . Choosing the exact value for  $N_{\rm pp}$ , however, is not trivial. In general, the reuse of a given presampled pile-up event within a particular MC sample, representing an individual hard-scatter physics process, should be avoided if possible; otherwise, each overlaid hard-scatter plus pile-up event would not be statistically independent. Such oversampling would be particularly worrisome if the presampled pile-up event in question contained a distinctive feature, such as a high-transverse-momentum jet, which could cause difficulties in using the MC sample for the statistical interpretation of the data distributions. In practice, such a repetition would not be statistically significant in the bulk of a distribution, but could be problematic in the tails, where there are few events. Given this, it is reasonable that the value for  $N_{\rm DD}$  be chosen to be about the size of the largest individual MC sample, so that no event is repeated within it.

For the ATLAS Run 2 MC-campaign,  $N_{\text{MC-campaign}} \sim 10\,\text{B}$  and the single largest individual MC sample had a size

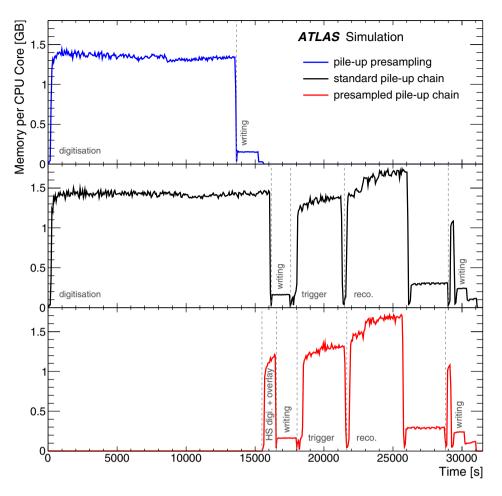
of 0.2 B events. Allowing for some increase in these sizes to be commensurate with the size of the evolving data samples,  $N_{\rm pp} \sim 0.5 \, \rm B$  should thus be sufficient. Taking the resulting  $N_{\text{MC-campaign}}/N_{\text{pp}} \sim 20$ , along with  $t_{\text{other}} \approx t_{\text{digi}}$  (as seen in Fig. 6), the ratio of the times required for the two methods is  $T_{\rm presample}/T_{\rm standard} \sim 0.53$ . Hence, the presampled pile-up simulation chain provides a CPU saving of 47% for the combined digitisation, reconstruction, trigger, and writing steps, compared to the standard pile-up simulation chain. If the time required for reconstruction and trigger is further improved (as is planned for Run 3), or the digitisation time were to further increase due to pile-up, the ratio would decrease; e.g., if  $t_{\text{other}} \approx t_{\text{digi}}/2$ , a CPU saving of 63% would be realised. The  $\sim 50\%$  reduction translates to an overall MC production CPU saving of around 20%, since 60% of the CPU time required for the simulation chain is at present used for event generation and G4 simulation, which is not affected by this improvement. These are illustrative examples that confirm the intuitive expectation that performing the digitisation just once per campaign is much more effective than doing it for each simulated hard-scatter event, as the number of presampled events needed is by construction smaller than the number of hard-scatter events.

From the memory usage point of view, the presampled pile-up load is similar to the standard pile-up and well below the (soft) production limit of  $\sim 2$  GB per core (see Fig. 6) for the  $\mu$  values observed during Run 2 and expected for Run 3. However, compared to the standard pile-up, the presampled pile-up simulation chain puts less stress on the I/O system both because, as is mentioned above, the presampled pile-up dataset files are about a factor of five smaller and because they can be read sequentially. The sequential reading is possible, because the random access necessary to combine the minimum-bias input files in the standard pile-up is now performed only once at the presampling stage. Hence, the presampled pile-up RDO production, with its heavier requirements, can be performed on a limited subset of ATLAS MC production sites designed to cope well with such workloads; the subsequent presampled pile-up simulation chain will then run on all resources available to ATLAS, utilising the  $\approx 20\%$ of sites that have previously been excluded for reconstruction due to insufficient I/O or disk resources. The reduced input size also enables the usage of opportunistic resources such as high-performance computing (HPC) sites, which typically have less available storage to ATLAS. The smaller I/O requirements from the presampled pile-up simulation chain jobs simplify the production workflow, and make it possible to transfer the pile-up datasets on demand to the computing node at a given production site, where they are needed. If network speed is further increased in the future, it might even be possible to access them directly via the network during the job from a remote storage site.



3 Page 8 of 35 Comput Softw Big Sci (2022) 6:3

Fig. 6 The memory usage profile of different production steps as a function of the job wall-time for 2000 hard-scatter events. The presampling (top), the standard pile-up (middle) and the presampled pile-up (bottom) simulation chain are compared. In the latter case, "HS digi." refers to the digitisation of the hard-scatter event. The underlying  $\mu$  distribution is that corresponding to the 2017 data  $\mu$  distribution



The Analysis Object Data (AOD) event size written to disk is the same for both methods, i.e., there is neither advantage nor disadvantage in using the presampled pile-up simulation chain in this regard. However, the many simulated minimumbias events do not have to be distributed as widely any more throughout the year as they only need to be accessed once for creating the presampled events. These presampled events need to be made available widely though. It is expected that these two effects roughly cancel out, but operational experience is needed to understand how to distribute the presampled sample in the most effective way.

#### 5 Inner detector

The ID consists of three subdetectors which all use different technologies as discussed in Sect. 2. Each of them has separate digitisation software and hence a different treatment for the presampled pile-up procedure is required for each. In this section, the readout of the three ID subdetectors is described, along with the presampled pile-up procedure for each. Validation results are also presented.

## 5.1 Detector readout

# 5.1.1 Silicon pixel detector

The charge produced by a particle traversing a silicon pixel is integrated if it passes a set threshold. In Run 2, this threshold is typically around 2500 electrons for the IBL and 3500 electrons for the remainder of the Pixel detector. The resulting charge deposited by a minimum-ionising particle (MIP) that traverses a single pixel is typically 16,000 and 20,000 electrons, respectively. The amount of charge deposited by a particle traversing the detector varies depending on the path length of the particle through the active silicon and can be spread across multiple pixels. The length of time during which the charge signal exceeds the threshold, termed time-over-threshold (ToT), is recorded. The ToT is roughly proportional to the charge. While most of the charge drifts to the pixel readout within the 25 ns bunch crossing time of the LHC, there is a small fraction which may take longer and only arrive in the subsequent bunch crossing (BC+1). Thus, in any given bunch crossing, the pile-up events both from the previous and the current bunch crossings contribute hits.



Comput Softw Big Sci (2022) 6:3 Page 9 of 35

## 5.1.2 Silicon microstrip detector (SCT)

For the SCT, the readout is in principle similar to the Pixel detector in that a threshold is applied for each strip. However, in contrast to the pixel readout, it is purely digital, i.e., neither the charge nor the ToT is stored for a given strip, just a bit, X = 0 or 1, to signal a hit (1) or the absence of a hit (0). Hence, the hit from the current BC as well as that of the two adjacent bunch crossings (i.e. BC–1 and BC+1) are read out. Several data compression modes have been used since the first LHC collisions; they are defined by the hit pattern of the three time bins:

- Any-hit mode (1XX, X1X or XX1); channels with a signal above threshold in either the current, previous, or next bunch crossing are read out.
- Level mode (X1X); only channels with a signal above threshold in the current bunch crossing are read out.
- Edge mode (01X); only channels with a signal above threshold in the current bunch crossing and explicitly no hit in the preceding bunch crossing are read out.

The data can be compressed further by storing, for adjacent strips with hits above threshold, only the address of the first strip and the number of these adjacent strips. When this compression is invoked, the information about which of the three bunch crossings observed a hit for a given strip is lost. When the LHC is running with 25 ns bunch spacing, SCT RDOs are required to satisfy the 01X hit pattern to be considered during event reconstruction to suppress pile-up from the previous crossings.

# 5.1.3 Transition radiation tracker (TRT)

When a particle crosses one of the tubes in the TRT, the electrons drift to the anode wire, producing an electrical signal. If the charge of that signal exceeds a low discriminator threshold, a corresponding hit is recorded, in eight time slices of 3.125 ns each. The drift time is calculated based on the time of the first hit, which is subsequently converted to distance to give a drift-circle radius. In addition, to provide information for electron identification, a record is kept of whether a high discriminator threshold is exceeded in any of the eight time slices. This information is stored for the previous, current, and subsequent bunch crossings (i.e., BC–1, BC, BC+1).

## 5.2 Overlay procedure

The quantities which are overlaid for the inner detector are the RDOs. Due to the high number of channels in the inner detector, zero suppression<sup>5</sup> is employed to reduce the amount of data read out and stored from the detector. Since for the ID, the RDOs do not contain the full information of the HITS created by simulation, the overlay of RDO information is less accurate than the overlay of the underlying HITS information. However, the impact on physics observables is generally found to be negligible as is described in the following; where a difference is observed, a parameterised correction is derived as is described below.

#### 5.2.1 Pixel detector

The pixel detector has in excess of 90 M readout channels and a very high granularity. The single-pixel occupancy is below  $2.5 \times 10^{-5}$  per unit  $\mu$  in all layers [24], so even at  $\mu \sim 100$ , it is below 0.25%. Therefore, the chance that a single pixel which contains a signal due to a charged particle from the hard-scatter event also contains one from the overlapping in-time pile-up events is < 0.25%. A pixel RDO contains the channel identifier and a 32-bit packed word containing the ToT, a bunch-crossing identifier, and information related to the L1 trigger not relevant in simulation. In the presampled pile-up, if an RDO of a given channel contains a hit above threshold from either the hard-scatter event or the pile-up event, but not both, the corresponding RDO is kept and written out. In the  $\lesssim 0.25\%$  of cases where it contains a hit above threshold in both the hard-scatter event and the pile-up event, only the hard-scatter RDO is kept to retain the ToT (and thus, for example, the energy deposited per path length dE/dx) from the signal process. This causes a small loss of information as in principle the ToT would be modified by the presence of the additional charge deposited in that pixel from the pile-up events. However, as it only affects a small fraction of cases, it has a negligible impact on the overall physics performance. In addition, there could be a loss of information if, for a given pixel, both the hard-scatter event and the pile-up event produce charge deposits which are below the readout threshold but whose sum is above the threshold. In this case, the presampled pile-up method will register no hit, while the standard method will register a hit above threshold. This effect could reduce the cluster size and the ToT. But again, only a very small fraction of pixels are affected, so both the cluster size and the ToT agree well between the two methods.

#### 5.2.2 SCT detector

The SCT is a strip detector with 6.3 M readout channels and an occupancy in high pile-up conditions of  $\mathcal{O}(1\%)$ ; consequently, the pile-up modelling is more critical than for the

<sup>&</sup>lt;sup>5</sup> Rather than reading out all channels, only those channels containing data (above a certain significance level) are recorded.



3 Page 10 of 35 Comput Softw Big Sci (2022) 6:3

pixel detector. To facilitate accurate modelling, it is important that presampled RDOs be stored in any-hit mode, without further compression, to ensure that the impact of out-of-time pile-up is modelled correctly. To combine hard-scatter and pile-up RDOs, all of the strips that are hit on a module are unpacked from the respective RDOs and repacked into RDOs using the desired compression mode. Loss of information only occurs if hits in both the hard-scatter event and the pile-up event are below threshold, but the sum of the two charges is above threshold. In this case, in the standard digitisation a hit would be present, while with the presampled pile-up procedure, it is not, causing the presampled pile-up procedure potentially to result in fewer SCT hits per track. The impact is, however, negligible as is shown below.

## 5.2.3 TRT detector

The TRT is a straw tube detector with 320 k readout channels, and in high pile-up conditions, the occupancy of the TRT exceeds 10%. Therefore, pile-up has a major impact on the TRT signals. If the channel identifiers in the hard-scatter and pile-up events are the same, the data word stored is set to a bitwise logical OR of the corresponding raw words. This results in some loss of information as the sum of the charge signals will be larger, and thus more easily pass a given threshold, than would be just the sum of the digitised signals. This particularly impacts the fraction of hits that pass the high discriminator threshold.

A correction for this effect is applied to improve the level of agreement between the presampled pile-up and the standard digitisation. For this correction, a high-threshold (HT) bit is activated according to a randomised procedure, tuned to describe the standard digitisation. The rate of randomly activating a high-threshold bit is parameterised as a linear function of the occupancy of the TRT in the simulated pile-up events (a proxy for the average energy deposited in the pileup events) and whether the charged particle that is traversing the straw from the hard-scatter event is an electron or not. A different correction is applied for electrons as they produce significant amounts of transition radiation in the momentum range relevant for physics analysis (5-140 GeV), while all other particles do not. The correction corresponds to approximately a 10% (5%) increase in the number of HT hits for electrons (non-electrons) at the average Run 2  $\mu$  value.

# 5.3 Validation results

To validate the presampled pile-up digitisation for each of the subdetectors, the properties of tracks in simulated  $t\bar{t}$  events, where at least one W boson from the top quarks decays leptonically, are compared between the presampled pile-up method and the standard digitisation. The  $t\bar{t}$  events are cho-

sen, because they represent a busy detector environment and contain tracks from a wide range of physics objects.

The primary track reconstruction is performed using an iterative track-finding procedure seeded from combinations of silicon detector measurements. The track candidates must have a transverse momentum  $p_T > 500\,\mathrm{MeV}$  and  $|\eta| < 2.5$  and meet the following criteria: a minimum of seven pixel and SCT clusters, a maximum of either one pixel or two SCT clusters shared among more than one track, and no more than two holes in the SCT and pixel detectors combined. The tracks formed from the silicon detector measurements are then extended into the TRT detector. Full details, including a description of the TRT track extensions, can be found in Refs. [25,26].

Figure 7 shows the number of pixel clusters associated with a muon track as a function of  $\mu$ , and the unbiased residual in the local x coordinate, which corresponds to the direction with the highest measurement precision. The unbiased residual is the distance of the cluster from the track trajectory (not including the cluster itself) at the point where that trajectory crosses the pixel sensor. Figure 8 shows the corresponding quantities for the SCT. In all cases, the presampled pile-up and standard digitisation are shown, and good agreement is observed between the two methods.

Figure 9 shows a comparison of the number of high-threshold TRT drift circles as a function of  $\mu$  for muons and electrons. As is explained above, due to the high occupancy of the detector, the number of high-threshold drift circles is particularly sensitive to the presampled pile-up procedure. After the parameterised corrections discussed in Sect. 5.2 are applied, the average numbers of high-threshold drift circles for electrons and muons are each comparable for the two methods.

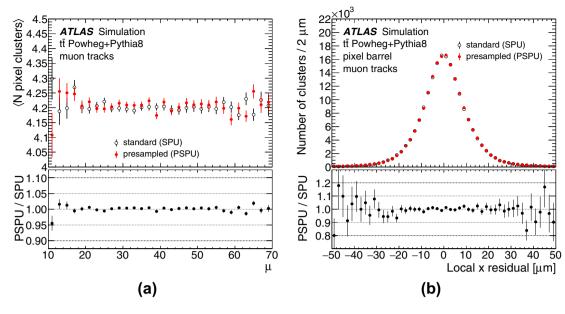
The resolution of all track parameters was examined for both methods, and they were found to agree well. Figure 10 shows the difference between the reconstructed and true values for the impact parameter of the track relative to the primary vertex  $(d_0)$ , measured in the transverse plane, and the track curvature  $(q/p_{\rm T}^{\rm track})$  for muons in  $t\bar{t}$  events. Finally, the track reconstruction efficiency is shown in Fig. 11 as a function of the  $p_{\rm T}$  and  $\eta$  of all tracks identified in  $t\bar{t}$  events. The level of agreement between the two methods is better than 0.5%.



<sup>&</sup>lt;sup>6</sup> A hole is defined as the absence of a hit on a traversed sensitive detector element.

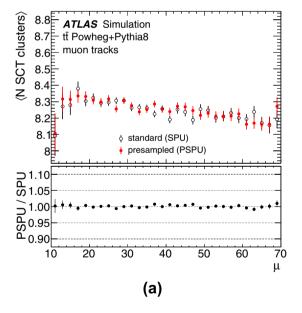
<sup>&</sup>lt;sup>7</sup> The proportion of transition radiation, and hence high-threshold hits for pions, which dominate in pile-up events, will behave similarly to muons due to their comparable mass. Muons are chosen here, because their momentum spectrum in  $t\bar{t}$  events is comparable to that of the electrons and hence allows a direct comparison.

Comput Softw Big Sci (2022) 6:3 Page 11 of 35 3

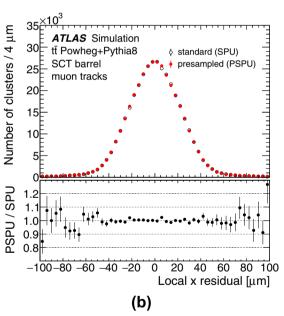


**Fig. 7** Comparison of the standard digitisation (open black circles) and the presampled pile-up (red filled circles), showing **a** the average number of pixel clusters on a track as a function of  $\mu$  and **b** the the local x residuals, for tracks produced by muons in simulated  $t\bar{t}$  events. The distributions are integrated over all clusters associated with muon

tracks in the hard-scatter event. The residual is defined as the measured hit position minus the expected hit position from the track extrapolation (not including the cluster in question). The bottom panels show the ratios of the two distributions



**Fig. 8** Comparison of the standard digitisation (open black circles) and the presampled pile-up (red filled circles), showing **a** the average number of SCT clusters on a track as a function of  $\mu$  and **b** the the local x residuals, for tracks produced by muons in simulated  $t\bar{t}$  events. The distributions are integrated over all clusters associated with muon



tracks in the hard-scatter event. The residual is defined as the measured hit position minus the expected hit position from the track extrapolation (not including the cluster in question). The bottom panels show the ratios of the two distributions



3 Page 12 of 35 Comput Softw Big Sci (2022) 6:3

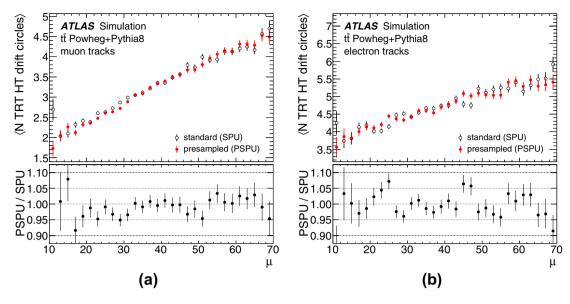


Fig. 9 Distributions of the average number of TRT high-threshold drift circles, after the corrections described in the text, for tracks produced by **a** muons and **b** electrons in simulated  $t\bar{t}$  events as a function of  $\mu$ .

The standard digitisation (open black circles) is compared with the presampled pile-up (red filled circles). The bottom panels show the ratios of the two distributions

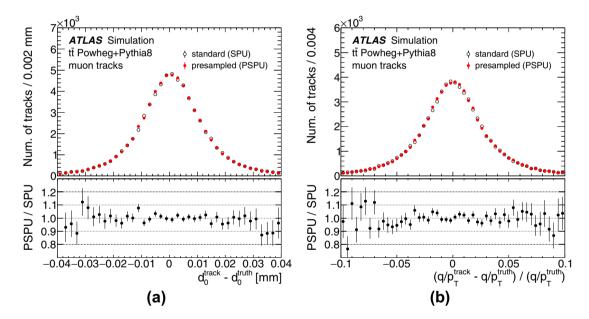


Fig. 10 A comparison of the reconstructed muon track parameter resolution for  $\mathbf{a}$   $d_0$  and  $\mathbf{b}$   $q/p_T$  between the standard digitisation (open black circles) and the presampled pile-up (red filled circles) methods, for simulated  $t\bar{t}$  events. The bottom panels show the ratios of the two distributions

#### 6 Calorimeters

## 6.1 Detector readout

The standard and presampled pile-up digitisation algorithms are based on an accurate emulation of the readout of the calorimeter system.

For the LAr calorimeter [27], the deposit of energy in the liquid–argon gaps induces an electric current proportional to the deposited energy. For a uniform energy deposit in the gap, the signal has a triangular shape as a function of time with a length corresponding to the maximum drift time of the ionisation electrons, typically 450 ns in the EM calorimeter. This signal is amplified and shaped by a bipolar CR– $(RC)^2$  filter in the front-end readout boards [28] to reduce the



Comput Softw Big Sci (2022) 6:3 Page 13 of 35 3

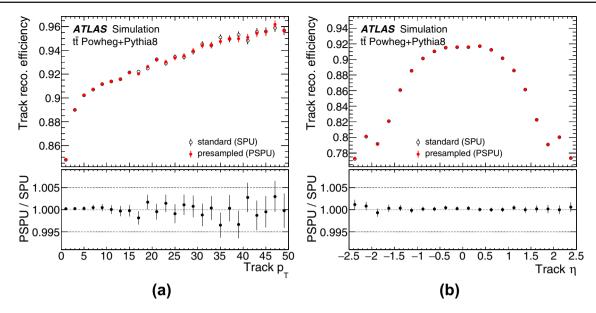


Fig. 11 A comparison of the track reconstruction efficiency between the standard digitisation (open black circles) and the presampled pile-up (red filled circles) methods, for simulated  $t\bar{t}$  events as a function of

**a** transverse momentum and **b** pseudorapidity ( $\eta$ ). All primary charged particles with  $p_T > 500\,\text{MeV}$  and  $|\eta| < 2.5$  are selected. The bottom panels show the ratios of the two distributions

effect of out-of-time pile-up energy deposits from collisions in the next or previous bunch crossings. To accommodate the required dynamic range, three different gains (high, medium, and low) are used. The shaped and amplified signals are sampled at the LHC bunch-crossing frequency of 40 MHz and, for each L1 trigger, are digitised by a 12-bit analog-to-digital converter (ADC). The medium gain for the time sample corresponding to the maximum expected amplitude is digitised first to choose the most suitable gain for a given signal. Four time samples for the selected gain are then digitised and sent to the back-end electronics via optical fibres. For the EMB, EMEC, and FCAL calorimeters, the position of the maximum of the signal is in the third time sample for an energy deposit produced in the same bunch crossing as the triggered event. For the HEC, it is in the second time sample.

For the Tile calorimeter [29], each cell is read out by two photomultiplier channels. The maximum height of the analogue pulse in a channel is proportional to the amount of energy deposited by the incident particle in the corresponding cell. The shaped signals are sampled and digitised by 10-bit ADCs at a frequency of 40 MHz. The sampled data are temporarily stored in a pipeline memory until an L1 trigger signal is received. Seven time samples, centred around the pulse peak, are obtained. A gain selector is used to determine which gain information is sent to the back-end electronics for event processing. By default the high-gain signal is used, unless any of the seven time samples saturates the ADC, at which point the low-gain signal is transmitted.

## 6.2 Overlay procedure

The procedure for the LAr calorimeter is described in detail below; a very similar procedure is used for the Tile calorimeter.

In the presampled RDO sample, the pulse shape (ADC data vs time sample) is stored over the time period for which the calorimeter is read out for each calorimeter cell without any zero suppression. Its computation is based on the standard pile-up simulation, described in more detail in Ref. [30]. It considers the energy deposited in each cell for each bunch crossing over the time window affecting the triggered BC, taking into account the time of each event relative to the trigger time. The resulting pulse shape, expressed in energy versus time, is then converted to ADC counts, applying the energy-to-ADC calibration factor per cell and adding the ADC pedestal. The gain used in the readout electronics for this conversion is selected by emulating the logic applied in the front-end readout electronics. The electronics noise is then added to the presampled RDO, with the proper correlation of the noise between the different samples, with a value that depends on the gain used to digitise the pulse.

In the presampled pile-up step, the pulse shape of the presampled event is converted back into energy and then the energy from the hard-scatter event is added. This is done for each time sample, resulting in a combined pulse shape of the hard-scatter and presampled pile-up events. From this summed pulse shape, the energies in each time sample are then converted back to ADC counts to produce a pulse shape mimicking the output of the front-end electronics. The read-



3 Page 14 of 35 Comput Softw Big Sci (2022) 6:3

out electronics gain used in this conversion is selected according to the energies of the summed pulse shape. If this gain differs from the ones used in the hard-scatter or presampled samples, the electronics noise is corrected accordingly.

This pulse shape is then processed following exactly the same algorithm as used in the standard pile-up digitisation, applying the optimal filtering coefficients [31] to estimate the energy per cell [30]. For cells with high enough energy, the time and pulse quality factors are also computed.

Since all cells are stored in the presampled RDO sample without any suppression, and the energy response is perfectly linear in the digitisation, the presampled pile-up does not rely on any approximations except for the integer rounding that is applied when storing ADC counts in the presampled sample. In practice, the impact of ADC integer rounding was found to be almost negligible. This rounding effect only applies to the LAr case; Tile ADC data are actually stored as floats in the presampled RDO sample.

## 6.3 Validation results

Figure 12a shows a comparison of the total energy deposited in the EMB calorimeter by dijet events for the presampled pile-up and standard digitisation methods. This distribution is sensitive to electronics and pile-up noise and shows that the simulation of the noise in the two methods is similar. Figure 12b shows the distribution of a calorimeter isolation quantity  $E_{\rm T}^{\rm cone20}/E_{\rm T}$  for simulated single-electron events. This variable is calculated from topological clusters [32] of energy deposits by summing the transverse energies of such clusters within a cone of size  $\Delta R = 0.2$  around (but not including) the candidate electron cluster. It is sensitive to pileup energy deposits close to the signal electrons and is again similar for the two methods. Figure 12c shows the invariant mass distribution of electron-positron pairs from simulated  $Z \rightarrow e^+e^-$  events. This comparison shows that the energy scale and resolution of electrons from signal events agree for the two methods.

Figure 13 shows the jet response in  $t\bar{t}$  MC events. The jet  $p_{\rm T}$  is calibrated using a multi-stage procedure [33] that accounts for several effects, including pile-up. The pile-up correction is performed at an early stage of the calibration procedure and removes excess energy due to both in-time and out-of-time pile-up. It is therefore sensitive to the details of the pile-up emulation. The shape of the distribution (which is sensitive to noise modelling) and the average response versus  $\eta$  over the full calorimeter acceptance are in good agreement for the two methods. Also shown in Fig. 13 is the distribution of missing transverse momentum  $E_{\rm T}^{\rm miss}$  for events in the same  $t\bar{t}$  sample. The *soft term* component, as reconstructed in the calorimeter, which is particularly sensitive to pile-up [34] is shown as well. Again, good agreement is observed for the two methods.



The MS consists of four subdetectors: two providing high-precision tracking measurements and two primarily providing trigger information. The technologies used in these are different and, as with the ID, they require specific digitisation treatments for the presampled pile-up. The main difference in the case of the MS compared to the ID is that the occupancy is much lower. This means that, while there is the potential for loss of information in the presampled pile-up method if two sub-threshold hits occur in the same detector channel, the probability of this occurring is much lower and the resulting effect is found to be negligible.

# 7.1 Detector readout and overlay procedure

## 7.1.1 Monitored drift tubes (MDT)

The MDTs consist of layers of drift tubes which are designed to have a position resolution below  $80~\mu m$  per tube. If a particle traverses a drift tube, ionisation is created and electrons drift to the anode wire. If the charge at that wire exceeds a set threshold, the charge and the time are recorded, and both are converted to digital information. For the presampled pile-up, the digital signals from the hard-scatter and pile-up events are combined as follows. If a signal in a given tube is only present in either the hard-scatter event or the pile-up event, that signal is copied to the output RDO. If a signal is present in both, then the two signal amplitudes are added, and the timing is taken to be the earlier of the two events.

# 7.1.2 Cathode strip chambers (CSC)

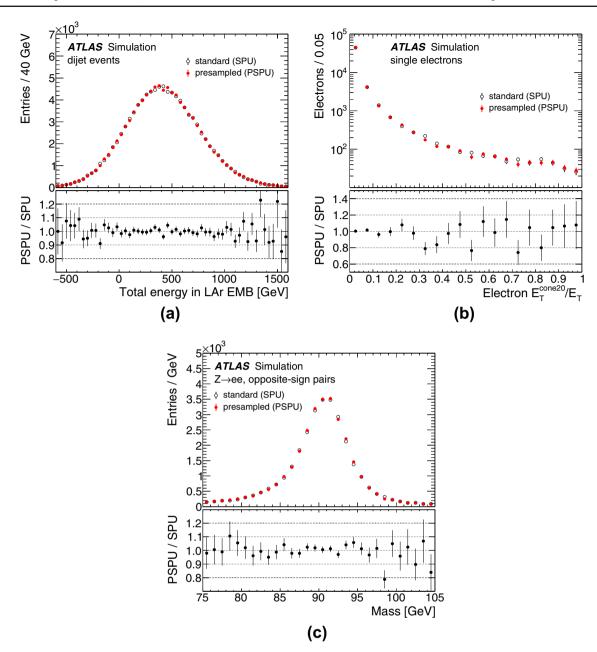
The CSCs are multiwire proportional chambers with cathode strip readout which, by charge interpolation, provide a spatial resolution of  $60\,\mu m$  in the radial, or bending, plane and 5 mm in the transverse, or  $\phi$ , plane. By combining the hits of a track crossing all four chambers, a time resolution of 4 ns is achieved, sufficient to identify the bunch crossing. For each wire, the charge information per strip is recorded, and then digitised and stored in four time slices, each of 50 ns. For the presampled pile-up, the charge deposited in each strip in the four time slices is read out for the hard-scatter event and the pile-up event; the two signals are then added separately per time slice and strip, taking care to ensure that the pedestal is subtracted appropriately. The combined RDO resulting from these summed signals is then written out.

#### 7.1.3 Resistive plate chambers (RPC)

The RPC system covers the region  $|\eta| < 1.05$  and is composed of gaseous parallel-plate detectors. The position resolution is about 1 cm in both the transverse and longitudinal



Comput Softw Big Sci (2022) 6:3 Page 15 of 35 3



**Fig. 12** A comparison between the standard digitisation (open black circles) and the presampled pile-up (red filled circles) for  $\bf a$  the total deposited energy distribution in the electromagnetic barrel of the liquid-argon calorimeter in simulated dijet events,  $\bf b$  the electron isolation  $E_{\bf c}^{\rm cone20}/E_{\bf T}$  distribution for single electrons, and  $\bf c$  the opposite-sign

electron-pair invariant mass distribution from simulated  $Z \to e^+e^-$  events. The normalisation of the figures is arbitrary as it is simply proportional to the number of events in the MC sample. The bottom panels show the ratios of the two distributions

directions, and the time resolution is 1.5 ns. If a muon crosses the 2 mm space between the two parallel resistive plates, an avalanche forms along the ionising track towards the anode. The signal is then read out via metallic strips mounted on the outer faces of the resistive plates if it exceeds a given threshold; the time of the signal is also recorded. For the presampled pile-up, the only relevant information is the time and the overlay is performed by taking, for each channel, the

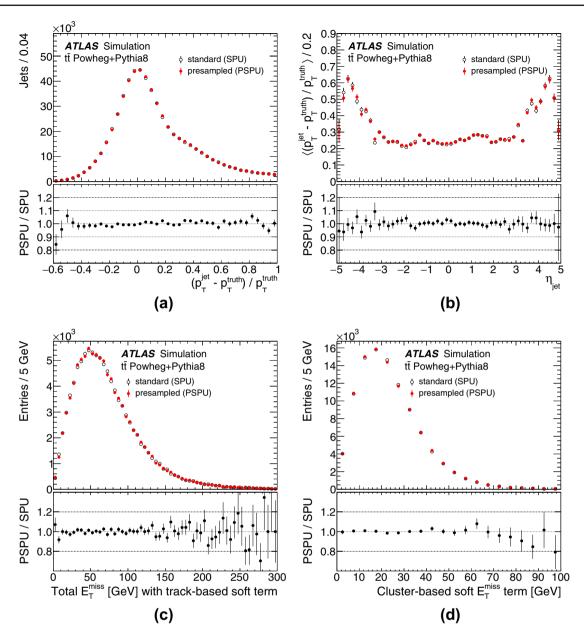
earliest signal time between the hard-scatter and the pile-up events.

## 7.1.4 Thin-gap chambers (TGC)

The TGCs cover the region 1.05  $< |\eta| < 2.4$ . They have a typical position resolution of 3–7 mm in the bending direction and 2–6 mm in the transverse direction, and a time resolution



3 Page 16 of 35 Comput Softw Big Sci (2022) 6:3



**Fig. 13** A comparison between the standard digitisation (open black circles) and the presampled pile-up (red filled circles) in simulated  $t\bar{t}$  events for **a** the jet  $p_{\rm T}$  response, **b** the mean jet  $p_{\rm T}$  response as a function of jet pseudorapidity  $\eta_{\rm jet}$ , **c** the total  $E_{\rm T}^{\rm miss}$  distribution, and **d** the

component of the  $E_{\rm T}^{\rm miss}$  from energy clusters in the calorimeter that are not associated with calibrated physics objects, known as the *soft term*. The bottom panels show the ratios of the two distributions

of 4 ns. The radial coordinate is measured by reading which TGC wire-group is hit; the azimuthal coordinate is measured by reading which radial strip is hit. For each wire, the time at which a signal is above threshold is recorded and digitised and then written in the digit format. As in the RPCs, the hard-scatter and pile-up events are combined by taking the earliest arrival time of any hard-scatter or pile-up signal for a given wire.

## 7.2 Validation results

The presampled pile-up procedure is validated using muons from simulated  $Z \to \mu^+\mu^-$  events and comparing their characteristics with those after the standard pile-up digitisation procedure. Figure 14 shows the reconstruction efficiency of muons as a function of  $p_T$  and  $\eta$  for the two methods. They agree to better than 0.1% for nearly the entire  $p_T$  and  $\eta$  range. Figure 14c shows the invariant mass of the two muons for the same event sample. Also here, good agreement is observed between the two methods.



Comput Softw Big Sci (2022) 6:3 Page 17 of 35 3

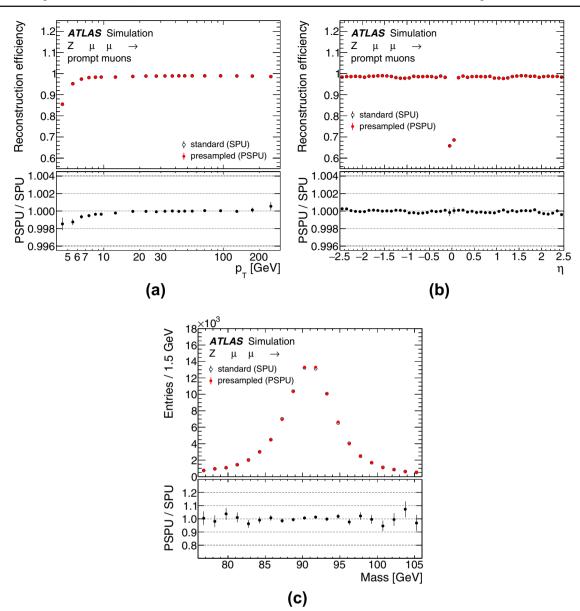


Fig. 14 The muon reconstruction efficiency versus  $\mathbf{a}$   $p_{\rm T}$  and  $\mathbf{b}$   $\eta$  and  $\mathbf{c}$  the dimuon invariant mass in simulated  $Z \to \mu^+ \mu^-$  events. The open black circles correspond to the standard digitisation and the red filled circles to presampled pile-up. The bottom panels show the ratios of the corresponding distributions

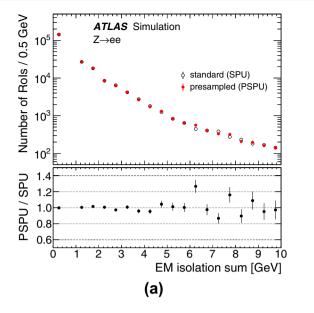
# 8 Trigger

The L1 trigger receives inputs from the L1 calorimeter (L1Calo) and L1 muon triggers. The L1Calo decision is formed using reduced granularity inputs from the LAr and Tile calorimeters. The L1 muon trigger receives signals from the RPCs in the barrel and from the TGCs in the endcaps as is described in Sect. 7. After the L1 trigger decision, the HLT has access to the data from the full detector to perform a refined analysis. The trigger decisions and all reconstructed objects are stored in a dedicated record of the accepted event.

The L1 hardware trigger is simulated using dedicated algorithms that strive to perform a bit-wise correct emulation of the trigger decision including any trigger objects that the hardware produces. The HLT runs on the output of the L1 trigger using the same simulation software as used for data. The following sections discuss the L1 calorimeter trigger and the overall HLT performance. No dedicated changes were required to the muon trigger simulation beyond what is discussed for the general simulation in Sect. 7. While the HLT software itself remains unchanged between the two methods, it depends on the inputs from the various subdetectors that do differ and hence serves as an additional validation.

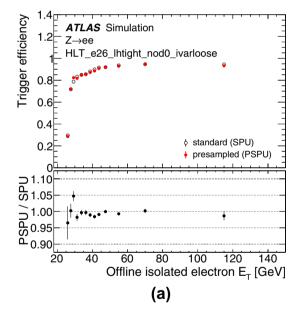


3 Page 18 of 35 Comput Softw Big Sci (2022) 6:3



**Fig. 15** Distributions of  $E_{\rm T}$  in the isolation regions of the L1Calo  $e/\gamma$  trigger: **a** in the electromagnetic calorimeter and **b** in the hadronic calorimeter. The standard digitisation (black open circles) is compared with the presampled pile-up (red filled circles). The distributions are for

regions around electrons in  $Z \to e^+e^-$  events, which are dominated by electronic noise and pile-up. The bottom panels show the ratios of the two distributions



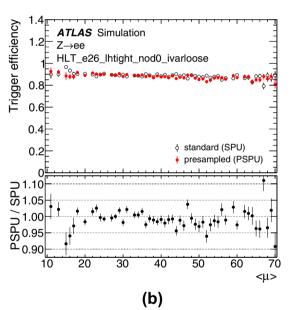


Fig. 16 The combined L1 and HLT trigger efficiency of the 28 GeV electron trigger from simulated  $Z \rightarrow e^+e^-$  events (red filled circles) as a function of **a**  $E_{\rm T}$  and **b** pile-up for the standard digitisation (open

black circles) and presampled pile-up (red filled circles). The bottom panels show the ratios of the two distributions

## 8.1 L1 calorimeter trigger simulation

The inputs to the L1Calo trigger processors are trigger towers [17]. These are formed in the on-detector electronics by summation of the analogue voltage pulses from calorimeter cells in groups of  $\Delta\eta \times \Delta\phi \sim 0.1 \times \pi/32$ , separately in the electromagnetic and hadronic calorimeter systems. These signals

are then transmitted over 70 m long, individually shielded, twisted-pair cables to the trigger electronics, where they are digitised with a least-count equivalent to 250 MeV transverse energy and a sampling frequency of 40 MHz. A custom digital processor, consisting of filters, comparators, and look-up tables, analyzes the shape of the digitised pulse to identify which bunch crossing it came from. It also corrects



Comput Softw Big Sci (2022) 6:3 Page 19 of 35 3

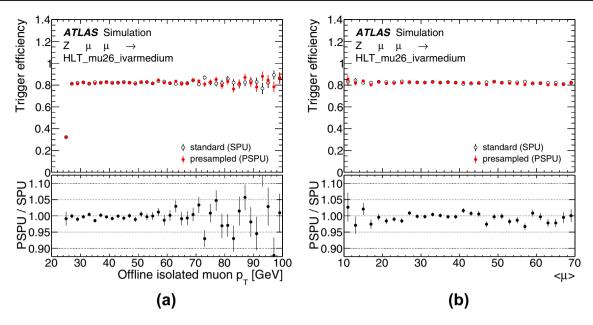
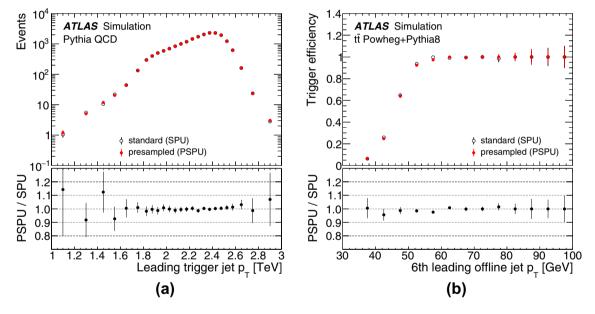


Fig. 17 The combined L1 and HLT trigger efficiency of the 26 GeV muon trigger from simulated  $Z \to \mu^+\mu^-$  events as a function of **a**  $p_{\rm T}$  and **b** pile-up for the standard digitisation (open black circles) and presampled pile-up (red filled circles). The bottom panels show the ratios of the two distributions



**Fig. 18** a The  $p_T$  of the leading jet in all events with a triggered jet and b the trigger efficiency of the six-jet trigger requiring a jet  $p_T > 45$  GeV as a function of the  $p_T$  of the sixth leading jet for the standard digiti-

sation (open black circles) and presampled pile-up (red filled circles). The bottom panels show the ratios of the two distributions

for shifts in the baseline caused by the interplay of in-time and out-of-time pile-up due to the LHC bunch structure, subtracts pedestals, and applies noise cuts. Finally, it provides calibrated transverse energy  $E_{\rm T}$  values for use in the trigger algorithms on scales of 500 MeV/count for the electron, photon, and  $\tau$ -lepton triggers and 1 GeV/count for all other triggers.

In the simulation, the analogue signals received from the calorimeters are represented by objects containing a vector of floating-point values, corresponding to the amplitudes of the pulses sampled at 25 ns intervals. These are then quantised, with the addition of noise from the digitisation system, and passed through a precise simulation of the signal processing performed by the trigger electronics. The calorimeter objects



3 Page 20 of 35 Comput Softw Big Sci (2022) 6:3

are formed from calorimeter hits, using a model of the pulse shaping and the noise from the readout and summation chain.

For presampled pile-up, the *analogue* calorimeter objects are merged before the trigger digitisation and processing are performed. This then allows the unmodified trigger simulation to be performed on the merged data, and it avoids any possible bias due to merging data that have been quantised on a relatively coarse scale. The merging is performed by an additional algorithm, which is run during the pile-up merging prior to the trigger simulation to create a set of merged calorimeter towers. The merging itself uses the calorimeter object identifiers to match corresponding towers in the hard-scatter and pile-up event collections, and the amplitudes of the signals of the same towers in both events are summed. A new collection of objects containing the summed amplitudes is then created and written to the output stream.

Figure 15 shows the L1Calo  $E_{\rm T}$  distributions in isolation regions around electrons in  $Z \to e^+e^-$  events, which are sensitive to the pile-up  $E_{\rm T}$  deposits close to the electrons. Good agreement is seen between the standard and presampled pile-up simulation chains.

## 8.2 HLT simulation and performance

After being accepted by the L1 trigger, the events are processed by the HLT using finer granularity calorimeter information, precision measurements from the muon system, and tracking information from the inner detector. As needed, the HLT reconstruction can be executed either for the full event or within smaller, isolated regions of interest (RoIs) identified by the L1 trigger. To reduce the processing time, most HLT triggers use a two-stage approach with a fast (trigger-specific) first-pass reconstruction to reject the majority of events and a slower, higher precision (offline-like) reconstruction for the remaining events.

The reconstruction of electron (muon) candidates requires the matching of a calorimeter cluster (muon spectrometer track) to a track in the inner detector and is therefore sensitive to changes in the inner detector, calorimeter, and muon spectrometer reconstruction. Figure 16 shows the trigger efficiency of the primary 28 GeV electron trigger measured with simulated  $Z \rightarrow e^+e^-$  events for the standard and presampled pile-up simulation chains. Similarly, Fig. 17 shows the trigger efficiency of the primary 26 GeV muon trigger measured with simulated  $Z \rightarrow \mu^+\mu^-$  events. No significant differences are observed in the trigger efficiency between the presampled and standard pile-up simulation chains.

Jet and  $E_{\rm T}^{\rm miss}$  triggers are mainly based on the calorimeter reconstruction and are especially sensitive to changes in the simulation of low- $p_{\rm T}$  jets. Figure 18 shows the  $p_{\rm T}$  distribution of the leading jet and the trigger efficiency as a function of the sixth leading jet  $p_{\rm T}$  for a multi-jet trigger requiring six jets with a  $p_{\rm T}$  larger than 45 GeV. Good agree-

ment between the standard and presampled pile-up simulation chains is observed in both cases.

All other triggers relevant to the ATLAS physics programme were also studied and no notable differences between the two methods were observed.

## 9 Conclusions

A new method for reproducing the impact of pile-up interactions on the ATLAS detector performance is presented, based on overlaying presampled pile-up events on the hard-scatter event of interest during the digitisation. The method is validated separately for each ATLAS detector system and the trigger. In all cases, it is possible to achieve good agreement with the standard pile-up simulation chain which has been used up to now. For a large variety of quantities, detailed comparisons are made between the two methods, and all the differences are found to be small, so that the impact on physics analyses is considered negligible.

The presampled pile-up method is shown to use significantly less computing resources than the standard method used so far within ATLAS. For the Run 2 pile-up distribution and software, the CPU resources required for the entire MC simulation chain are reduced by around 20%.

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Comput Softw Big Sci (2022) 6:3 Page 21 of 35 3

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3 Page 22 of 35 Comput Softw Big Sci (2022) 6:3

#### ATLAS Collaboration

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Bechtle 4, H. P. Beck 20, 9, P. Bechtle 24, P. H. P. Beck 20, 9, P. Bechtle 24, P. Beck 20, 9, P. Be K, Becker<sup>176</sup>, C, Becot<sup>46</sup>, A, J, Beddall<sup>12a</sup>, V, A, Bednyakoy<sup>79</sup>, C, P, Bee<sup>154</sup>, T, A, Beermann<sup>180</sup>, M. Begalli<sup>80b</sup>, M. Begel<sup>29</sup>, A. Behera<sup>154</sup>, J. K. Behr<sup>46</sup>, J. F. Beirer<sup>53,36</sup>, F. Beisiegel<sup>24</sup>, M. Belfkir<sup>5</sup>, G. Bella<sup>160</sup>, L. Bellagamba<sup>23b</sup>, A. Bellerive<sup>34</sup>, P. Bellos<sup>21</sup>, K. Beloborodov<sup>121b,121a</sup>, K. Belotskiy<sup>111</sup>, N. L. Belyaev<sup>111</sup>, D. Benchekroun<sup>35a,35b,35c,35d,35e,35f</sup>, N. Benekos<sup>10</sup>, Y. Benhammou<sup>160</sup>, D. P. Benjamin<sup>6</sup>, M. Benoit<sup>29</sup>, J. R. Bensinger<sup>26</sup>, S. Bentvelsen<sup>119</sup>, L. Beresford<sup>133</sup>, M. Beretta<sup>51</sup>, E. Bergeaas Kuutmann<sup>170</sup>, N. Berger<sup>5</sup>, B. Bergmann<sup>140</sup>, L. J. Bergsten<sup>26</sup>, J. Beringer<sup>18</sup>, S. Berlendis<sup>7</sup>, G. Bernardi <sup>134</sup>, C. Bernius <sup>152</sup>, F. U. Bernlochner <sup>24</sup>, T. Berry <sup>93</sup>, P. Berta <sup>46</sup>, A. Berthold <sup>48</sup>, I. A. Bertram <sup>89</sup>, O. Bessidskaia Bylund<sup>180</sup>, S. Bethke<sup>114</sup>, A. Betti<sup>42</sup>, A. J. Bevan<sup>92</sup>, S. Bhatta<sup>154</sup>, D. S. Bhattacharya<sup>175</sup>, P. Bhattarai<sup>26</sup>, V. S. Bhopatkar<sup>6</sup>, R. Bi<sup>137</sup>, R. M. Bianchi<sup>137</sup>, O. Biebel<sup>113</sup>, R. Bielski<sup>36</sup>, K. Bierwagen<sup>99</sup>, N. V. Biesuz<sup>71a,71b</sup>, M. Biglietti<sup>74a</sup>, T. R. V. Billoud<sup>140</sup>, M. Bindi<sup>53</sup>, A. Bingul<sup>12d</sup>, C. Bini<sup>72a,72b</sup>, S. Biondi<sup>23b,23a</sup>, C. J. Birch-sykes<sup>100</sup>, M. Birman<sup>178</sup>, T. Bisanz<sup>36</sup>, J. P. Biswal<sup>3</sup>, D. Biswas 179, j, A. Bitadze<sup>100</sup>, C. Bittrich<sup>48</sup>, K. Bjørke<sup>132</sup>, T. Blazek<sup>28a</sup>, I. Bloch<sup>46</sup>, C. Blocker<sup>26</sup>, A. Blue<sup>57</sup>, U. Blumenschein<sup>92</sup>, G. J. Bobbink<sup>119</sup>, V. S. Bobrovnikov<sup>121b,121a</sup>, D. Bogavac<sup>14</sup>, A. G. Bogdanchikov<sup>121b,121a</sup> C. Bohm<sup>45a</sup>, V. Boisvert<sup>93</sup>, P. Bokan<sup>170,53</sup>, T. Bold<sup>83a</sup>, M. Bomben<sup>134</sup>, M. Bona<sup>92</sup>, J. S. Bonilla<sup>130</sup> M. Boonekamp<sup>143</sup>, C. D. Booth<sup>93</sup>, A. G. Borbély<sup>57</sup>, H. M. Borecka-Bielska<sup>90</sup>, L. S. Borgna<sup>94</sup>, G. Borissov<sup>89</sup>, D. Bortoletto<sup>133</sup>, D. Boscherini<sup>23b</sup>, M. Bosman<sup>14</sup>, J. D. Bossio Sola<sup>103</sup>, K. Bouaouda<sup>35a</sup>, J. Boudreau<sup>137</sup>, E. V. Bouhova-Thacker<sup>89</sup>, D. Boumediene<sup>38</sup>, R. Bouquet<sup>134</sup>, A. Boveia<sup>126</sup>, J. Boyd<sup>36</sup>, D. Boye<sup>29</sup> I. R. Boyko<sup>79</sup>, A. J. Bozson<sup>93</sup>, J. Bracinik<sup>21</sup>, N. Brahimi<sup>60d,60c</sup>, G. Brandt<sup>180</sup>, O. Brandt<sup>32</sup>, F. Braren<sup>46</sup>,



Comput Softw Big Sci (2022) 6:3 Page 23 of 35 3

B. Brau<sup>102</sup>, J. E. Brau<sup>130</sup>, W. D. Breaden Madden<sup>57</sup>, K. Brendlinger<sup>46</sup>, R. Brener<sup>159</sup>, L. Brenner<sup>36</sup>, R. Brenner<sup>170</sup>, S. Bressler<sup>178</sup>, B. Brickwedde<sup>99</sup>, D. L. Briglin<sup>21</sup>, D. Britton<sup>57</sup>, D. Britzger<sup>114</sup>, I. Brock<sup>24</sup>, R. Brock<sup>106</sup>, G. Brooijmans<sup>39</sup>, W. K. Brooks<sup>145d</sup>, E. Brost<sup>29</sup>, P. A. Bruckman de Renstrom<sup>84</sup>, B. Brüers<sup>46</sup>, D. Bruncko<sup>28b</sup>, A. Bruni<sup>23b</sup>, G. Bruni<sup>23b</sup>, M. Bruschi<sup>23b</sup>, N. Bruscino<sup>72a,72b</sup>, L. Bryngemark<sup>152</sup>, T. Buanes<sup>17</sup>, Q. Buat<sup>154</sup>, P. Buchholz<sup>150</sup>, A. G. Buckley<sup>57</sup>, I. A. Budagov<sup>79</sup>, M. K. Bugge<sup>132</sup>, O. Bulekov<sup>111</sup>, B. A. Bullard<sup>59</sup>, T. J. Burch<sup>120</sup>, S. Burdin<sup>90</sup>, C. D. Burgard<sup>46</sup>, A. M. Burger<sup>128</sup>, B. Burghgrave<sup>8</sup>, J. T. P. Burr<sup>46</sup>, C. D. Burton<sup>11</sup>, J. C. Burzynski<sup>102</sup>, V. Büscher<sup>99</sup>, E. Buschmann<sup>53</sup>, P. J. Bussey<sup>57</sup>, J. M. Butler<sup>25</sup>, C. M. Buttar<sup>57</sup>, J. M. Butterworth<sup>94</sup>, W. Buttinger<sup>142</sup>, C. J. Buxo Vazquez<sup>106</sup>, A. R. Buzykaev<sup>121b,121a</sup>, G. Cabras<sup>23b,23a</sup>, S. Cabrera Urbán<sup>172</sup>, D. Caforio<sup>56</sup>, H. Cai<sup>137</sup>, V. M. M. Cairo<sup>152</sup>, O. Cakir<sup>4a</sup>, N. Calace<sup>36</sup>, P. Calafiura<sup>18</sup>, G. Calderini<sup>134</sup>, P. Calfayan<sup>65</sup>, G. Callea<sup>57</sup>, L. P. Caloba<sup>80b</sup> A. Caltabiano<sup>73a,73b</sup>, S. Calvente Lopez<sup>98</sup>, D. Calvet<sup>38</sup>, S. Calvet<sup>38</sup>, T. P. Calvet<sup>101</sup>, M. Calvetti<sup>71a,71b</sup>, R. Camacho Toro<sup>134</sup>, S. Camarda<sup>36</sup>, D. Camarero Munoz<sup>98</sup>, P. Camarri<sup>73a,73b</sup>, M. T. Camerlingo<sup>74a,74b</sup>, D. Cameron<sup>132</sup>, C. Camincher<sup>36</sup>, M. Campanelli<sup>94</sup>, A. Camplani<sup>40</sup>, V. Canale<sup>69a,69b</sup>, A. Canesse<sup>103</sup>, M. Cano Bret<sup>77</sup>, J. Cantero<sup>128</sup>, Y. Cao<sup>171</sup>, M. Capua<sup>41b,41a</sup>, R. Cardarelli<sup>73a</sup>, F. Cardillo<sup>172</sup>, G. Carducci<sup>41b,41a</sup>, T. Carli<sup>36</sup>, G. Carlino<sup>69a</sup>, B. T. Carlson<sup>137</sup>, E. M. Carlson<sup>174,166a</sup>, L. Carminati<sup>68a,68b</sup>, M. Carnesale<sup>72a,72b</sup>, R. M. D. Carney<sup>152</sup>, S. Caron<sup>118</sup>, E. Carquin<sup>145d</sup>, S. Carrá<sup>46</sup>, G. Carratta<sup>23b,23a</sup>, J. W. S. Carter<sup>165</sup>, T. M. Carter<sup>50</sup>, M. P. Casado<sup>14</sup>, D. A. F. Casha<sup>165</sup>, E. G. Castiglia<sup>181</sup>, F. L. Castillo<sup>172</sup>, L. Castillo Garcia<sup>14</sup>, V. Castillo Gimenez<sup>172</sup>, N. F. Castro<sup>138a,138e</sup>, A. Catinaccio<sup>36</sup>, J. R. Catmore<sup>132</sup>, A. Cattai<sup>36</sup>, V. Cavaliere<sup>29</sup>, V. Cavasinni<sup>71a,71b</sup>, E. Celebi<sup>12b</sup>, F. Celli<sup>133</sup>, K. Cerny<sup>129</sup>, A. S. Cerqueira<sup>80a</sup>, A. Cerri<sup>155</sup>, L. Cerrito<sup>73a,73b</sup>, F. Cerutti<sup>18</sup>, A. Cervelli<sup>23b,23a</sup>, S. A. Cetin<sup>12b</sup>, Z. Chadi<sup>35a</sup>, D. Chakraborty<sup>120</sup>, M. Chala<sup>138f</sup>, J. Chan<sup>179</sup>, W. S. Chan<sup>119</sup>, W. Y. Chan<sup>90</sup>, J. D. Chapman<sup>32</sup>, B. Chargeishvili<sup>158</sup>, D. G. Charlton<sup>21</sup>, T. P. Charman<sup>92</sup>, M. Chatterjee<sup>20</sup>, C. C. Chau<sup>34</sup>, S. Chekanov<sup>6</sup>, S. V. Chekulaev<sup>166a</sup>, G. A. Chelkov<sup>79,af</sup>, B. Chen<sup>78</sup>, C. Chen<sup>60a</sup>, C. H. Chen<sup>78</sup>, H. Chen<sup>15c</sup>, H. Chen<sup>29</sup>, J. Chen<sup>60a</sup>, J. Chen<sup>39</sup>, J. Chen<sup>39</sup>, S. Chen<sup>135</sup>, S. J. Chen<sup>15c</sup>, X. Chen<sup>15b</sup>, Y. Chen<sup>60a</sup>, Y. Chen<sup>60a</sup>, Y. Chen<sup>60a</sup>, C. L. Cheng<sup>179</sup>, H. C. Cheng<sup>62a</sup>, H. J. Cheng<sup>15a</sup>, A. Cheplakov<sup>79</sup>, E. Cheremushkina<sup>122</sup>, R. Cherkaoui El Moursli<sup>35f</sup>, E. Cheu<sup>7</sup>, K. Cheung<sup>63</sup>, L. Chevalier<sup>143</sup>, V. Chiarella<sup>51</sup>, G. Chiarelli<sup>71a</sup>, G. Chiodini<sup>67a</sup>, A. S. Chisholm<sup>21</sup>, A. Chitan<sup>27b</sup>, I. Chiu<sup>162</sup>, Y. H. Chiu<sup>174</sup>, M. V. Chizhov<sup>79</sup>, K. Choi<sup>11</sup>, A. R. Chomont<sup>72a,72b</sup>, Y. Chou<sup>102</sup>, Y. S. Chow<sup>119</sup> L. D. Christopher<sup>33f</sup>, M. C. Chu<sup>62a</sup>, X. Chu<sup>15a,15d</sup>, J. Chudoba<sup>139</sup>, J. J. Chwastowski<sup>84</sup>, D. Cieri<sup>114</sup>, K. M. Ciesla<sup>84</sup>, V. Cindro<sup>91</sup>, I. A. Cioară<sup>27b</sup>, A. Ciocio<sup>18</sup>, F. Cirotto<sup>69a,69b</sup>, Z. H. Citron<sup>178,k</sup>, M. Citterio<sup>68a</sup>, D. A. Ciubotaru<sup>27b</sup>, B. M. Ciungu<sup>165</sup>, A. Clark<sup>54</sup>, P. J. Clark<sup>50</sup>, S. E. Clawson<sup>100</sup>, C. Clement<sup>45a,45b</sup>, L. Clissa<sup>23b,23a</sup>, Y. Coadou<sup>101</sup>, M. Cobal<sup>66a,66c</sup>, A. Coccaro<sup>55b</sup>, J. Cochran<sup>78</sup>, R. Coelho Lopes De Sa<sup>102</sup>, S. Coelli<sup>68a</sup>, H. Cohen<sup>160</sup>, A. E. C. Coimbra<sup>36</sup>, B. Cole<sup>39</sup>, J. Collot<sup>58</sup>, P. Conde Muiño<sup>138a,138h</sup>, S. H. Connell<sup>33c</sup>, I. A. Connelly<sup>57</sup>, F. Conventi<sup>69a,al</sup>, A. M. Cooper-Sarkar<sup>133</sup>, F. Cormier<sup>173</sup>, L. D. Corpe<sup>94</sup>, M. Corradi<sup>72a,72b</sup>, E. E. Corrigan<sup>96</sup>, F. Corriveau<sup>103,aa</sup>, M. J. Costa<sup>172</sup>, F. Costanza<sup>5</sup>, D. Costanzo<sup>148</sup>, G. Cowan<sup>93</sup>, J. W. Cowley<sup>32</sup>, J. Crane<sup>100</sup>, K. Cranmer<sup>124</sup>, R. A. Creager<sup>135</sup>, S. Crépé-Renaudin<sup>58</sup>, F. Crescioli<sup>134</sup>, M. Cristinziani<sup>150</sup>, M. Cristoforetti<sup>75a,75b</sup>, V. Croft<sup>168</sup>, G. Crosetti<sup>41b,41a</sup>, A. Cueto<sup>5</sup>, T. Cuhadar Donszelmann<sup>169</sup>, H. Cui<sup>15a,15d</sup>, A. R. Cukierman<sup>152</sup>, W. R. Cunningham<sup>57</sup>, S. Czekierda<sup>84</sup>, P. Czodrowski<sup>36</sup>, M. M. Czurylo<sup>61b</sup>, M. J. Da Cunha Sargedas De Sousa<sup>60b</sup>, J. V. Da Fonseca Pinto<sup>80b</sup>, C. Da Via<sup>100</sup>, W. Dabrowski<sup>83a</sup>, T. Dado<sup>47</sup>, S. Dahbi<sup>33f</sup>, T. Dai<sup>105</sup>, C. Dallapiccola<sup>102</sup>, M. Dam<sup>40</sup>, G. D'amen<sup>29</sup>, V. D'Amico<sup>74a,74b</sup>, J. Damp<sup>99</sup>, J. R. Dandoy<sup>135</sup>, M. F. Daneri<sup>30</sup>, M. Danninger<sup>151</sup>, V. Dao<sup>36</sup>, G. Darbo<sup>55b</sup>, A. Dattagupta<sup>130</sup>, S. D'Auria<sup>68a,68b</sup>, C. David<sup>166b</sup>, T. Davidek<sup>141</sup>, D. R. Davis<sup>49</sup>, I. Dawson<sup>148</sup>, K. De<sup>8</sup>, R. De Asmundis<sup>69a</sup>, M. De Beurs<sup>119</sup>, S. De Castro<sup>23b,23a</sup>, N. De Groot<sup>118</sup>, P. de Jong<sup>119</sup>, H. De la Torre<sup>106</sup>, A. De Maria<sup>15</sup>, D. De Pedis<sup>72a</sup>, A. De Salvo<sup>72a</sup>, U. De Sanctis<sup>73a,73b</sup>, M. De Santis<sup>73a,73b</sup>, A. De Santo<sup>155</sup>, J. B. De Vivie De Regie<sup>58</sup>, D. V. Dedovich<sup>79</sup>. A. M. Deiana<sup>42</sup>, J. Del Peso<sup>98</sup>, Y. Delabat Diaz<sup>46</sup>, F. Deliot<sup>143</sup>, C. M. Delitzsch<sup>7</sup>, M. Della Pietra<sup>69a,69b</sup>, D. Della Volpe<sup>54</sup>, A. Dell'Acqua<sup>36</sup>, L. Dell'Asta<sup>68a,68b</sup>, M. Delmastro<sup>5</sup>, C. Delporte<sup>64</sup>, P. A. Delsart<sup>58</sup>, S. Demers<sup>181</sup>, M. Demichev<sup>79</sup>, G. Demontigny<sup>109</sup>, S. P. Denisov<sup>122</sup>, L. D'Eramo<sup>120</sup>, D. Derendarz<sup>84</sup>, J. E. Derkaoui<sup>35e</sup>, F. Derue<sup>134</sup>, P. Dervan<sup>90</sup>, K. Desch<sup>24</sup>, K. Dette<sup>165</sup>, C. Deutsch<sup>24</sup>, P. O. Deviveiros<sup>36</sup>, F. A. Di Bello<sup>72a,72b</sup>, A. Di Ciaccio<sup>73a,73b</sup>, L. Di Ciaccio<sup>5</sup>, C. Di Donato<sup>69a,69b</sup>, A. Di Girolamo<sup>36</sup>, G. Di Gregorio<sup>71a,71b</sup>, A. Di Luca<sup>75a,75b</sup>, B. Di Micco<sup>74a,74b</sup>, R. Di Nardo<sup>74a,74b</sup>, C. Diaconu<sup>101</sup>, F. A. Dias<sup>119</sup>, T. Dias Do Vale<sup>138a</sup>, M. A. Diaz<sup>145a</sup>, F. G. Diaz Capriles<sup>24</sup>, J. Dickinson<sup>18</sup>, M. Didenko<sup>164</sup>, E. B. Diehl<sup>105</sup>, J. Dietrich<sup>19</sup>, S. Díez Cornell<sup>46</sup>, C. Diez Pardos<sup>150</sup>, A. Dimitrievska<sup>18</sup>, W. Ding<sup>15b</sup>, J. Dingfelder<sup>24</sup>, S. J. Dittmeier<sup>61b</sup>, F. Dittus<sup>36</sup>, F. Djama<sup>101</sup>, T. Djobava<sup>158b</sup>, J. I. Djuvsland<sup>17</sup>, M. A. B. Do Vale<sup>146</sup>, 3 Page 24 of 35 Comput Softw Big Sci (2022) 6:3

M. Dobre<sup>27b</sup>, D. Dodsworth<sup>26</sup>, C. Doglioni<sup>96</sup>, J. Dolejsi<sup>141</sup>, Z. Dolezal<sup>141</sup>, M. Donadelli<sup>80c</sup>, B. Dong<sup>60c</sup> J. Donini<sup>38</sup>, A. D'onofrio<sup>15c</sup>, M. D'Onofrio<sup>90</sup>, J. Dopke<sup>142</sup>, A. Doria<sup>69a</sup>, M. T. Dova<sup>88</sup>, A. T. Doyle<sup>57</sup>, E. Drechsler<sup>151</sup>, E. Dreyer<sup>151</sup>, T. Dreyer<sup>53</sup>, A. S. Drobac<sup>168</sup>, D. Du<sup>60b</sup>, T. A. du Pree<sup>119</sup>, Y. Duan<sup>60d</sup>, F. Dubinin<sup>110</sup>, M. Dubovsky<sup>28a</sup>, A. Dubreuil<sup>54</sup>, E. Duchovni<sup>178</sup>, G. Duckeck<sup>113</sup>, O. A. Ducu<sup>36,27b</sup>, D. Duda<sup>114</sup>, A. Dudarev<sup>36</sup>, A. C. Dudder<sup>99</sup>, M. D'uffizi<sup>100</sup>, L. Duflot<sup>64</sup>, M. Dührssen<sup>36</sup>, C. Dülsen<sup>180</sup>, M. Dumancic<sup>178</sup>, A. E. Dumitriu<sup>27b</sup>, M. Dunford<sup>61a</sup>, S. Dungs<sup>47</sup>, A. Duperrin<sup>101</sup>, H. Duran Yildiz<sup>4a</sup>, M. Düren<sup>56</sup>, A. Durglishvili<sup>158</sup>, B. Dutta<sup>46</sup>, D. Duvnjak<sup>1</sup>, G. I. Dyckes<sup>135</sup>, M. Dyndal<sup>36</sup>, S. Dysch<sup>100</sup>, B. S. Dziedzic<sup>84</sup>, B. Eckerova<sup>28a</sup>, M. G. Eggleston<sup>49</sup>, L. F. Ehrke<sup>54</sup>, T. Eifert<sup>8</sup>, G. Eigen<sup>17</sup>, K. Einsweiler<sup>18</sup>, T. Ekelof<sup>170</sup>, H. El Jarrari<sup>35f</sup>, A. El Moussaouy<sup>35a</sup>, V. Ellajosyula<sup>170</sup>, M. Ellert<sup>170</sup>, F. Ellinghaus<sup>180</sup>, N. Ellis<sup>36</sup>, J. Elmsheuser<sup>29</sup>, M. Elsing<sup>36</sup>, A. A. Elliot<sup>92</sup>, D. Emeliyanov<sup>142</sup>, A. Emerman<sup>39</sup>, Y. Enari 162, J. Erdmann 47, A. Ereditato 20, P. A. Erland 4, M. Errenst 180, M. Escalier 64, C. Escobar 172, O. Estrada Pastor<sup>172</sup>, E. Etzion<sup>160</sup>, G. Evans<sup>138a</sup>, H. Evans<sup>65</sup>, M. O. Evans<sup>155</sup>, A. Ezhilov<sup>136</sup>, F. Fabbri<sup>57</sup>, L. Fabbri<sup>23b,23a</sup>, V. Fabiani<sup>118</sup>, G. Facini<sup>176</sup>, R. M. Fakhrutdinov<sup>122</sup>, S. Falciano<sup>72a</sup>, P. J. Falke<sup>24</sup>, S. Falke<sup>36</sup>, J. Faltova<sup>141</sup>, Y. Fang<sup>15a</sup>, Y. Fang<sup>15a</sup>, G. Fanourakis<sup>44</sup>, M. Fanti<sup>68a,68b</sup>, M. Faraj<sup>60c</sup>, A. Farbin<sup>8</sup>, A. Farilla<sup>74a</sup>, E. M. Farina<sup>70a,70b</sup>, T. Farooque<sup>106</sup>, S. M. Farrington<sup>50</sup>, P. Farthouat<sup>36</sup>, F. Fassi<sup>35f</sup>, D. Fassouliotis<sup>9</sup>, M. Faucci Giannelli<sup>73a,73b</sup>, W. J. Fawcett<sup>32</sup>, L. Fayard<sup>64</sup>, O. L. Fedin<sup>136,p</sup>, A. Fehr<sup>20</sup>, M. Feickert<sup>171</sup>, L. Feligioni<sup>101</sup>, A. Fell<sup>148</sup>, C. Feng<sup>60b</sup>, M. Feng<sup>49</sup>, M. J. Fenton<sup>169</sup>, A. B. Fenyuk<sup>122</sup>, S. W. Ferguson<sup>43</sup>, J. Ferrando<sup>46</sup>, A. Ferrari<sup>170</sup>, P. Ferrari<sup>119</sup>, R. Ferrari<sup>70a</sup>, D. Ferrere<sup>54</sup>, C. Ferretti<sup>105</sup>, F. Fiedler<sup>99</sup>, A. Filipčič<sup>91</sup>, F. Filthaut<sup>118</sup>, K. D. Finelli<sup>25</sup>, M. C. N. Fiolhais<sup>138a,138c,a</sup>, L. Fiorini<sup>172</sup>, F. Fischer<sup>113</sup>, J. Fischer<sup>99</sup>, W. C. Fisher<sup>106</sup>, T. Fitschen<sup>21</sup>, I. Fleck<sup>150</sup>, P. Fleischmann<sup>105</sup>, T. Flick<sup>180</sup>, B. M. Flierl<sup>113</sup>, L. Flores<sup>135</sup>, L. R. Flores Castillo<sup>62a</sup>, F. M. Follega<sup>75a,75b</sup>, N. Fomin<sup>17</sup>, N. Fomin<sup>17</sup> J. H. Foo<sup>165</sup>, G. T. Forcolin<sup>75a,75b</sup>, B. C. Forland<sup>65</sup>, A. Formica<sup>143</sup>, F. A. Förster<sup>14</sup>, A. C. Forti<sup>100</sup>, E. Fortin<sup>101</sup>, M. G. Foti<sup>133</sup>, D. Fournier<sup>64</sup>, H. Fox<sup>89</sup>, P. Francavilla<sup>71a,71b</sup>, S. Francescato<sup>72a,72b</sup>, M. Franchini<sup>23b,23a</sup>, S. Franchino<sup>61a</sup>, D. Francis<sup>36</sup>, L. Franco<sup>5</sup>, L. Franconi<sup>20</sup>, G. Frattari<sup>72a,72b</sup>, P. M. Freeman<sup>21</sup>, B. Freund<sup>109</sup>, W. S. Freund<sup>80b</sup>, E. M. Freundlich<sup>47</sup>, D. C. Frizzell<sup>127</sup>, D. Froidevaux<sup>36</sup>, J. A. Frost<sup>133</sup>, Y. Fu<sup>60a</sup>, M. Fujimoto<sup>125</sup>, E. Fullana Torregrosa<sup>172</sup>, T. Fusayasu<sup>115</sup>, J. Fuster<sup>172</sup>, A. Gabrielli<sup>23b,23a</sup>, A. Gabrielli<sup>36</sup>, P. Gadow<sup>114</sup>, G. Gagliardi<sup>55b,55a</sup>, L. G. Gagnon<sup>109</sup>, G. E. Gallardo<sup>133</sup>, E. J. Gallas<sup>133</sup>, B. J. Gallop<sup>142</sup>, R. Gamboa Goni<sup>92</sup>, K. K. Gan<sup>126</sup>, S. Ganguly<sup>178</sup>, J. Gao<sup>60a</sup>, Y. Gao<sup>50</sup>, Y. S. Gao<sup>31,m</sup>, F. M. Garay Walls<sup>145a</sup>, C. García<sup>172</sup>, J. E. García Navarro<sup>172</sup>, J. A. García Pascual<sup>15a</sup>, M. Garcia-Sciveres<sup>18</sup>, R. W. Gardner<sup>37</sup>, S. Gargiulo<sup>52</sup>, C. A. Garner<sup>165</sup>, V. Garonne<sup>132</sup> S. J. Gasiorowski <sup>147</sup>, P. Gaspar <sup>806</sup>, G. Gaudio <sup>70a</sup>, P. Gauzzi <sup>72a,72b</sup>, I. L. Gavrilenko <sup>110</sup>, A. Gavrilyuk <sup>123</sup>, C. Gay<sup>173</sup>, G. Gaycken<sup>46</sup>, E. N. Gazis<sup>10</sup>, A. A. Geanta<sup>27b</sup>, C. M. Gee<sup>144</sup>, C. N. P. Gee<sup>142</sup>, J. Geisen<sup>96</sup>, M. Geisen<sup>99</sup>, C. Gemme<sup>55b</sup>, M. H. Genest<sup>58</sup>, C. Geng<sup>105</sup>, S. Gentile<sup>72a,72b</sup>, S. George<sup>93</sup>, T. Geralis<sup>44</sup>, L. O. Gerlach<sup>53</sup>, P. Gessinger-Befurt<sup>99</sup>, G. Gessner<sup>47</sup>, M. Ghasemi Bostanabad<sup>174</sup>, M. Ghneimat<sup>150</sup>, A. Ghosh<sup>169</sup>, A. Ghosh<sup>77</sup>, B. Giacobbe<sup>23b</sup>, S. Giagu<sup>72a,72b</sup>, N. Giangiacomi<sup>165</sup>, P. Giannetti<sup>71a</sup>, P. Giannetti<sup>71a</sup>, A. Giannini<sup>69a,69b</sup>, S. M. Gibson<sup>93</sup>, M. Gignac<sup>144</sup>, D. T. Gil<sup>83b</sup>, B. J. Gilbert<sup>39</sup>, D. Gillberg<sup>34</sup>, G. Gilles<sup>180</sup>, N. E. K. Gillwald<sup>46</sup>, D. M. Gingrich<sup>3,ak</sup>, M. P. Giordani<sup>66a,66c</sup>, P. F. Giraud<sup>143</sup>, G. Giugliarelli<sup>66a,66c</sup>, D. Giugni<sup>68a</sup>, F. Giuli<sup>73a,73b</sup>, S. Gkaitatzis<sup>161</sup>, I. Gkialas<sup>9,h</sup>, E. L. Gkougkousis<sup>14</sup>, P. Gkountoumis<sup>10</sup>, L. K. Gladilin<sup>112</sup>, C. Glasman<sup>98</sup>, G. R. Gledhill<sup>130</sup>, I. Gnesi<sup>41b,c</sup>, M. Goblirsch-Kolb<sup>26</sup>, D. Godin<sup>109</sup>, S. Goldfarb<sup>104</sup>, T. Golling<sup>54</sup>, D. Golubkov<sup>122</sup>, A. Gomes<sup>138a,138b</sup>, R. Goncalves Gama<sup>53</sup>, R. Gonçalo<sup>138a,138c</sup>, G. Gonella<sup>130</sup>, L. Gonella<sup>21</sup>, A. Gongadze<sup>79</sup>, F. Gonnella<sup>21</sup>, J. L. Gonski<sup>39</sup>, S. González de la Hoz<sup>172</sup>, R. Gonzalez Lopez<sup>90</sup>, S. Gonzalez Fernandez<sup>14</sup>, C. Gonzalez Renteria 18, R. Gonzalez Suarez<sup>170</sup>, S. Gonzalez-Sevilla<sup>54</sup>, G. R. Gonzalvo Rodriguez<sup>172</sup>, L. Goossens<sup>36</sup>, N. A. Gorasia<sup>21</sup>, P. A. Gorbounov<sup>123</sup>, H. A. Gordon<sup>29</sup>, B. Gorini<sup>36</sup>, E. Gorini<sup>67a,67b</sup>, A. Gorišek<sup>91</sup>, A. T. Goshaw<sup>49</sup>, M. I. Gostkin<sup>79</sup>, M. I. Gostkin<sup>79</sup> C. A. Gottardo<sup>118</sup>, M. Gouighri<sup>35b</sup>, A. G. Goussiou<sup>147</sup>, N. Govender<sup>33c</sup>, C. Goy<sup>5</sup>, I. Grabowska-Bold<sup>83a</sup>, E. Gramstad<sup>132</sup>, S. Grancagnolo<sup>19</sup>, M. Grandi<sup>155</sup>, V. Gratchev<sup>136</sup>, P. M. Gravila<sup>27f</sup>, F. G. Gravili<sup>67a,67b</sup>, F. G. Gravili<sup>67a,67b</sup>, C. Gray<sup>57</sup>, H. M. Gray<sup>18</sup>, C. Grefe<sup>24</sup>, I. M. Gregor<sup>46</sup>, P. Grenier<sup>152</sup>, K. Grevtsov<sup>46</sup>, C. Grieco<sup>14</sup>, N. A. Grieser<sup>127</sup>, A. A. Grillo<sup>144</sup>, K. Grimm<sup>31,1</sup>, S. Grinstein<sup>14</sup>, W., J.-F. Grivaz<sup>64</sup>, S. Groh<sup>99</sup>, E. Gross<sup>178</sup>, J. Grosse-Knetter<sup>53</sup>, Z. J. Grout<sup>94</sup>, C. Grud<sup>105</sup>, A. Grummer<sup>117</sup>, J. C. Grundy<sup>133</sup>, L. Guan<sup>105</sup>, W. Guan<sup>179</sup>, C. Gubbels<sup>173</sup>, J. Guenther<sup>36</sup>, J. G. R. Guerrero Rojas<sup>172</sup>, F. Guescini<sup>114</sup>, D. Guest<sup>19</sup>, R. Gugel<sup>99</sup>, R. Gugel<sup>99</sup> A. Guida<sup>46</sup>, T. Guillemin<sup>5</sup>, S. Guindon<sup>36</sup>, J. Guo<sup>60</sup>, L. Guo<sup>64</sup>, Y. Guo<sup>105</sup>, Z. Guo<sup>101</sup>, R. Gupta<sup>46</sup>, S. Gurbuz<sup>24</sup>, G. Gustavino<sup>127</sup>, M. Guth<sup>52</sup>, P. Gutierrez<sup>127</sup>, L. F. Gutierrez Zagazeta<sup>135</sup>, C. Gutschow<sup>94</sup>, C. Guyot<sup>143</sup>, C. Gwenlan<sup>133</sup>, C. B. Gwilliam<sup>90</sup>, E. S. Haaland<sup>132</sup>, A. Haas<sup>124</sup>, M. H. Habedank<sup>19</sup>,



Comput Softw Big Sci (2022) 6:3 Page 25 of 35 3

C. Haber<sup>18</sup>, H. K. Hadavand<sup>8</sup>, A. Hadef<sup>99</sup>, M. Haleem<sup>175</sup>, J. Haley<sup>128</sup>, J. J. Hall<sup>148</sup>, G. Halladjian<sup>106</sup>, G. D. Hallewell<sup>101</sup>, K. Hamano<sup>174</sup>, H. Hamdaoui<sup>35f</sup>, M. Hamer<sup>24</sup>, G. N. Hamity<sup>50</sup>, K. Han<sup>60a</sup>, L. Han<sup>15c</sup>, L. Han<sup>60a</sup>, S. Han<sup>18</sup>, Y. F. Han<sup>165</sup>, K. Hanagaki<sup>81</sup>, M. Hance<sup>144</sup>, M. D. Hank<sup>37</sup>, R. Hankache<sup>100</sup>, E. Hansen<sup>96</sup>, J. B. Hansen<sup>40</sup>, J. D. Hansen<sup>40</sup>, M. C. Hansen<sup>24</sup>, P. H. Hansen<sup>40</sup>, E. C. Hanson<sup>100</sup>, K. Hara<sup>167</sup>, T. Harenberg<sup>180</sup>, S. Harkusha<sup>107</sup>, P. F. Harrison<sup>176</sup>, N. M. Hartmann<sup>152</sup>, N. M. Hartmann<sup>113</sup>, Y. Hasegawa<sup>149</sup>, A. Hasib<sup>50</sup>, S. Hassani<sup>143</sup>, S. Haug<sup>20</sup>, R. Hauser<sup>106</sup>, M. Havranek<sup>140</sup>, C. M. Hawkes<sup>21</sup>, R. J. Hawkings<sup>36</sup>, S. Hayashida<sup>116</sup>, D. Hayden<sup>106</sup>, C. Hayes<sup>105</sup>, R. L. Hayes<sup>173</sup>, C. P. Hays<sup>133</sup>, J. M. Hays<sup>92</sup>, H. S. Hayward<sup>90</sup>, S. J. Haywood<sup>142</sup>, F. He<sup>60a</sup>, Y. He<sup>163</sup>, Y. He<sup>134</sup>, M. P. Heath<sup>50</sup>, V. Hedberg<sup>96</sup>, A. L. Heggelund<sup>132</sup>, N. D. Hehir<sup>92</sup>, C. Heidegger<sup>52</sup>, K. K. Heidegger<sup>52</sup>, W. D. Heidorn<sup>78</sup>, J. Heilman<sup>34</sup>, S. Heim<sup>46</sup>, T. Heim<sup>18</sup>, B. Heinemann<sup>46</sup>, i, J. G. Heinlein<sup>135</sup>, J. J. Heinrich<sup>130</sup>, L. Heinrich<sup>36</sup>, L. Heinrich<sup>36</sup> J. Hejbal<sup>139</sup>, L. Helary<sup>46</sup>, A. Held<sup>124</sup>, S. Hellesund<sup>132</sup>, C. M. Helling<sup>144</sup>, S. Hellman<sup>45a,45b</sup>, C. Helsens<sup>36</sup>, R. C. W. Henderson<sup>89</sup>, L. Henkelmann<sup>32</sup>, A. M. Henriques Correia<sup>36</sup>, H. Herde<sup>152</sup>, Y. Hernández Jiménez<sup>33f</sup>, H. Herr<sup>99</sup>, M. G. Herrmann<sup>113</sup>, T. Herrmann<sup>48</sup>, G. Herten<sup>52</sup>, R. Hertenberger<sup>113</sup>, L. Hervas<sup>36</sup>, N. P. Hessey<sup>166a</sup>, H. Hibi<sup>82</sup>, S. Higashino<sup>81</sup>, E. Higón-Rodriguez<sup>172</sup>, K. Hildebrand<sup>37</sup>, K. K. Hill<sup>29</sup>, K. H. Hiller<sup>46</sup>, S. J. Hillier<sup>21</sup>, M. Hils<sup>48</sup>, I. Hinchliffe<sup>18</sup>, F. Hinterkeuser<sup>24</sup>, M. Hirose<sup>131</sup>, S. Hirose<sup>167</sup>, D. Hirschbuehl<sup>180</sup>, B. Hiti<sup>91</sup>, O. Hladik<sup>139</sup>, J. Hobbs<sup>154</sup>, R. Hobincu<sup>27e</sup>, N. Hod<sup>178</sup>, M. C. Hodgkinson<sup>148</sup>, A. Hoecker<sup>36</sup>, D. Hohn<sup>52</sup>, T. Holm<sup>24</sup>, T. R. Holmes<sup>37</sup>, M. Holzbock<sup>114</sup>, L. B. A. H. Hommels<sup>32</sup>, T. M. Hong<sup>137</sup>, J. C. Honig<sup>52</sup>, A. Hönle<sup>114</sup>, B. H. Hooberman<sup>171</sup>, W. H. Hopkins<sup>6</sup>, Y. Horii<sup>116</sup>, P. Horn<sup>48</sup>, L. A. Horyn<sup>37</sup>, S. Hou<sup>157</sup>, J. Howarth<sup>57</sup>, J. Hoya<sup>88</sup>, M. Hrabovsky<sup>129</sup>, A. Hrynevich<sup>108</sup>, T. Hryn'ova<sup>5</sup>, P. J. Hsu<sup>63</sup>, S.-C. Hsu<sup>147</sup>, Q. Hu<sup>39</sup>, S. Hu<sup>60c</sup>, Y. F. Hu<sup>15a,15d,am</sup>, D. P. Huang<sup>94</sup>, X. Huang<sup>15c</sup>, Y. Huang<sup>60a</sup>, Y. Huang<sup>15a</sup>, F. Hubaut<sup>101</sup>, M. Huebner<sup>24</sup>, F. Huegging<sup>24</sup>, T. B. Huffman<sup>133</sup>, M. Huhtinen<sup>36</sup>, Z. Hubacek<sup>140</sup>. R. F. H. Hunter<sup>34</sup>, N. Huseynov<sup>79</sup>, ab J. Huston<sup>106</sup>, R. Hulsken<sup>58</sup>, J. Huth<sup>59</sup>, R. Hyneman<sup>152</sup>, S. Hyrych<sup>28a</sup>, G. Iacobucci<sup>54</sup>, G. Iakovidis<sup>29</sup>, I. Ibragimov<sup>150</sup>, L. Iconomidou-Fayard<sup>64</sup>, P. Iengo<sup>36</sup>, R. Ignazzi<sup>40</sup>, R. Iguchi<sup>162</sup>, T. Iizawa<sup>54</sup>, Y. Ikegami<sup>81</sup>, N. Ilic<sup>165,165</sup>, H. Imam<sup>35a</sup>, G. Introzzi<sup>70a,70b</sup>, M. Iodice<sup>74a</sup>, K. Iordanidou<sup>166a</sup>, V. Ippolito<sup>72a,72b</sup>, M. Ishino<sup>162</sup>, W. Islam<sup>128</sup>, C. Issever<sup>19,46</sup>, S. Istin<sup>12c</sup> J. M. Iturbe Ponce<sup>62a</sup>, R. Iuppa<sup>75a,75b</sup>, A. Ivina<sup>178</sup>, J. M. Izen<sup>43</sup>, V. Izzo<sup>69a</sup>, P. Jacka<sup>139</sup>, P. Jackson<sup>1</sup>, P. Jackson<sup>1</sup> R. M. Jacobs<sup>46</sup>, B. P. Jaeger<sup>151</sup>, G. Jäkel<sup>180</sup>, K. B. Jakobi<sup>99</sup>, K. Jakobs<sup>52</sup>, T. Jakoubek<sup>178</sup>, J. Jamieson<sup>57</sup> K. W. Janas<sup>83a</sup>, P. A. Janus<sup>83a</sup>, G. Jarlskog<sup>96</sup>, A. E. Jaspan<sup>90</sup>, N. Javadov<sup>79,ab</sup>, T. Javůrek<sup>36</sup>, M. Javurkova<sup>102</sup>, F. Jeanneau<sup>143</sup>, L. Jeanty<sup>130</sup>, J. Jejelava<sup>158a</sup>, P. Jenni<sup>52,d</sup>, S. Jézéquel<sup>5</sup>, J. Jia<sup>154</sup>, Z. Jia<sup>15c</sup>, Y. Jiang<sup>60a</sup>, S. Jiggins<sup>52</sup>, F. A. Jimenez Morales<sup>38</sup>, J. Jimenez Pena<sup>114</sup>, S. Jin<sup>15c</sup>, A. Jinaru<sup>27b</sup>, O. Jinnouchi<sup>163</sup>, H. Jivan<sup>33f</sup>, P. Johansson <sup>148</sup>, K. A. Johns<sup>7</sup>, C. A. Johnson <sup>65</sup>, E. Jones <sup>176</sup>, R. W. L. Jones <sup>89</sup>, T. J. Jones <sup>90</sup>, J. Jovicevic <sup>36</sup>, X. Ju<sup>18</sup>, J. J. Junggeburth<sup>114</sup>, A. Juste Rozas<sup>14</sup>, A. Kaczmarska<sup>84</sup>, M. Kado<sup>72a,72b</sup>, H. Kagan<sup>126</sup>, M. Kagan<sup>152</sup>, A. Kahn<sup>39</sup>, C. Kahra<sup>99</sup>, T. Kaji<sup>177</sup>, E. Kajomovitz<sup>159</sup>, C. W. Kalderon<sup>29</sup>, A. Kaluza<sup>99</sup>, A. Kamenshchikov<sup>122</sup>, M. Kaneda<sup>162</sup>, N. J. Kang<sup>144</sup>, S. Kang<sup>78</sup>, Y. Kano<sup>116</sup>, J. Kanzaki<sup>81</sup>, D. Kar<sup>33f</sup>, K. Karava<sup>133</sup>, M. J. Kareem<sup>166b</sup>, I. Karkanias<sup>161</sup>, S. N. Karpov<sup>79</sup>, Z. M. Karpova<sup>79</sup>, V. Kartvelishvili<sup>89</sup>, A. N. Karyukhin<sup>122</sup>, E. Kasimi<sup>161</sup>, C. Kato<sup>60d</sup>, J. Katzy<sup>46</sup>, K. Kawade<sup>149</sup>, K. Kawagoe<sup>87</sup>, T. Kawaguchi<sup>116</sup>, T. Kawamoto<sup>143</sup>, G. Kawamura<sup>53</sup>, E. F. Kay<sup>174</sup>, F. I. Kaya<sup>168</sup>, S. Kazakos<sup>14</sup>, V. F. Kazanin<sup>121b,121a</sup>, Y. Ke<sup>154</sup>, J. M. Keaveney<sup>33a</sup>, R. Keeler<sup>174</sup>, J. S. Keller<sup>34</sup>, D. Kelsey<sup>155</sup>, J. J. Kempster<sup>21</sup>, J. Kendrick<sup>21</sup>, K. E. Kennedy<sup>39</sup>, O. Kepka<sup>139</sup>, S. Kersten<sup>180</sup>, B. P. Kerševan<sup>91</sup>, S. Ketabchi Haghighat<sup>165</sup>, F. Khalil-Zada<sup>13</sup>, M. Khandoga<sup>143</sup>, A. Khanov<sup>128</sup>, A. G. Kharlamov<sup>121b,121a</sup>, T. Kharlamova<sup>121b,121a</sup>, E. E. Khoda<sup>173</sup>, E. E. Khoda<sup>173</sup> T. J. Khoo<sup>19</sup>, G. Khoriauli<sup>175</sup>, E. Khramov<sup>79</sup>, J. Khubua<sup>158b</sup>, S. Kido<sup>82</sup>, M. Kiehn<sup>36</sup>, A. Kilgallon<sup>130</sup>, E. Kim<sup>163</sup>, Y. K. Kim<sup>37</sup>, N. Kimura<sup>94</sup>, A. Kirchhoff<sup>53</sup>, D. Kirchmeier<sup>48</sup>, J. Kirk<sup>142</sup>, A. E. Kiryunin<sup>114</sup>, T. Kishimoto<sup>162</sup>, D. P. Kisliuk<sup>165</sup>, V. Kitali<sup>46</sup>, C. Kitsaki<sup>10</sup>, O. Kivernyk<sup>24</sup>, T. Klapdor-Kleingrothaus<sup>52</sup>, M. Klassen<sup>61a</sup>, C. Klein<sup>34</sup>, L. Klein<sup>175</sup>, M. H. Klein<sup>105</sup>, M. Klein<sup>90</sup>, U. Klein<sup>90</sup>, P. Klimek<sup>36</sup>, A. Klimentov<sup>29</sup>, F. Klimpel<sup>36</sup>, T. Klingl<sup>24</sup>, T. Klioutchnikova<sup>36</sup>, F. F. Klitzner<sup>113</sup>, P. Kluit<sup>119</sup>, S. Kluth<sup>114</sup>, E. Kneringer<sup>76</sup>, A. Knue<sup>52</sup>, D. Kobayashi<sup>87</sup>, M. Kobel<sup>48</sup>, M. Kocian<sup>152</sup>, T. Kodama<sup>162</sup>, P. Kodys<sup>141</sup>, P. Kodys<sup>141</sup> D. M. Koeck<sup>155</sup>, P. T. Koenig<sup>24</sup>, T. Koffas<sup>34</sup>, N. M. Köhler<sup>36</sup>, M. Kolb<sup>143</sup>, I. Koletsou<sup>5</sup>, T. Komarek<sup>129</sup>, K. Köneke<sup>52</sup>, A. X. Y. Kong<sup>1</sup>, T. Kono<sup>125</sup>, V. Konstantinides<sup>94</sup>, N. Konstantinidis<sup>94</sup>, R. Kopeliansky<sup>65</sup>, S. Koperny<sup>83</sup>, K. Korcyl<sup>84</sup>, K. Kordas<sup>161</sup>, G. Koren<sup>160</sup>, A. Korn<sup>94</sup>, S. Korn<sup>53</sup> I. Korolkov<sup>14</sup>, E. V. Korolkova<sup>148</sup>, N. Korotkova<sup>112</sup>, O. Kortner<sup>114</sup>, S. Kortner<sup>114</sup>, V. V. Kostyukhin<sup>148,164</sup>, A. Kotsokechagia<sup>64</sup>, A. Kotwal<sup>49</sup>, A. Koulouris<sup>10</sup>, A. Kourkoumeli-Charalampidi<sup>70a,70b</sup>, C. Kourkoumelis<sup>9</sup>, C. Kourkoumelis<sup>9</sup> E. Kourlitis<sup>6</sup>, R. Kowalewski<sup>174</sup>, W. Kozanecki<sup>143</sup>, A. S. Kozhin<sup>122</sup>, V. A. Kramarenko<sup>112</sup>, G. Kramberger<sup>91</sup>, D. Krasnopevtsev<sup>60a</sup>, M. W. Krasny<sup>134</sup>, A. Krasznahorkay<sup>36</sup>, J. A. Kremer<sup>99</sup>, J. Kretzschmar<sup>90</sup>, K. Kreul<sup>19</sup>, 3 Page 26 of 35 Comput Softw Big Sci (2022) 6:3

F. Krieter<sup>113</sup>, S. Krishnamurthy<sup>102</sup>, A. Krishnan<sup>61b</sup>, P. Krieger<sup>165</sup>, M. Krivos<sup>141</sup>, K. Krizka<sup>18</sup>, J. Kroll<sup>139</sup>, J. Kroll<sup>135</sup>, K. S. Krowpman<sup>106</sup>, K. Kroeninger<sup>47</sup>, H. Kroha<sup>114</sup>, U. Kruchonak<sup>79</sup>, H. Krüger<sup>24</sup>, N. Krumnack<sup>78</sup>, M. C. Kruse<sup>49</sup>, J. A. Krzysiak<sup>84</sup>, A. Kubota<sup>163</sup>, O. Kuchinskaia<sup>164</sup>, S. Kuday<sup>46</sup>, D. Kuechler<sup>46</sup>, J. T. Kuechler<sup>46</sup>, S. Kuehn<sup>36</sup>, T. Kuhl<sup>46</sup>, V. Kukhtin<sup>79</sup>, Y. Kulchitsky<sup>107,ae</sup>, S. Kuleshov<sup>145b</sup>, M. Kumar<sup>33f</sup>, M. Kuna<sup>58</sup>, A. Kupco<sup>139</sup>, T. Kupfer<sup>47</sup>, O. Kuprash<sup>52</sup>, H. Kurashige<sup>82</sup>, L. L. Kurchaninov<sup>166a</sup>, Y. A. Kurochkin<sup>107</sup>, A. Kurova<sup>111</sup>, M. G. Kurth<sup>15a,15d</sup>, E. S. Kuwertz<sup>36</sup>, M. Kuze<sup>163</sup>, A. K. Kvam<sup>147</sup>, J. Kvita<sup>129</sup>, T. Kwan<sup>103</sup>, C. Lacasta<sup>172</sup>, F. Lacava<sup>72a,72b</sup>, D. P. J. Lack<sup>100</sup>, H. Lacker<sup>19</sup>, D. Lacour<sup>134</sup>, E. Ladygin<sup>79</sup>, R. Lafaye<sup>5</sup>, B. Laforge<sup>134</sup>, T. Lagouri<sup>145</sup>, S. Lai<sup>53</sup>, I. K. Lakomiec<sup>83a</sup>, J. E. Lambert<sup>127</sup>, S. Lammers<sup>65</sup>, W. Lampl<sup>7</sup>, C. Lampoudis<sup>161</sup>, E. Lançon<sup>29</sup>, U. Landgraf<sup>52</sup>, M. P. J. Landon<sup>92</sup>, V. S. Lang<sup>52</sup>, J. C. Lange<sup>53</sup>, R. J. Langenberg<sup>102</sup>, A. J. Lankford<sup>169</sup>, F. Lanni<sup>29</sup>, K. Lantzsch<sup>24</sup>, A. Lanza<sup>70a</sup>, A. Lapertosa<sup>55b,55a</sup>, J. F. Laporte<sup>143</sup>, T. Lari<sup>68a</sup>, F. Lasagni Manghi<sup>23b,23a</sup>, M. Lassnig<sup>36</sup>, V. Latonova<sup>139</sup>, T. S. Lau<sup>62a</sup>, A. Laudrain<sup>99</sup>, A. Laurier<sup>34</sup>, M. Lavorgna<sup>69a,69b</sup>, S. D. Lawlor<sup>93</sup>, M. Lazzaroni<sup>68a,68b</sup>, B. Le<sup>100</sup>, A. Lebedev<sup>78</sup>, M. LeBlanc<sup>7</sup>, T. LeCompte<sup>6</sup>, F. Ledroit-Guillon<sup>58</sup>, A. C. A. Lee<sup>94</sup>, C. A. Lee<sup>29</sup>, G. R. Lee<sup>17</sup>, L. Lee<sup>59</sup>, S. C. Lee<sup>157</sup>, S. Lee<sup>78</sup>, L. L. Leeuw<sup>33c</sup>, B. Lefebvre<sup>166a</sup>, H. P. Lefebvre<sup>93</sup>, M. Lefebvre<sup>174</sup>, C. Leggett<sup>18</sup>, K. Lehmann<sup>151</sup>, N. Lehmann<sup>20</sup>, G. Lehmann Miotto<sup>36</sup>, W. A. Leight<sup>46</sup>, A. Leisos<sup>161</sup>, M. A. L. Leite<sup>80</sup>, C. E. Leitgeb<sup>113</sup>, R. Leitner<sup>141</sup>, K. J. C. Leney<sup>42</sup>, T. Lenz<sup>24</sup>, S. Leone<sup>71a</sup>, C. Leonidopoulos<sup>50</sup>, A. Leopold<sup>134</sup>, C. Leroy<sup>109</sup>, R. Les<sup>106</sup>, C. G. Lester<sup>32</sup>, M. Levchenko<sup>136</sup>, J. Levêque<sup>5</sup>, D. Levin<sup>105</sup>, L. J. Levinson<sup>178</sup>, D. J. Lewis<sup>21</sup>, B. Li<sup>15b</sup>, B. Li<sup>105</sup>, C-Q. Li<sup>60c,60d</sup>, F. Li<sup>60c</sup>, H. Li<sup>60a</sup>, H. Li<sup>60b</sup>, J. Li<sup>60c</sup>, K. Li<sup>147</sup>, L. Li<sup>60c</sup>, M. Li<sup>15a,15d</sup>, Q. Y. Li<sup>60a</sup> S. Li<sup>60d,60c,b</sup>, X. Li<sup>46</sup>, Y. Li<sup>46</sup>, Z. Li<sup>60b</sup>, Z. Li<sup>133</sup>, Z. Li<sup>103</sup>, Z. Li<sup>90</sup>, Z. Liang<sup>15a</sup>, M. Liberatore<sup>46</sup>, B. Liberti<sup>73a</sup>, K. Lie<sup>62c</sup>, C. Y. Lin<sup>32</sup>, K. Lin<sup>106</sup>, R. A. Linck<sup>65</sup>, R. E. Lindley<sup>7</sup>, J. H. Lindon<sup>21</sup>, A. Linss<sup>46</sup>, A. L. Lionti<sup>54</sup>, E. Lipeles<sup>135</sup>, A. Lipniacka<sup>17</sup>, T. M. Liss<sup>171</sup>, A. Lister<sup>173</sup>, J. D. Little<sup>8</sup>, B. Liu<sup>15a</sup>, B. X. Liu<sup>151</sup>, J. B. Liu<sup>60a</sup>, J. K. K. Liu<sup>37</sup>, K. Liu<sup>60d</sup>, M. Liu<sup>60a</sup>, M. Y. Liu<sup>60a</sup>, P. Liu<sup>15a</sup>, X. Liu<sup>60a</sup>, Y. Liu<sup>46</sup>, Y. Liu<sup>15a</sup>, Y. Liu<sup>105</sup>, Y. W. Liu<sup>60a</sup>, M. Livan<sup>70a,70b</sup>, A. Lleres<sup>58</sup>, J. Llorente Merino<sup>151</sup>, S. L. Lloyd<sup>92</sup>, E. M. Lobodzinska<sup>46</sup>, P. Loch<sup>7</sup>, S. Loffredo<sup>73a,73b</sup>, T. Lohse<sup>19</sup>, K. Lohwasser<sup>148</sup>, M. Lokajicek 139 , J. D. Long 171 , R. E. Long 89 , I. Longarini 72a,72b , L. Longo 36 , R. Longo 171 , I. Lopez Paz 14, A. Lopez Solis<sup>46</sup>, J. Lorenz<sup>113</sup>, N. Lorenzo Martinez<sup>5</sup>, A. M. Lory<sup>113</sup>, A. Lösle<sup>52</sup>, X. Lou<sup>45a,45b</sup>, X. Lou<sup>15a</sup>, A. Lounis<sup>64</sup>, J. Love<sup>6</sup>, P. A. Love<sup>89</sup>, J. J. Lozano Bahilo<sup>172</sup>, G. Lu<sup>15a</sup>, M. Lu<sup>60a</sup>, S. Lu<sup>135</sup>, Y. J. Lu<sup>63</sup>, H. J. Lubatti <sup>147</sup>, C. Luci <sup>72a,72b</sup>, F. L. Lucio Alves <sup>15c</sup>, A. Lucotte <sup>58</sup>, F. Luehring <sup>65</sup>, I. Luise <sup>154</sup>, L. Luminari <sup>72a</sup>, B. Lund-Jensen 153 , N. A. Luongo 130 , M. S. Lutz 160 , D. Lynn 90 , H. Lyons 90 , R. Lysak 139 , E. Lytken 96 , F. Lyu<sup>15a</sup>, V. Lyubushkin<sup>79</sup>, T. Lyubushkina<sup>79</sup>, H. Ma<sup>29</sup>, L. L. Ma<sup>60b</sup>, Y. Ma<sup>94</sup>, D. M. Mac Donell<sup>174</sup>, G. Maccarrone<sup>51</sup>, C. M. Macdonald<sup>148</sup>, J. C. MacDonald<sup>148</sup>, J. Machado Miguens<sup>135</sup>, W. F. Mader<sup>48</sup>, M. Madugoda Ralalage Don<sup>128</sup>, N. Madysa<sup>48</sup>, J. Maeda<sup>82</sup>, T. Maeno<sup>29</sup>, M. Maerker<sup>48</sup>, V. Magerl<sup>52</sup>, J. Magro<sup>66a,66c</sup>, D. J. Mahon<sup>39</sup>, C. Maidantchik<sup>80b</sup>, A. Maio<sup>138a,138b,138d</sup>, K. Maj<sup>83a</sup>, O. Majersky<sup>28a</sup>, S. Majewski<sup>130</sup>, N. Makovec<sup>64</sup>, B. Malaescu<sup>134</sup>, Pa. Malecki<sup>84</sup>, V. P. Maleev<sup>136</sup>, F. Malek<sup>58</sup>, D. Malito<sup>41b,41a</sup>, U. Mallik<sup>77</sup>, C. Malone<sup>32</sup>, S. Maltezos<sup>10</sup>, S. Malyukov<sup>79</sup>, J. Mamuzic<sup>172</sup>, G. Mancini<sup>51</sup>, I. Mandić<sup>91</sup>, L. Manhaes de Andrade Filho<sup>80a</sup>, I. M. Maniatis<sup>161</sup>, M. Manisha<sup>143</sup>, J. Manjarres Ramos<sup>48</sup>, K. H. Mankinen<sup>96</sup>, A. Mann<sup>113</sup>, A. Manousos<sup>76</sup>, B. Mansoulie<sup>143</sup>, I. Manthos<sup>161</sup>, S. Manzoni<sup>119</sup>, A. Marantis<sup>161</sup>, D. L. Marchese<sup>133</sup>, G. Marchiori<sup>134</sup>, M. Marcisovsky<sup>139</sup>, L. Marcoccia<sup>73a,73b</sup>, C. Marcon<sup>96</sup>, M. Marjanovic<sup>127</sup>, Z. Marshall<sup>18</sup>, M. U. F. Martensson<sup>170</sup>, S. Marti-Garcia<sup>172</sup>, T. A. Martin<sup>176</sup>, B. Martin dit Latour<sup>17</sup>, L. Martinelli<sup>74a,74b</sup>, M. Martinez<sup>14,w</sup>, P. Martinez Agullo<sup>172</sup>, V. I. Martinez Outschoorn<sup>102</sup>, S. Martin-Haugh<sup>142</sup>, V. S. Martoiu<sup>27b</sup>, A. C. Martyniuk<sup>94</sup>, A. Marzin<sup>36</sup>, S. R. Maschek<sup>114</sup>, L. Masetti<sup>99</sup>, T. Mashimo<sup>162</sup>, R. Mashinistov<sup>110</sup>, J. Masik<sup>100</sup>, A. L. Maslennikov<sup>121b,121a</sup> L. Massa<sup>23b,23a</sup>, P. Massarotti<sup>69a,69b</sup>, P. Mastrandrea<sup>71a,71b</sup>, A. Mastroberardino<sup>41b,41a</sup>, T. Masubuchi<sup>162</sup>, D. Matakias<sup>29</sup>, T. Mathisen<sup>170</sup>, A. Matic<sup>113</sup>, N. Matsuzawa<sup>162</sup>, J. Maurer<sup>27b</sup>, B. Maček<sup>91</sup>, D. A. Maximov<sup>121b,121a</sup>, R. Mazini<sup>157</sup>, I. Maznas<sup>161</sup>, S. M. Mazza<sup>144</sup>, C. Mc Ginn<sup>29</sup>, J. P. Mc Gowan<sup>103</sup>, T. G. McCarthy<sup>114</sup>, W. P. McCormack<sup>18</sup>, E. F. McDonald<sup>104</sup>, A. E. McDougall<sup>119</sup>, S. P. Mc Kee<sup>105</sup>, G. Mchedlidze<sup>158b</sup>, M. A. McKay<sup>42</sup>, K. D. McLean<sup>174</sup>, J. A. Mcfayden<sup>155</sup>, S. J. McMahon<sup>142</sup>, P. C. McNamara<sup>104</sup>, R. A. McPherson<sup>174,aa</sup>, J. E. Mdhluli<sup>33f</sup>, Z. A. Meadows<sup>102</sup>, S. Meehan<sup>36</sup>, T. Megy<sup>38</sup>, S. Mehlhase 113, A. Mehta 90, B. Meirose 43, D. Melini 159, B. R. Mellado Garcia 33f, F. Meloni 46, A. Melzer 24, E. D. Mendes Gouveia<sup>138a,138e</sup>, A. M. Mendes Jacques Da Costa<sup>21</sup>, H. Y. Meng<sup>165</sup>, L. Meng<sup>36</sup>, S. Menke<sup>114</sup>, E. Meoni<sup>41b,41a</sup>, S. A. M. Merkt<sup>137</sup>, C. Merlassino<sup>133</sup>, P. Mermod<sup>54,†</sup>, L. Merola<sup>69a,69b</sup>, C. Meroni<sup>68a</sup>, G. Merz<sup>105</sup>, O. Meshkov<sup>112,110</sup>, J. K. R. Meshreki<sup>150</sup>, J. Metcalfe<sup>6</sup>, A. S. Mete<sup>6</sup>, C. Meyer<sup>65</sup>, J.-P. Meyer<sup>143</sup>,



Comput Softw Big Sci (2022) 6:3 Page 27 of 35 3

M. Michetti<sup>19</sup>, R. P. Middleton<sup>142</sup>, L. Mijović<sup>50</sup>, G. Mikenberg<sup>178</sup>, M. Mikestikova<sup>139</sup>, M. Mikuž<sup>91</sup>, H. Mildner 148, A. Milic 165, C. D. Milke 42, D. W. Miller 37, L. S. Miller 34, A. Milov 178, D. A. Milstead 45a, 45b, A. A. Minaenko<sup>122</sup>, I. A. Minashvili<sup>158b</sup>, L. Mince<sup>57</sup>, A. I. Mincer<sup>124</sup>, B. Mindur<sup>83a</sup>, M. Mineev<sup>79</sup>, Y. Minegishi<sup>162</sup>, Y. Mino<sup>85</sup>, L. M. Mir<sup>14</sup>, M. Miralles Lopez<sup>172</sup>, M. Mironova<sup>133</sup>, T. Mitani<sup>177</sup>, V. A. Mitsou<sup>172</sup> M. Mittal<sup>60c</sup>, O. Miu<sup>165</sup>, A. Miucci<sup>20</sup>, P. S. Miyagawa<sup>92</sup>, A. Mizukami<sup>81</sup>, J. U. Mjörnmark<sup>96</sup>, T. Mkrtchyan<sup>61a</sup> M. Mlynarikova<sup>120</sup>, T. Moa<sup>45a,45b</sup>, S. Mobius<sup>53</sup>, K. Mochizuki<sup>109</sup>, P. Moder<sup>46</sup>, P. Mogg<sup>113</sup>, S. Mohapatra<sup>39</sup>, G. Mokgatitswane<sup>33f</sup> D, B. Mondal<sup>150</sup> D, S. Mondal<sup>140</sup> D, K. Mönig<sup>46</sup> D, E. Monnier<sup>101</sup> D, A. Montalbano<sup>151</sup> D, J. Montejo Berlingen<sup>36</sup>, M. Montella<sup>94</sup>, F. Monticelli<sup>88</sup>, N. Morange<sup>64</sup>, A. L. Moreira De Carvalho<sup>138</sup>, M. Moreno Llácer<sup>172</sup>, C. Moreno Martinez<sup>14</sup>, P. Morettini<sup>55b</sup>, M. Morgenstern<sup>159</sup>, S. Morgenstern<sup>176</sup>, D. Mori<sup>151</sup>, M. Morii<sup>59</sup>, M. Morinaga<sup>177</sup>, V. Morisbak<sup>132</sup>, A. K. Morley<sup>36</sup>, A. P. Morris<sup>94</sup>, L. Morvaj<sup>36</sup>, P. Moschovakos<sup>36</sup>, B. Moser<sup>119</sup>, M. Mosidze<sup>158b</sup>, T. Moskalets<sup>143</sup>, P. Moskvitina<sup>118</sup>, J. Moss<sup>31,n</sup>, E. J. W. Moyse<sup>102</sup>, S. Muanza<sup>101</sup>, J. Mueller<sup>137</sup>, D. Muenstermann<sup>89</sup>, G. A. Mullier<sup>96</sup>, J. J. Mullian<sup>135</sup>, D. P. Mungo<sup>68a,68b</sup>, J. L. Munoz Martinez<sup>14</sup>, F. J. Munoz Sanchez<sup>100</sup>, P. Murin<sup>28b</sup>, W. J. Murray<sup>176,142</sup>, A. Murrone<sup>68a,68b</sup>, J. M. Muse<sup>127</sup>, M. Muškinja<sup>18</sup>, C. Mwewa<sup>33a</sup>, A. G. Myagkov<sup>122,af</sup>, A. A. Myers<sup>137</sup>, G. Myers<sup>65</sup>, J. Myers<sup>130</sup>, M. Myska<sup>140</sup>, B. P. Nachman<sup>18</sup>, O. Nackenhorst<sup>47</sup>, A.Nag Nag<sup>48</sup>, K. Nagai<sup>133</sup>, K. Nagano<sup>81</sup>, J. L. Nagle<sup>29</sup>, E. Nagy<sup>101</sup>, A. M. Nairz<sup>36</sup>, Y. Nakahama<sup>116</sup>, K. Nakamura<sup>81</sup>, H. Nanjo<sup>131</sup>, F. Napolitano<sup>61a</sup>, R. F. Naranjo Garcia<sup>46</sup>, R. Narayan<sup>42</sup>, I. Naryshkin<sup>136</sup>, M. Naseri<sup>34</sup>, T. Naumann<sup>46</sup>, G. Navarro<sup>22a</sup>, J. Navarro-Gonzalez<sup>172</sup>, P. Y. Nechaeva<sup>110</sup>, F. Nechansky<sup>46</sup>, T. J. Neep<sup>21</sup>, A. Negri<sup>70a,70b</sup>, M. Negrini<sup>23b</sup>, C. Nellist<sup>118</sup>, C. Nelson<sup>103</sup>, K. Nelson<sup>105</sup>, M. E. Nelson<sup>45a,45b</sup>, S. Nemecek<sup>139</sup>, M. Nessi<sup>36,f</sup>, M. Nessi<sup>36,f</sup> M. S. Neubauer<sup>171</sup>, F. Neuhaus<sup>99</sup>, M. Neumann<sup>180</sup>, R. Newhouse<sup>173</sup>, P. R. Newman<sup>21</sup>, C. W. Ng<sup>137</sup>, B. Ngair<sup>35f</sup>, H. D. N. Nguyen<sup>101</sup>, T. Nguyen Manh<sup>109</sup>, E. Nibigira<sup>38</sup>, Y. S. Ng<sup>19</sup>, Y. W. Y. Ng<sup>169</sup>, R. B. Nickerson<sup>133</sup>, R. Nicolaidou<sup>143</sup>, D. S. Nielsen<sup>40</sup>, J. Nielsen<sup>144</sup>, M. Niemeyer<sup>53</sup>, N. Nikiforou<sup>11</sup>, V. Nikolaenko<sup>122,af</sup>, I. Nikolic-Audit<sup>134</sup>, K. Nikolopoulos<sup>21</sup>, P. Nilsson<sup>29</sup>, H. R. Nindhito<sup>54</sup>, A. Nisati<sup>72a</sup>, N. Nishu<sup>60c</sup>, R. Nisius<sup>114</sup>, T. Nitta<sup>177</sup>, T. Nobe<sup>162</sup>, D. L. Noel<sup>32</sup>, Y. Noguchi<sup>85</sup>, I. Nomidis<sup>134</sup>, M. A. Nomura<sup>29</sup>, R. R. B. Norisam<sup>94</sup>, J. Novak<sup>91</sup>, T. Novak<sup>46</sup>, O. Novgorodova<sup>48</sup>, R. Novotny<sup>117</sup>, L. Nozka<sup>129</sup>, K. Ntekas<sup>169</sup>, E. Nurse<sup>94</sup>, F. G. Oakham<sup>34,ak</sup>, J. Ocariz<sup>134</sup>, A. Ochi<sup>82</sup>, I. Ochoa<sup>138a</sup>, J. P. Ochoa-Ricoux<sup>145a</sup> K. O'Connor<sup>26</sup>, S. Oda<sup>87</sup>, S. Odaka<sup>81</sup>, S. Oerdek<sup>53</sup>, A. Ogrodnik<sup>83a</sup>, A. Oh<sup>100</sup>, C. C. Ohm<sup>153</sup>, H. Oide<sup>163</sup>, R. Oishi<sup>162</sup>, M. L. Ojeda<sup>165</sup>, Y. Okazaki<sup>85</sup>, M. W. O'Keefe<sup>90</sup>, Y. Okumura<sup>162</sup>, A. Olariu<sup>27b</sup>, L. F. Oleiro Seabra 138a, S. A. Olivares Pino 145c, D. Oliveira Damazio 29, D. Oliveira Goncalves 80a, J. L. Oliver 1, D. Oliveira Damazio 29, D. Oliveira Goncalves 80a, J. L. Oliver 1, D. Oliveira Damazio 29, D. Oliveira Goncalves 80a, J. L. Oliver 1, D. Oliveira Damazio 29, D. Oliveira Goncalves 80a, J. L. Oliver 1, D. Oliveira Damazio 29, D. Oliveira Damazio 29, D. Oliveira Goncalves 80a, D. Oliveira Damazio 29, D. Oliveira Damazio 20, D. Oliveira Damazio 20, D. Oliveira Damazio 20, D. Oliveira 20, D. Oliveira Damazio 20, D. Oliveira 20, D. M. J. R. Olsson<sup>169</sup>, A. Olszewski<sup>84</sup>, J. Olszowska<sup>84</sup>, Ö.O. Öncel<sup>24</sup>, D. C. O'Neil<sup>151</sup>, A. P. O'neill<sup>133</sup>, A. Onofre<sup>138a,138e</sup>, P. U. E. Onyisi<sup>11</sup>, H. Oppen<sup>132</sup>, R. G. Oreamuno Madriz<sup>120</sup>, M. J. Oreglia<sup>37</sup>, G. E. Orellana<sup>88</sup>, D. Orestano<sup>74a,74b</sup>, N. Orlando<sup>14</sup>, R. S. Orr<sup>165</sup>, V. O'Shea<sup>57</sup>, R. Ospanov<sup>60a</sup>, G. Otero y Garzon<sup>30</sup>, H. Otono<sup>87</sup>, P. S. Ott<sup>61a</sup>, G. J. Ottino<sup>18</sup>, M. Ouchrif<sup>35e</sup>, J. Ouellette<sup>29</sup>, F. Ould-Saada<sup>132</sup>, A. Ouraou<sup>143,†</sup> Q. Ouyang <sup>15</sup>a, M. Owen <sup>57</sup>b, R. E. Owen <sup>142</sup>b, V. E. Ozcan <sup>12</sup>c, N. Ozturk <sup>8</sup>b, J. Pacalt <sup>129</sup>b, H. A. Pacey <sup>32</sup>b, K. Pachal <sup>49</sup>b, A. Pacheco Pages <sup>14</sup>b, C. Padilla Aranda <sup>14</sup>b, S. Pagan Griso <sup>18</sup>b, G. Palacino <sup>65</sup>, S. Palazzo <sup>50</sup>b, S. Palestini<sup>36</sup>, M. Palka<sup>83b</sup>, P. Palni<sup>83a</sup>, D. K. Panchal<sup>11</sup>, C. E. Pandini<sup>54</sup>, J. G. Panduro Vazquez<sup>93</sup>, P. Pani<sup>46</sup>, G. Panizzo<sup>66a,66c</sup>, L. Paolozzi<sup>54</sup>, C. Papadatos<sup>109</sup>, S. Parajuli<sup>42</sup>, A. Paramonov<sup>6</sup>, C. Paraskevopoulos<sup>10</sup>, D. Paredes Hernandez<sup>62b</sup>, S. R. Paredes Saenz<sup>133</sup>, B. Parida<sup>178</sup>, T. H. Park<sup>165</sup>, A. J. Parker<sup>31</sup>, M. A. Parker<sup>32</sup>, F. Parodi<sup>55b,55a</sup>, E. W. Parrish<sup>120</sup>, J. A. Parsons<sup>39</sup>, U. Parzefall<sup>52</sup>, L. Pascual Dominguez<sup>134</sup>, V. R. Pascuzzi<sup>18</sup>, J. M. P. Pasner<sup>144</sup>, F. Pasquali<sup>119</sup>, E. Pasqualucci<sup>72a</sup>, S. Passaggio<sup>55b</sup>, F. Pastore<sup>93</sup>, P. Pasuwan<sup>45a,45b</sup>, J. R. Pater<sup>100</sup>, A. Pathak<sup>179</sup>, b, J. Patton<sup>90</sup>, T. Pauly<sup>36</sup>, J. Pearkes<sup>152</sup>, M. Pedersen<sup>132</sup>, L. Pedraza Diaz<sup>118</sup>, R. Pedro<sup>138a</sup>, T. Peiffer<sup>53</sup>, S. V. Peleganchuk<sup>121b,121a</sup>, O. Penc<sup>139</sup>, C. Peng<sup>62b</sup>, H. Peng<sup>60a</sup>, M. Penzin<sup>164</sup>, B. S. Peralva<sup>80a</sup>, M. M. Perego<sup>64</sup>, A. P. Pereira Peixoto<sup>138a</sup>, L. Pereira Sanchez<sup>45a,45b</sup>, D. V. Perepelitsa<sup>29</sup>, E. Perez Codina 166a, M. Perganti 10, L. Perini 68a,68b, H. Pernegger 36, S. Perrella 36, A. Perrevoort 119, K. Peters<sup>46</sup>, R. F. Y. Peters<sup>100</sup>, B. A. Petersen<sup>36</sup>, T. C. Petersen<sup>40</sup>, E. Petit<sup>101</sup>, V. Petousis<sup>140</sup>, C. Petridou<sup>161</sup>, P. Petroff<sup>64</sup>, F. Petrucci<sup>74a,74b</sup>, M. Pettee<sup>181</sup>, N. E. Pettersson<sup>102</sup>, K. Petukhova<sup>141</sup>, A. Peyaud<sup>143</sup>, R. Pezoa<sup>145d</sup>, L. Pezzotti<sup>70a,70b</sup>, G. Pezzullo<sup>181</sup>, T. Pham<sup>104</sup>, P. W. Phillips<sup>142</sup>, M. W. Phipps<sup>171</sup>, G. Piacquadio<sup>154</sup>, E. Pianori<sup>18</sup>, A. Picazio<sup>102</sup>, R. Piegaia<sup>30</sup>, D. Pietreanu<sup>27b</sup>, J. E. Pilcher<sup>37</sup>, A. D. Pilkington<sup>100</sup>, M. Pinamonti<sup>66a,66c</sup>, J. L. Pinfold<sup>3</sup>, C. Pitman Donaldson<sup>94</sup>, D. A. Pizzii<sup>34</sup>, L. Pizzimento<sup>73a,73b</sup>, A. Pizzini<sup>119</sup>, M.-A. Pleier<sup>29</sup>, V. Plesanovs<sup>52</sup>, V. Pleskot<sup>141</sup>, E. Plotnikova<sup>79</sup>, P. Podberezko<sup>121b,121a</sup>, R. Poettgen<sup>96</sup>, R. Poggi<sup>54</sup>, L. Poggioli<sup>134</sup>, I. Pogrebnyak<sup>106</sup>, D. Pohl<sup>24</sup>, I. Pokharel<sup>53</sup>, G. Polesello<sup>70a</sup>, A. Poley<sup>151,166a</sup>, A. Policicchio<sup>72a,72b</sup>, R. Polifka<sup>141</sup>, A. Polini<sup>23b</sup>, C. S. Pollard<sup>46</sup>, V. Polychronakos<sup>29</sup>, D. Ponomarenko<sup>111</sup>, D. L. Pontecorvo<sup>36</sup>, S. Popa<sup>27a</sup>, G. A. Popeneciu<sup>27d</sup>, L. Portales<sup>5</sup>, D. M. Portillo Quintero<sup>58</sup>, S. Pospisil<sup>140</sup>,

3 Page 28 of 35 Comput Softw Big Sci (2022) 6:3

P. Postolache<sup>27c</sup>, K. Potamianos<sup>133</sup>, I. N. Potrap<sup>79</sup>, C. J. Potter<sup>32</sup>, H. Potti<sup>11</sup>, T. Poulsen<sup>46</sup>, J. Poveda<sup>172</sup> G. Pownall<sup>46</sup>, M. E. Pozo Astigarraga<sup>36</sup>, A. Prades Ibanez<sup>172</sup>, P. Pralavorio<sup>101</sup>, M. M. Prapa<sup>44</sup>, S. Prell<sup>78</sup>, D. Price<sup>100</sup>, M. Primavera<sup>67a</sup>, M. L. Proffitt<sup>147</sup>, N. Proklova<sup>111</sup>, K. Prokofiev<sup>62c</sup>, S. Protopopescu<sup>29</sup>, J. Proudfoot<sup>6</sup>, M. Przybycien<sup>83</sup>, D. Pudzha<sup>136</sup>, F. Prokoshin<sup>79</sup> D. Pyatiizbyantseva<sup>111</sup>, J. Qian<sup>105</sup>, Y. Qin<sup>100</sup>, A. Quadt<sup>53</sup>, M. Queitsch-Maitland<sup>36</sup>, G. Rabanal Bolanos<sup>59</sup>, F. Ragusa<sup>68a,68b</sup>, G. Rahal<sup>97</sup>, J. A. Raine<sup>54</sup>, S. Rajagopalan<sup>29</sup>, K. Ran<sup>15a,15d</sup>, D. F. Rassloff<sup>61a</sup>, D. M. Rauch<sup>46</sup>, S. Rave<sup>99</sup>, B. Ravina<sup>57</sup>, I. Ravinovich<sup>178</sup>, M. Raymond<sup>36</sup>, A. L. Read<sup>132</sup>, N. P. Readioff<sup>148</sup>, M. Reale<sup>67a,67b</sup>, D. M. Rebuzzi<sup>70a,70b</sup>, G. Redlinger<sup>29</sup>, K. Reeves<sup>43</sup>, D. Reikher<sup>160</sup>, A. Reiss<sup>99</sup>, A. Rej<sup>150</sup>, C. Rembser<sup>36</sup>, A. Renardi<sup>46</sup>, M. Renda<sup>27b</sup>, M. B. Rendel<sup>114</sup>, A. G. Rennie<sup>57</sup>, S. Resconi<sup>68a</sup>, E. D. Resseguie<sup>18</sup>, S. Rettie<sup>94</sup>, B. Reynolds<sup>126</sup>, E. Reynolds<sup>21</sup>, M. Rezaei Estabragh<sup>180</sup>, O. L. Rezanova<sup>121b,121a</sup>, P. Reznicek<sup>141</sup>, E. Ricci<sup>75a,75b</sup>, R. Richter<sup>114</sup>, S. Richter<sup>46</sup>, E. Richter-Was<sup>83b</sup>, M. Ridel<sup>134</sup>, P. Rieck<sup>114</sup>, O. Rifki<sup>46</sup>, M. Rijssenbeek<sup>154</sup>, A. Rimoldi<sup>70a,70b</sup>, M. Rimoldi<sup>46</sup>, L. Rinaldi<sup>23b</sup>, T. T. Rinn<sup>171</sup>, G. Ripellino<sup>153</sup>, I. Riu<sup>14</sup>, P. Rivadeneira<sup>46</sup>, J. C. Rivera Vergara<sup>174</sup>, F. Rizatdinova<sup>128</sup>, E. Rizvi<sup>92</sup>, C. Rizzi<sup>54</sup>, S. H. Robertson<sup>103,aa</sup>, M. Robin<sup>46</sup>, D. Robinson<sup>32</sup>, C. M. Robles Gajardo<sup>145d</sup>, M. Robles Manzano<sup>99</sup>, A. Robson<sup>57</sup>, A. Rocchi<sup>73a,73b</sup>, C. Roda<sup>71a,71b</sup>, S. Rodriguez Bosca<sup>172</sup>, A. Rodriguez Rodriguez<sup>52</sup>, A. M. Rodríguez Vera<sup>166b</sup>, S. Roe<sup>36</sup>, J. Roggel<sup>180</sup>, O. Røhne<sup>132</sup>, R. A. Rojas<sup>145</sup>, B. Roland<sup>52</sup>, C. P. A. Roland<sup>65</sup>, J. Roloff<sup>29</sup>, A. Romaniouk<sup>111</sup>, M. Romano<sup>23b,23a</sup>, N. Rompotis<sup>90</sup>, M. Ronzani<sup>124</sup>, L. Roos<sup>134</sup>, S. Rosati<sup>72a</sup>, G. Rosin<sup>102</sup>, B. J. Rosser<sup>135</sup>, E. Rossi<sup>50</sup>, E. Rossi<sup>69a,69b</sup>, L. P. Rossi<sup>55b</sup>, L. Rossin<sup>46</sup>, R. Rosten<sup>126</sup>, M. Rotaru<sup>27b</sup>, B. Rottler<sup>52</sup>, D. Rousseau<sup>64</sup>, G. Rovelli<sup>70a,70b</sup>, A. Roy<sup>11</sup>, A. Rozanov<sup>101</sup>, Y. Rozen<sup>159</sup>, X. Ruan<sup>33f</sup>, A. J. Ruby<sup>90</sup>, T. A. Ruggeri<sup>1</sup>, F. Rühr<sup>52</sup>, A. Ruiz-Martinez<sup>172</sup>, A. Rummler<sup>36</sup>, Z. Rurikova<sup>52</sup>, N. A. Rusakovich<sup>79</sup>, H. L. Russell<sup>36</sup>, L. Rustige<sup>38</sup>, J. P. Rutherfoord<sup>7</sup>, E. M. Rüttinger<sup>148</sup>, M. Rybar<sup>141</sup>, M. Rybar<sup>141</sup>, Russell<sup>36</sup>, Russell<sup>36</sup> A. Rummler<sup>36</sup>, Z. Rurikova<sup>52</sup>. E. B. Rye<sup>132</sup>, A. Ryzhov<sup>122</sup>, J. A. Sabater Iglesias<sup>46</sup>, P. Sabatini<sup>172</sup>, L. Sabetta<sup>72a,72b</sup>, H.F-W. Sadrozinski<sup>144</sup>, R. Sadykov<sup>79</sup>, F. Safai Tehrani<sup>72a</sup>, B. Safarzadeh Samani<sup>155</sup>, M. Safdari<sup>152</sup>, P. Saha<sup>120</sup>, S. Saha<sup>103</sup>, S. Saha<sup>103</sup> M. Sahinsoy<sup>114</sup>, A. Sahu<sup>180</sup>, M. Saimpert<sup>36</sup>, M. Saito<sup>162</sup>, T. Saito<sup>162</sup>, D. Salamani<sup>54</sup>, G. Salamanna<sup>74a,74b</sup>, A. Salnikov<sup>152</sup>, J. Salt<sup>172</sup>, A. Salvador Salas<sup>14</sup>, D. Salvatore<sup>41b,41a</sup>, F. Salvatore<sup>155</sup>, A. Salzburger<sup>36</sup>, D. Sammel<sup>52</sup>, D. Sampsonidis<sup>161</sup>, D. Sampsonidou<sup>60d,60c</sup>, J. Sánchez<sup>172</sup>, A. Sanchez Pineda<sup>66a,36,66c</sup>, H. Sandaker<sup>132</sup>, C. O. Sander<sup>46</sup>, I. G. Sanderswood<sup>89</sup>, M. Sandhoff<sup>180</sup>, C. Sandoval<sup>22b</sup>, D. P. C. Sankey<sup>142</sup>, M. Sannino<sup>55b,55a</sup>, Y. Sano<sup>116</sup>, A. Sansoni<sup>51</sup>, C. Santoni<sup>38</sup>, H. Santos<sup>138a,138b</sup>, S. N. Santpur<sup>18</sup>, A. Santra<sup>178</sup>, K. A. Saoucha<sup>148</sup>, A. Sapronov<sup>79</sup>, J. G. Saraiva<sup>138a,138d</sup>, O. Sasaki<sup>81</sup>, F. Sauerburger<sup>52</sup>, E. Sauvan<sup>5</sup>, P. Savard<sup>165,ak</sup>, R. Sawada<sup>162</sup>, C. Sawyer<sup>142</sup>, L. Sawyer<sup>95</sup>, I. Sayago Galvan<sup>172</sup>, C. Sbarra<sup>23b</sup>, A. Sbrizzi<sup>66a,66c</sup>, T. Scanlon<sup>94</sup>, J. Schaarschmidt<sup>147</sup>, P. Schacht<sup>114</sup>, D. Schaefer<sup>37</sup>, D. Schaefer<sup>37</sup> L. Schaefer<sup>135</sup>, U. Schäfer<sup>99</sup>, A. C. Schaffer<sup>64</sup>, D. Schaile<sup>113</sup>, R. D. Schamberger<sup>154</sup>, E. Schanet<sup>113</sup> C. Scharf<sup>19</sup>, N. Scharmberg<sup>100</sup>, V. A. Schegelsky<sup>136</sup>, D. Scheirich<sup>141</sup>, F. Schenck<sup>19</sup>, M. Schernau<sup>169</sup>, C. Schiavi<sup>55b,55a</sup>. L. K. Schildgen<sup>24</sup>, Z. M. Schillaci<sup>26</sup>, E. J. Schioppa<sup>67a,67b</sup>, M. Schioppa<sup>41b,41a</sup>, K. E. Schleicher<sup>52</sup>, S. Schlenker<sup>36</sup>, K. Schmieden<sup>99</sup>, C. Schmitt<sup>99</sup>, S. Schmitt<sup>46</sup>, L. Schoeffel<sup>143</sup>, A. Schoening<sup>61b</sup>, P. G. Scholer<sup>52</sup>, E. Schopf<sup>133</sup>, M. Schott<sup>99</sup>, J. Schovancova<sup>36</sup>, S. Schramm<sup>54</sup>, F. Schroeder 180, A. Schulte 99, H-C. Schultz-Coulon 61a, M. Schumacher 52, B. A. Schumm 144, Ph. Schume 143, Ph. Schume 144, Ph. Schume 143, Ph. Schume 144, A. Schwartzman<sup>152</sup>, T. A. Schwarz<sup>105</sup>, Ph. Schwemling<sup>143</sup>, R. Schwienhorst<sup>106</sup>, A. Sciandra<sup>144</sup>, G. Sciolla<sup>26</sup>, F. Scuri<sup>71a</sup>, F. Scutti<sup>104</sup>, C. D. Sebastiani<sup>90</sup>, K. Sedlaczek<sup>47</sup>, P. Seema<sup>19</sup>, S. C. Seidel<sup>117</sup>, A. Seiden<sup>144</sup>, B. D. Seidlitz<sup>29</sup>, T. Seiss<sup>37</sup>, C. Seitz<sup>46</sup>, J. M. Seixas<sup>80b</sup>, G. Sekhniaidze<sup>69a</sup>, S. J. Sekula<sup>42</sup>, L. P. Selem<sup>5</sup>, N. Semprini-Cesari<sup>23b,23a</sup>, S. Sen<sup>49</sup>, C. Serfon<sup>29</sup>, L. Serin<sup>64</sup>, L. Serkin<sup>66a,66b</sup>, M. Sessa<sup>60a</sup>, H. Severini<sup>127</sup>, S. Sevova<sup>152</sup>, F. Sforza<sup>55b,55a</sup>, A. Sfyrla<sup>54</sup>, E. Shabalina<sup>53</sup>, J. D. Shahinian<sup>135</sup>, N. W. Shaikh<sup>45a,45b</sup>, D. Shaked Renous<sup>178</sup>, L. Y. Shan<sup>15a</sup>, M. Shapiro<sup>18</sup>, A. Sharma<sup>36</sup>, A. S. Sharma<sup>1</sup>, P. B. Shatalov<sup>123</sup>, K. Shaw<sup>155</sup>, S. M. Shaw<sup>100</sup>, M. Shehade<sup>178</sup>, Y. Shen<sup>127</sup>, P. Sherwood<sup>94</sup>, L. Shi<sup>94</sup>, C. O. Shimmin<sup>181</sup>, Y. Shimogama<sup>177</sup>, M. Shimojima<sup>115</sup>, J. D. Shinner<sup>93</sup>, I. P. J. Shipsey<sup>133</sup>, S. Shirabe<sup>163</sup>, M. Shiyakova<sup>79</sup>, b, J. Shlomi<sup>178</sup>, M. J. Shochet<sup>37</sup>, J. Shojaii<sup>104</sup>, D. R. Shope<sup>153</sup>, S. Shrestha<sup>126</sup>, E. M. Shrif<sup>33f</sup>, M. J. Shroff<sup>174</sup>, E. Shulga<sup>178</sup>, P. Sicho<sup>139</sup>, A. M. Sickles<sup>171</sup>, E. Sideras Haddad<sup>33f</sup>, O. Sidiropoulou<sup>36</sup>, A. Sidoti<sup>23b,23a</sup>, F. Siegert<sup>48</sup>, Dj. Sijacki<sup>16</sup>, M. V. Silva Oliveira<sup>36</sup>, S. B. Silverstein<sup>45</sup>, S. Simion<sup>64</sup>, R. Simoniello<sup>36</sup>, S. Simsek<sup>12b</sup>, P. Sinervo<sup>165</sup>, V. Sinetckii<sup>112</sup>, S. Singh<sup>151</sup>, S. Sinha<sup>33f</sup>, M. Sioli<sup>23b,23a</sup>, I. Siral<sup>130</sup>, S. Yu. Sivoklokov<sup>112</sup>, J. Sjölin<sup>45a,45b</sup>, A. Skaf<sup>53</sup>, E. Skorda<sup>96</sup>, P. Skubic<sup>127</sup>, M. Slawinska<sup>84</sup>, K. Sliwa<sup>168</sup>, V. Smakhtin<sup>178</sup>, B. H. Smart<sup>142</sup>, J. Smiesko<sup>141</sup>, S. Yu. Smirnov<sup>111</sup>, Y. Smirnov<sup>111</sup>, L. N. Smirnova<sup>112</sup>, T. O. Smirnova<sup>96</sup>, E. A. Smith<sup>37</sup>, H. A. Smith<sup>133</sup>, M. Smizanska<sup>89</sup>, K. Smolek<sup>140</sup>, A. Smykiewicz<sup>84</sup>, A. A. Snesarev<sup>110</sup>, H. L. Snoek<sup>119</sup>, I. M. Snyder<sup>130</sup>, S. Snyder<sup>29</sup>, R. Sobie<sup>174</sup>, aa, A. Soffer<sup>160</sup>, A. Søgaard<sup>50</sup>,



Comput Softw Big Sci (2022) 6:3 Page 29 of 35 3

F. Sohns<sup>53</sup>, C. A. Solans Sanchez<sup>36</sup>, E. Yu. Soldatov<sup>111</sup>, U. Soldevila<sup>172</sup>, A. A. Solodkov<sup>122</sup>, S. Solomon<sup>52</sup>, A. Soloshenko<sup>79</sup>, O. V. Solovyanov<sup>122</sup>, V. Solovyev<sup>136</sup>, P. Sommer<sup>148</sup>, H. Son<sup>168</sup>, W. Y. Song 166b, A. Sopczak 140, A. L. Sopio 4, F. Sopkova 8b, S. Sottocornola 70a,70b, R. Soualah 66a,66c, R. Soualah 66a,66c A. M. Soukharev<sup>121b,121a</sup>, Z. Soumaimi<sup>35f</sup>, D. South<sup>46</sup>, S. Spagnolo<sup>67a,67b</sup>, M. Spalla<sup>114</sup>, M. Spangenberg<sup>176</sup>, F. Spanò<sup>93</sup>, D. Sperlich<sup>52</sup>, T. M. Spieker<sup>61a</sup>, G. Spigo<sup>36</sup>, M. Spina<sup>155</sup>, D. P. Spiteri<sup>57</sup>, M. Spousta<sup>141</sup>, A. Stabile<sup>68a,68b</sup>, B. L. Stamas<sup>120</sup>, R. Stamen<sup>61a</sup>, M. Stamenkovic<sup>119</sup>, A. Stampekis<sup>21</sup>, E. Stanecka<sup>84</sup>, B. Stanislaus<sup>133</sup>, M. M. Stanitzki<sup>46</sup>, M. Stankaityte<sup>133</sup>, B. Stapf<sup>119</sup>, E. A. Starchenko<sup>122</sup>, G. H. Stark<sup>144</sup>, J. Stark<sup>101</sup>, P. Staroba<sup>139</sup>, P. Starovoitov<sup>61a</sup>, S. Stärz<sup>103</sup>, R. Staszewski<sup>84</sup>, G. Stavropoulos<sup>44</sup>, P. Steinberg<sup>29</sup>, A. L. Steinhebel<sup>130</sup>, B. Stelzer<sup>151,166a</sup>, H. J. Stelzer<sup>137</sup>, O. Stelzer-Chilton<sup>166a</sup>, H. Stenzel<sup>56</sup>, T. J. Stevenson<sup>155</sup>, G. A. Stewart<sup>36</sup>, M. C. Stockton<sup>36</sup>, G. Stoicea<sup>27b</sup>, M. Stolarski<sup>138</sup>, S. Stonjek<sup>114</sup>, A. Straessner<sup>48</sup>, J. Strandberg 153 D, S. Strandberg 45a, 45b D, M. Strauss 127 D, T. Strebler 101 D, P. Strizenec 28b D, R. Ströhmer 175 D, D. M. Strom<sup>130</sup>, L. R. Strom<sup>46</sup>, R. Stroynowski<sup>42</sup>, A. Strubig<sup>45a,45b</sup>, S. A. Stucci<sup>29</sup>, B. Stugu<sup>17</sup>, J. Stupak<sup>127</sup>, N. A. Styles<sup>46</sup>, D. Su<sup>152</sup>, W. Su<sup>60d,147,60c</sup>, X. Su<sup>60a</sup>, N. B. Suarez<sup>137</sup>, V. V. Sulin<sup>110</sup>, M. J. Sullivan<sup>90</sup>, D. M. S. Sultan<sup>54</sup>, S. Sultansoy<sup>4c</sup>, T. Sumida<sup>85</sup>, S. Sun<sup>105</sup>, S. Sun<sup>179</sup>, X. Sun<sup>100</sup>, C. J. E. Suster<sup>156</sup>, M. R. Sutton<sup>155</sup>, M. Svatos<sup>139</sup>, M. Swiatlowski<sup>166a</sup>, S. P. Swift<sup>2</sup>, T. Swirski<sup>175</sup>, A. Sydorenko<sup>99</sup>, I. Sykora<sup>28a</sup> M. Sykora<sup>141</sup>, T. Sykora<sup>141</sup>, D. Ta<sup>99</sup>, K. Tackmann<sup>46,x</sup>, A. Taffard<sup>169</sup>, R. Tafirout<sup>166a</sup>, E. Tagiev<sup>122</sup>, R. H. M. Taibah<sup>134</sup>, R. Takashima<sup>86</sup>, K. Takeda<sup>82</sup>, T. Takeshita<sup>149</sup>, E. P. Takeva<sup>50</sup>, Y. Takubo<sup>81</sup>, M. Talby<sup>101</sup>, A. A. Talyshev<sup>121b,121a</sup>, K. C. Tam<sup>62b</sup>, N. M. Tamir<sup>160</sup>, J. Tanaka<sup>162</sup>, R. Tanaka<sup>64</sup>, S. Tapia Araya<sup>171</sup>, S. Tapprogge<sup>99</sup>, A. Tarek Abouelfadl Mohamed<sup>106</sup>, S. Tarem<sup>159</sup>, K. Tariq<sup>60b</sup>, G. Tarna<sup>27b</sup>, G. F. Tartarelli<sup>68a</sup>, P. Tas <sup>141</sup>, M. Tasevsky <sup>139</sup>, E. Tassi <sup>41b,41a</sup>, G. Tateno <sup>162</sup>, Y. Tayalati <sup>35f</sup>, G. N. Taylor <sup>104</sup>, W. Taylor <sup>166b</sup>, H. Teagle<sup>90</sup>, A. S. Tee<sup>89</sup>, R. Teixeira De Lima<sup>152</sup>, P. Teixeira-Dias<sup>93</sup>, H. Ten Kate<sup>36</sup>, J. J. Teoh<sup>119</sup>, K. Terashi<sup>162</sup>, J. Terron 98 , S. Terzo 14 , M. Testa 51 , R. J. Teuscher 165, aa , N. Themistokleous 50 , T. Theveneaux-Pelzer 19 , D. W. Thomas<sup>93</sup>, J. P. Thomas<sup>21</sup>, E. A. Thompson<sup>46</sup>, P. D. Thompson<sup>21</sup>, E. Thomson<sup>135</sup>, E. J. Thorpe<sup>92</sup>, V. O. Tikhomirov<sup>110</sup>, ag, Yu. A. Tikhonov<sup>121b</sup>, S. Timoshenko<sup>111</sup>, P. Tipton<sup>181</sup>, S. Tisserant<sup>101</sup>, S. H. Tlou<sup>33f</sup>, A. Tnourji<sup>38</sup>, K. Todome<sup>23b,23a</sup>, S. Todorova-Nova<sup>141</sup>, S. Todt<sup>48</sup>, J. Tojo<sup>87</sup>, S. Tokár<sup>28a</sup>, K. Tokushuku<sup>81</sup>, E. Tolley<sup>126</sup>, R. Tombs<sup>32</sup>, M. Tomoto<sup>81,116</sup>, L. Tompkins<sup>152</sup>, P. Tornambe<sup>102</sup>, E. Torrence<sup>130</sup>, H. Torres<sup>48</sup>, E. Torró Pastor<sup>172</sup>, M. Toscani<sup>30</sup>, C. Tosciri<sup>37</sup>, J. Toth<sup>101,z</sup>, D. R. Tovey<sup>148</sup>, A. Traeet<sup>17</sup> C. J. Treado<sup>124</sup>, T. Trefzger<sup>175</sup>, A. Tricoli<sup>29</sup>, I. M. Trigger<sup>166a</sup>, S. Trincaz-Duvoid<sup>134</sup>, D. A. Trischuk<sup>173</sup>, W. Trischuk<sup>165</sup>, B. Trocmé<sup>58</sup>, A. Trofymov<sup>64</sup>, C. Troncon<sup>68a</sup>, F. Trovato<sup>155</sup>, L. Truong<sup>33c</sup>, M. Trzebinski<sup>84</sup>, A. Trzupek<sup>84</sup>, F. Tsai<sup>46</sup>, P. V. Tsiareshka<sup>107</sup>, ae, A. Tsirigotis<sup>161</sup>, V., V. Tsiskaridze<sup>154</sup>, E. G. Tskhadadze<sup>158</sup>, M. Tsopoulou<sup>161</sup>, I. I. Tsukerman<sup>123</sup>, V. Tsulaia<sup>18</sup>, S. Tsuno<sup>81</sup>, D. Tsybychev<sup>154</sup>, Y. Tu<sup>62b</sup>, A. Tudorache<sup>27b</sup>, V. Tudorache<sup>27b</sup>, A. N. Tuna<sup>36</sup>, S. Turchikhin<sup>79</sup>, D. Turgeman<sup>178</sup>, I. Turk Cakir<sup>4b,t</sup>, R. J. Turner<sup>21</sup>, R. Turra<sup>68a</sup>, P. M. Tuts<sup>39</sup>, S. Tzamarias<sup>161</sup>, P. Tzanis<sup>10</sup>, E. Tzovara<sup>99</sup>, K. Uchida<sup>162</sup>, F. Ukegawa<sup>167</sup>, G. Unal<sup>36</sup>, M. Unal<sup>11</sup>, A. Undrus<sup>29</sup>, G. Unel<sup>169</sup>, F. C. Ungaro<sup>104</sup>, K. Uno<sup>162</sup>, J. Urban<sup>28b</sup>, P. Urquijo<sup>104</sup>, G. Usai<sup>8</sup>, Z. Uysal<sup>12d</sup>, V. Vacek<sup>140</sup>, B. Vachon<sup>103</sup>, K. O. H. Vadla<sup>132</sup>, T. Vafeiadis<sup>36</sup>, C. Valderanis<sup>113</sup>, E. Valdes Santurio<sup>45a,45b</sup>, M. Valente<sup>166a</sup>, S. Valentinetti<sup>23b,23a</sup>, A. Valero<sup>172</sup>, L. Valéry<sup>46</sup>, R. A. Vallance<sup>21</sup>, R. A. Vallance<sup>21</sup> A. Vallier<sup>36</sup>, J. A. Valls Ferrer<sup>172</sup>, T. R. Van Daalen<sup>14</sup>, P. Van Gemmeren<sup>6</sup>, S. Van Stroud<sup>94</sup>, I. Van Vulpen<sup>119</sup>, M. Vanadia<sup>73a,73b</sup>, W. Vandelli<sup>36</sup>, M. Vandenbroucke<sup>143</sup>, E. R. Vandewall<sup>128</sup>, D. Vannicola<sup>72a,72b</sup>, R. Vari<sup>72a</sup>, R. Vari<sup>72a</sup> E. W. Varnes<sup>7</sup>, C. Varni<sup>55b,55a</sup>, T. Varol<sup>157</sup>, D. Varouchas<sup>64</sup>, K. E. Varvell<sup>156</sup>, M. E. Vasile<sup>27b</sup>, L. Vaslin<sup>38</sup>, G. A. Vasquez 174, F. Vazeille 18, D. Vazquez Furelos 14, T. Vazquez Schroeder 16, J. Veatch 100, V. Vecchio 100, M. J. Veen 119 , L. M. Veloce 165 , F. Veloso 138a, 138c , S. Veneziano 72a , A. Ventura 67a, 67b , A. Verbytskyi 114 , M. Verducci<sup>71a,71b</sup>, C. Vergis<sup>24</sup>, W. Verkerke<sup>119</sup>, A. T. Vermeulen<sup>119</sup>, J. C. Vermeulen<sup>119</sup>, C. Vermieri<sup>152</sup>, P. J. Verschuuren<sup>93</sup>, M. L. Vesterbacka<sup>124</sup>, M. C. Vetterli<sup>151</sup>, ak, N. Viaux Maira<sup>145d</sup>, O. E. Vickey Boeriu<sup>148</sup>, G. H. A. Viehhauser<sup>133</sup>, L. Vigani<sup>61b</sup>, M. Villa<sup>23b,23a</sup>, M. Villaplana Perez<sup>172</sup>, E. M. Villhauer<sup>50</sup>, E. Vilucchi<sup>51</sup>, M. G. Vincter<sup>34</sup>, G. S. Virdee<sup>21</sup>, A. Vishwakarma<sup>50</sup>, C. Vittori<sup>23b,23a</sup>, I. Vivarelli<sup>155</sup>, V. Vladimirov<sup>176</sup>, M. Vogel<sup>180</sup>, P. Vokac<sup>140</sup>, J. Von Ahnen<sup>46</sup>, S. E. von Buddenbrock<sup>33f</sup>, E. Von Toerne<sup>24</sup>, V. Vorobel<sup>141</sup>, K. Vorobev<sup>111</sup>, M. Vos<sup>172</sup>, J. H. Vossebeld<sup>90</sup>, M. Vozak<sup>100</sup>, N. Vranjes<sup>16</sup>, M. Vranjes Milosavljevic  $^{16}$  D, V. Vrba $^{140,\dagger}$ , M. Vreeswijk  $^{119}$  D, N. K. Vu $^{101}$  D, R. Vuillermet  $^{36}$  D, I. Vukotic  $^{37}$  D, S. Wada $^{167}$  D, C. Wagner  $^{102}$ , P. Wagner  $^{24}$  D, W. Wagner  $^{180}$  D, S. Wahdan  $^{180}$  D, H. Wahlberg  $^{88}$  D, R. Wakasa  $^{167}$  D, V. M. Walbrecht<sup>114</sup>, J. Walder<sup>142</sup>, R. Walker<sup>113</sup>, S. D. Walker<sup>93</sup>, W. Walkowiak<sup>150</sup>, V. Wallangen<sup>45a,45b</sup>, A. M. Wang<sup>59</sup>, A. Z. Wang<sup>179</sup>, C. Wang<sup>60a</sup>, C. Wang<sup>60c</sup>, H. Wang<sup>18</sup>, J. Wang<sup>62a</sup>, P. Wang<sup>42</sup>, R.-J. Wang<sup>99</sup>, R. Wang<sup>60a</sup>, R. Wang<sup>120</sup>, S. M. Wang<sup>157</sup>, S. Wang<sup>60b</sup>, T. Wang<sup>60a</sup>, W. T. Wang<sup>60a</sup>, W. X. Wang<sup>60a</sup>, Y. Wang<sup>60a</sup>, Z. Wang<sup>105</sup>, C. Wanotayaroj<sup>36</sup>, A. Warburton<sup>103</sup>, C. P. Ward<sup>32</sup>, R. J. Ward<sup>21</sup>, N. Warrack<sup>57</sup>,

3 Page 30 of 35 Comput Softw Big Sci (2022) 6:3

A. T. Watson<sup>21</sup>, M. F. Watson<sup>21</sup>, G. Watts<sup>147</sup>, B. M. Waugh<sup>94</sup>, A. F. Webb<sup>11</sup>, C. Weber<sup>29</sup>, M. S. Weber<sup>20</sup>, S. A. Weber<sup>34</sup>, S. M. Weber<sup>61a</sup>, C. Wei<sup>60a</sup>, Y. Wei<sup>133</sup>, A. R. Weidberg<sup>133</sup>, J. Weingarten<sup>47</sup>, M. Weirich<sup>99</sup>, C. Weiser<sup>52</sup>, P. S. Wells<sup>36</sup>, T. Wenaus<sup>29</sup>, B. Wendland<sup>47</sup>, T. Wengler<sup>36</sup>, S. Wenig<sup>36</sup>, N. Wermes<sup>24</sup>, M. Wessels<sup>61a</sup>, T. D. Weston<sup>20</sup>, K. Whalen<sup>130</sup>, A. M. Wharton<sup>89</sup>, A. S. White<sup>105</sup>, A. White<sup>8</sup>, M. J. White<sup>1</sup> D. Whiteson 169, W. Wiedenmann 779, C. Wiel 8, M. Wielers 142, N. Wieseotte 99, C. Wiglesworth 0, L. A. M. Wilk-Fuchs<sup>52</sup>, H. G. Wilkens<sup>36</sup>, L. J. Wilkins<sup>93</sup>, D. M. Williams<sup>39</sup>, H. H. Williams<sup>135</sup>, S. Williams<sup>32</sup> S. Willocq<sup>102</sup>, P. J. Windischhofer<sup>133</sup>, I. Wingerter-Seez<sup>5</sup>, F. Winklmeier<sup>130</sup>, B. T. Winter<sup>52</sup>, M. Wittgen<sup>152</sup>, M. Wobisch<sup>95</sup>, A. Wolf<sup>99</sup>, R. Wölker<sup>133</sup>, J. Wollrath<sup>52</sup>, M. W. Wolter<sup>84</sup>, H. Wolters<sup>138a,138c</sup>, V. W. S. Wong<sup>173</sup>, A. F. Wongel<sup>46</sup>, N. L. Woods<sup>144</sup>, S. D. Worm<sup>46</sup>, B. K. Wosiek<sup>84</sup>, K. W. Woźniak<sup>84</sup>, K. Wraight<sup>57</sup>, K. Wraight<sup>57</sup> J. Wu<sup>15a,15d</sup>, S. L. Wu<sup>179</sup>, X. Wu<sup>54</sup>, Y. Wu<sup>60a</sup>, Z. Wu<sup>143</sup>, J. Wuerzinger<sup>133</sup>, T. R. Wyatt<sup>100</sup>, B. M. Wynne<sup>50</sup>, S. Xella<sup>40</sup>, J. Xiang<sup>62c</sup>, X. Xiao<sup>105</sup>, X. Xie<sup>60a</sup>, I. Xiotidis<sup>155</sup>, D. Xu<sup>15a</sup>, H. Xu<sup>60a</sup>, H. Xu<sup>60a</sup>, L. Xu<sup>60a</sup> R. Xu<sup>135</sup>, T. Xu<sup>143</sup>, W. Xu<sup>105</sup>, Y. Xu<sup>15b</sup>, Z. Xu<sup>60b</sup>, Z. Xu<sup>152</sup>, B. Yabsley<sup>156</sup>, S. Yacoob<sup>33a</sup>, D. P. Yallup<sup>94</sup>, N. Yamaguchi<sup>87</sup>, Y. Yamaguchi<sup>163</sup>, M. Yamatani<sup>162</sup>, H. Yamauchi<sup>167</sup>, T. Yamazaki<sup>18</sup>, Y. Yamazaki<sup>82</sup>, J. Yan<sup>60c</sup>, Z. Yan<sup>25</sup>, H. J. Yang<sup>60c</sup>, <sup>60d</sup>, H. T. Yang<sup>18</sup>, S. Yang<sup>60a</sup>, T. Yang<sup>62c</sup>, X. Yang<sup>60a</sup>, X. Yang <sup>15a</sup>, Y. Yang <sup>162</sup>, Z. Yang <sup>105</sup>, <sup>60a</sup>, W-M. Yao <sup>18</sup>, Y. C. Yap <sup>46</sup>, H. Ye <sup>15c</sup>, J. Ye <sup>42</sup>, S. Ye <sup>29</sup>, I. Yeletskikh <sup>79</sup>, M. R. Yexley <sup>89</sup>, P. Yin <sup>39</sup>, K. Yorita <sup>177</sup>, K. Yoshihara <sup>78</sup>, C. J. S. Young <sup>36</sup>, C. Young <sup>152</sup>, R. Yuan<sup>60b,i</sup>, X. Yue<sup>61a</sup>, M. Zaazoua<sup>35f</sup>, B. Zabinski<sup>84</sup>, G. Zacharis<sup>10</sup>, E. Zaffaroni<sup>54</sup>, J. Zahreddine<sup>101</sup>, A. M. Zaitsev<sup>122,af</sup>, T. Zakareishvili<sup>158b</sup>, N. Zakharchuk<sup>34</sup>, S. Zambito<sup>36</sup>, D. Zanzi<sup>52</sup>, S. V. Zeißner<sup>47</sup> C. Zeitnitz<sup>180</sup>, G. Zemaityte<sup>133</sup>, J. C. Zeng<sup>171</sup>, O. Zenin<sup>122</sup>, T. Ženiš<sup>28a</sup>, S. Zenz<sup>92</sup>, S. Zerradi<sup>35a</sup>, D. Zerwas<sup>64</sup>, M. Zgubič<sup>133</sup>, B. Zhang<sup>15c</sup>, D. F. Zhang<sup>15b</sup>, G. Zhang<sup>15b</sup>, J. Zhang<sup>6</sup>, K. Zhang<sup>15a</sup>, L. Zhang<sup>15c</sup>, L. Zhang<sup>60a</sup>, M. Zhang<sup>171</sup>, R. Zhang<sup>179</sup>, S. Zhang<sup>105</sup>, X. Zhang<sup>60c</sup>, X. Zhang<sup>60b</sup>, Z. Zhang<sup>64</sup>, P. Zhao<sup>49</sup>, Y. Zhao<sup>144</sup>, Z. Zhao<sup>60a</sup>, A. Zhemchugov<sup>79</sup>, Z. Zheng<sup>105</sup>, D. Zhong<sup>171</sup>, B. Zhou<sup>105</sup>, C. Zhou<sup>179</sup>, H. Zhou<sup>7</sup>, M. Zhou<sup>154</sup>, N. Zhou<sup>60c</sup>, Y. Zhou<sup>7</sup>, C. G. Zhu<sup>60b</sup>, C. Zhu<sup>15a,15d</sup>, H. L. Zhu<sup>60a</sup>, H. Zhu<sup>15a</sup>, J. Zhu<sup>105</sup>, Y. Zhu<sup>60a</sup>, X. Zhuang<sup>15a</sup>, K. Zhukov<sup>110</sup>, V. Zhulanov<sup>121b,121a</sup>, D. Zieminska<sup>65</sup>, N. I. Zimine<sup>79</sup>, S. Zimmermann<sup>52,†</sup>, Z. Zinonos<sup>114</sup>, M. Ziolkowski<sup>150</sup>, L. Živković<sup>16</sup>, A. Zoccoli<sup>23b,23a</sup>, T. G. Zorbas <sup>148</sup>, R. Zou<sup>37</sup>, W. Zou<sup>39</sup>, L. Zwalinski<sup>36</sup>

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Comput Softw Big Sci (2022) 6:3 Page 31 of 35

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3 Page 32 of 35 Comput Softw Big Sci (2022) 6:3

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Comput Softw Big Sci (2022) 6:3 Page 33 of 35

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3 Page 34 of 35 Comput Softw Big Sci (2022) 6:3

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Comput Softw Big Sci (2022) 6:3 Page 35 of 35 3

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