# The size of BDDs and other data structures in temporal logics model checking 

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#### Abstract

Temporal Logic Model Checking is a verification method in which we describe a system, the model, and then we verify whether important properties, expressed in a temporal logic formula, hold in the system. Many Model Checking tools employ BDDs or some other data structure to represent sets of states. It has been empirically observed that the BDDs used in these algorithms may grow exponentially as the model and formula increase in size. We formally prove that no kind of data structure of polynomial size can represent the set of valid initial states for all models and all formulae. This result holds for all data structures where a state can be checked in polynomial time. Therefore, it holds not only for all types of BDDs regardless of variable ordering, but also for more powerful data structures, such as RBCs, MTBDDs, ADDs and SDDs. Thus, the size explosion of BDDs is not a limit of these specific data representation structures, but is unavoidable: every formalism used in the same way would lead to an exponential size blow up.


Keywords: Model Checking, Complexity, Compilability, Succinctness, BDDs.

## I. Introduction

Temporal Logic Model Checking [1] is a verification method for discrete systems. In a nutshell, the system, often called the model, is described by the possible transitions of its components, while the properties to verify are encoded in a temporal modal logic. It is used, for example, for the verification of protocols and hardware circuits [2]. Many tools, called model checkers, have been developed to this aim. The most famous ones are SPIN [3], SMV [4] (with its well known implementation NuSMV [5]) and PRISM [6].

Many languages can be used to express the model; the most widespread ones are Promela and SMV. Two temporal logics are mainly used to
define the specification: CTL [1] and LTL [7]. A detailed survey of complexity results on temporal logic model checking can be found in [8].

In many cases, the inputs of model checking problems can be processed in different ways. As an example, if the same system (encoded as a model) has to be checked against different properties (each expressed by a formula), it makes sense to spend more time on the system if it allows checking each property more efficiently. The converse applies when the same property has to be checked against different systems. A previous article by the same authors of the present one [9] analyzes these cases.

In this article, we consider the initial state problem: given a formula, a model and a state, is there a run of the model that starts from the given state and that satisfies the formula? We prove that complexity does not decrease even if both model and formula undergo a preprocessing step that produces a polynomially-sized result. In a way, this shows that the choice of the initial state can encode the whole complexity of model checking.

This result allows answering a long-time standing question in Symbolic Model Checking [4], [10]. It has been observed that the BDDs [11], [12], [13], [14] used by SMV and other Symbolic Model Checking systems may become exponentially large. This article formally proves that this phenomenon is not limited to BDDs, but holds for every possible form of representation of sets of states in which a state can be checked for membership in polynomial time.

Some specific Boolean functions have already been shown not to be representable with polynomial-size BDDs [15]. The results in this article proves that this is the case for every data
structure of this kind aimed at representing sets of states in symbolic model checking. It holds for all decision diagrams representing integervalue functions whose evaluation problem is in the polynomial hierarchy. Therefore it also applies to all current, and future, enhancements of BDDs, such as BMD and *BMD [16], RBCs [17], MTBDDs [18], ADDs [19] and SDD [20].

## II. DEFINITIONS

In this section we briefly introduce the terminology and important properties of LTL and CTL temporal model checking [1], [7] and automated planning formalism STRIPS [21]. For more details we refer the reader to the original articles.

## A. Model checking

In this section, we briefly recall the basic definitions about model checking that are needed in the rest of the paper. We follow the notation used by Sistla and Clarke [22]. Only CTL formulae $E F \phi$ (or equivalently, LTL formulae $F \phi$ ) are used in this article, meaning that the propositional formula $\phi$ is true in some future time point. The meaning and formal definition of the other operators can be found in any of a number of articles on the topic [1], [7].

The semantics is based on Kripke structures. Given a set of atomic propositions, a Kripke structure is a tuple $\langle Q, R, \ell, I\rangle$, where $Q$ is a set of states, $R$ is a binary relation over states (the transition relation), $\ell$ is a function from states to atomic propositions (it labels each state with the atomic propositions that are true in that state), $I$ is a set of the possible initial states. A run of a Kripke structure is called a Kripke model: an infinite sequence of states such that $s_{i} R s_{i+1}$ for each pair of consecutive states $s_{i}$, $s_{i+1}$ in the sequence. The truth of formulas in such a sequence $\left[s_{0}, s_{1}, \ldots\right]$ is defined as: atomic propositions are evaluated according to the initial state of the sequence (in this case $\ell\left(s_{0}\right)$ ); nonmodal connectives are evaluated as usual; $F \phi$ is true if there exists $i$ such that $\phi$ is true in [ $\left.s_{i}, s_{i+1} \ldots\right]$. A formula $E F \phi$ is true in a Kripke structure, which may have several Kripke models (runs), if $\phi$ is true in at least one of them.

In formal verification, the behavior of a system is represented as a Kripke structure, and the
property to check as a modal formula. Checking the structure against the formula tells whether the system satisfies the property. Since Kripke structure is usually called a "model" (which is in fact very different from a Kripke model, which is only a possible run), this problem is called Model Checking.

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In most applications, the Kripke structure is not specified by a tuple $\langle Q, R, \ell, I\rangle$ but in a more compact form. More precisely, the set of states $Q$ is the set of all possible interpretations over a given alphabet of variables $V$ and the initial states and the allowed transitions between states are specified by formulas $L$ and $\varrho$; the latter is a formula over variables $V \cup V^{\prime}$, where $V^{\prime}$ is a new set of variables in one-to-one relation with $V$; These variables represent the state after the change.

Such a triple $(V, L, \varrho)$ is called a model, and represents a Kripke structure $\langle Q, R, \ell, I\rangle$ in which $Q$ is the set of all possible interpretations over $V, \ell$ is the identity function, $I$ is the set of interpretations satisfying $L$, and $s R s^{\prime}$ holds if the formula $\varrho$ is satisfied by the model $s \cup s^{\prime}\left[V / V^{\prime}\right]$ $\left(s \cup s^{\prime}\left[V / V^{\prime}\right] \models \varrho\right)$, where $s^{\prime}\left[V / V^{\prime}\right]$ denotes the state obtained by substituting each variable $x \in V$ with its corresponding variable $x^{\prime} \in V^{\prime}$.

As a result, states are interpretations over $V$, and a Kripke model is an infinite sequence of states such that the first one satisfies $L$ and each consecutive pair satisfies $\varrho$; this sequence is also called a run of the model.

Problems like reachability (where the formula is $E F \phi$ ) can be checked in time polynomial in the size of the Kripke structure, but a model can represent a Kripke structure of size exponentially larger than it. In particular, a model can have an exponential number of initial states satisfying $F \phi$, but this alone does not rule out the possibility that the set of such states can be represented in a compact form. As a limit example, $E F\left(p_{1} \vee \cdots \vee p_{n}\right)$
holds in an exponential number of initial states in the model $\bigwedge p_{i} \equiv p_{i}^{\prime}$, yet the formula $p_{1} \vee \ldots \vee p_{n}$ itself provides a very compact representation of the set of such states.

Usually, models are specified as compositions of modules. Each module is a sort of "minimodel": it is like a model but some variables are local to it while the others are shared with the other modules. If $V^{L}$ is the set of local variables of a module and $V^{S}$ the set of shared variables, which is common to all modules, then the module is $M=\left(V^{L} \cup V^{S}, L, \varrho\right)$. A model can be then obtained as the composition of a number of modules, each set of local variables being disjoint from all others.

A set of modules $M_{1}, \ldots, M_{k}$ can be composed, forming a single model, by using the following set of variables and initial state formula:

$$
\begin{aligned}
V & =V^{S} \cup \bigcup_{i=1, \ldots, k} V_{i}^{L} \\
L & =\bigwedge_{i=1, \ldots, k} L_{i}
\end{aligned}
$$

The transition formulas can be composed according to a synchronous or an (interleaved) asynchronous behavior. In the synchronous composition, every module changes state at every time point: $\varrho=\bigwedge_{i=1, \ldots, k} \varrho_{i}$.

In the interleaved asynchronous composition, only one module at time can change state. There is however no specified rule as for which module changes at each time, so for example a module can change state three times in a row. This is formalized as follows:

$$
\begin{equation*}
\varrho=\bigvee_{i=1, \ldots, k}\left[\varrho_{i} \wedge \bigwedge_{j=1, \ldots, i-1, i+1, \ldots, k} V_{j}^{L} \equiv V_{j}^{\prime L}\right] \tag{1}
\end{equation*}
$$

This formula is satisfied if one of its subformulas is satisfied. Therefore, when the subformula of index $i$ changes state it forces all local variables of modules $M_{1}, \ldots, M_{i-1}, M_{i+1}, \ldots, M_{k}$ to maintain their value, while the local variables of module $M_{i}$ and the shared variables change value as specified by $\varrho_{i}$.

Definition 1 (Initial state problem): Given a model $M$, a formula $\phi$ and an initial state, is
there a run of $M$ starting from that state and satisfying $\phi$ in some state?

This problem can be recast into the existence of a propositional formula $I$ satisfied by exactly one propositional interpretation and such that $I \wedge E F \phi$ holds in the model.

The complexity of temporal logic model checking has been extensively studied in the literature and a good survey can be found in [8].

## B. STRIPS

We use the STRIPS planning formalism [21] as the source problem for our reduction, because of the structural similarities among STRIPS planning and model checking.

A STRIPS instance (or STRIPS domain) is a 4-tuple $\langle\mathcal{P}, \mathcal{O}, \mathcal{I}, \mathcal{G}\rangle$, where $\mathcal{P}$ is the set of conditions, $\mathcal{O}$ is the set of operators, $\mathcal{I}$ is the initial state, and $\mathcal{G}$ is the goal. The conditions are facts that can be true or false. A state $s$ is a set of conditions, and represents the state of the world at a certain time point: conditions in $s$ represent facts that are true in the world, those not in $s$ represent facts currently false.

The initial state is a state, thus a set of conditions. The goal is specified by a set of conditions that should be achieved, and another set specifying which conditions should not be made true. Thus, a goal $\mathcal{G}$ is a pair $\langle\mathcal{M}, \mathcal{N}\rangle$, where $\mathcal{M}$ is the set of conditions that should be made true and $\mathcal{N}$ is the set of conditions that should be made false.

The operators are actions that can be performed to achieve the goal. Each operator is a 4-tuple $\langle\phi, \eta, \alpha, \beta\rangle$, where $\phi, \eta, \alpha$, and $\beta$ are sets of conditions. An operator can be executed if and only if the conditions in $\phi$ are true and those in $\eta$ are false, and its execution makes the conditions in $\alpha$ true, and those in $\beta$ false. The conditions in $\phi$ and $\eta$ are called the positive and negative preconditions of the operator. The conditions in $\alpha$ and $\beta$ are called the positive and negative effects or postconditions of the operator. Given a state $s$ and an operator $o=\langle\phi, \eta, \alpha, \beta\rangle, r(s, o)$ denotes the result of applying action $o$ in state $s$ and is defined as follows:
$r(s,\langle\phi, \eta, \alpha, \beta\rangle)=\left\{\begin{array}{c}(s \cup \alpha) \backslash \beta \\ \text { if } \phi \subseteq s \text { and } \eta \cap s=\emptyset \\ \text { undefined otherwise }\end{array}\right.$

A plan for $\langle\mathcal{P}, \mathcal{O}, \mathcal{I}, \mathcal{G}\rangle$ is a sequence of operators $o_{1}, o_{2}, \ldots, o_{k}$ such that $s=$ $\left.\left.r\left(r\left(\ldots, r\left(\mathcal{I}, o_{1}\right)\right), \ldots\right), o_{i-1}\right), o_{i}\right)$ is defined for all $i \in\{1, \ldots, k\}$ and it holds that $\mathcal{M} \subseteq s$ and $\mathcal{N} \cap s=\emptyset$.

The plan-existence problem for STRIPS can be formulated as follows: Given a planning instance $\langle\mathcal{P}, \mathcal{O}, \mathcal{I}, \mathcal{G}\rangle$, decide whether it has a plan. For a more detailed presentation of STRIPS and its properties we refer the reader to the article by Fikes and Nilsson [21]. In this article, we consider the restrictions STRIPS $_{k}$ where the goal is composed of a single positive condition and, for each operator, the maximum number of preconditions is $k$ and there are at most 2 postconditions. Plan existence is PSPACE-hard for every $k \geq 2$, as proved by Bylander [23].

## III. Complexity and Compilability

This article employs the complexity class PSPACE, which is the set of decision problems that can be solved in space polynomial in the size of the input [24], [25]. The class $P$ is defined in the same way with polynomial time instead of space. The polynomial hierarchy [26] is a hierarchy of classes of decision problems whose basic class is P and it is completely included in PSPACE. It is generally assumed to be a proper hierarchy, that is for each $k$, level $k$ is strictly included in level $k+1$. The collapse of the polynomial hierarchy is considered very unlikely and many results in computational complexity are frequently stated as "The property holds unless the polynomial hierarchy collapse", such in [27]. We assume that the instances of a decision problem are strings built over a finite arbitrary alphabet $\Sigma$. The set of all strings over $\Sigma$ is denoted by $\Sigma^{*}$. The length of a string $x \in \Sigma^{*}$ is denoted by $\|x\|$. A language (decision problem) is defined as a set of strings.

We recall some definitions and results about the on-line complexity of problems [28]. This deals with problems that, as many do, have instances that can be naturally broken into two parts: one part known in advance and one part known only at run-time. An instance of such problems can be represented as a pair of strings rather than a single string. Therefore, the language corresponding to these decision problems is a language over
pairs of strings, i.e., a set of pairs of strings (rather than a set of strings). Formally, a language of pairs is a subset of $\Sigma^{*} \times \Sigma^{*}$, where $\times$ is the Cartesian product.

The difference between the first and second string of a pair is that the first is known in advance, meaning that more time can be spent on it. This could be useful to solve the problem faster when the second string comes to be known. The idea is that an algorithm can take advantage of the difference between the first and the second part of the input:
preprocessing phase:
elaborates only the first part of the input; since that part is known advance, this phase may take a long time;
online processing:
when the rest of the input arrives, the algorithm can use both it and the result of the preprocessing phase to solve the problem.
While the first phase is allowed to take more time than the second one, some constraints have to be put on it: we impose its result to be of polynomial size. Poly-size functions are introduced to this purpose: a function $f$ from tuples of strings to strings is called poly-size if there exists a polynomial $p$ such that, for all tuples of strings $\left\langle x_{1}, \ldots, x_{n}\right\rangle$, the inequality $\left\|f\left(\left\langle x_{1}, \ldots, x_{n}\right\rangle\right)\right\| \leq$ $p\left(\left\|x_{1}\right\|+\cdots+\left\|x_{n}\right\|\right)$ holds. We extend this definition to mixed tuples of strings and integers by considering each number $i$ as a string of length $i$; more details are in the article where poly-size functions have been introduced [28].

In the computational complexity framework, every function computable in polynomial time is also poly-size [29], but not vice versa. For example, the function that computes whether there exists a winning strategy for a generalized $N$ by $N$ version of checkers requires exponential time [30] to be computed but its output is either "yes" or "no". In spite of the long computing time, the produced value is always representable with a single bit.

This article uses classes that characterize the complexity of problems that can be expressed as languages of pairs, where the first input (the fixed part) is available in advance and the second one (the varying part) is only available at the last moment. The idea is that a class such as
$\mathbb{H} \leftrightarrow \mathrm{P}$ contains all problems that are in P after preprocessing the first part of the data, that is, neglecting the complexity of the preprocessing phase, and concentrating only on the complexity of what remains to be done after the preprocessing step. We constraint the preprocessing phase to produce a polynomially sized result.

In a similar way, the class $\| \rightsquigarrow$ PSPACE contains all problems that can be solved in polynomial space after the preprocessing phase is over. For technical reasons, we make the assumption that preprocessing can use not only the fixed part of the instance but also the size of the varying part. This will not be a problem because the impossibility of making a problem belong in a class will carry on to the case in which this size is not given [28].

Definition 2: The class $\| \nprec$ PSPACE contains all languages of pairs $S \subseteq \Sigma^{*} \times \Sigma^{*}$ such that there exist a poly-size function $f$ from strings and integers to strings and another language of pairs $S^{\prime} \in$ PSPACE such that, for all $\langle x, y\rangle \in S$, the following holds:

$$
\langle x, y\rangle \in S \quad \text { iff } \quad\langle f(x,\|y\|), y\rangle \in S^{\prime}
$$

The poly-size function $f$ of this definition represents the preprocessing phase: $f(x,\|y\|)$ is the result of preprocessing the first element of the data plus the size of the second element. Since $S^{\prime}$ is in PSPACE, the problem is in PSPACE given the result $f(x,\|y\|)$ of preprocessing. The preprocessing phase is allowed to know, in addition to $x$, the size of $y$ (i.e., $\|y\|)$ for technical reasons [28].

Every problem in PSPACE is also in $\mathbb{H} \leadsto$ PSPACE. Proving that a problem is also one of the most difficult ones in $\| \leftrightarrow$ PSPACE cannot be done with the usual definition of polynomial reduction. A new definition of reduction is necessary.

Definition 3: A nucomp reduction is a triple of functions $\left\langle f_{1}, f_{2}, g\right\rangle$, where $g$ is poly-time and $f_{1}$ and $f_{2}$ are poly-size. A problem $A$ is non-uniformly comp-reducible to a problem $B$ (denoted $A \leq_{\text {nucomp }} B$ ) iff there exists a nonuniform comp-reduction $\left\langle f_{1}, f_{2}, g\right\rangle$ such that, for every pair $\langle x, y\rangle$, the following holds:

$$
\begin{aligned}
& \langle x, y\rangle \in A \text { if and only if } \\
& \quad\left\langle f_{1}(x,\|y\|), g\left(f_{2}(x,\|y\|), y\right)\right\rangle \in B
\end{aligned}
$$

The rationale behind this formal definition is quite technical, but it intuitively means that the fixed part of $A$ is treated differently than the varying part. These reductions allows for a concept of hardness and completeness. More details and justifications can be found in [28].

Definition 4: A language of pairs $S$ Ih PSPACE-hard iff for all problems $A \in \| \leftrightarrow \mathrm{PSPACE}$, it is the case that $A \leq_{\text {nucomp }} S$. Moreover, $S$ is $\| \rightsquigarrow$ PSPACE-complete if $S$ is in $\| \rightsquigarrow$ PSPACE and is $\mathbb{H} \leadsto$ PSPACE-hard.

It can be proved that if a $\| \rightsquigarrow$ PSPACE-hard problem could be solved in polynomial time after preprocessing the fixed part, and this preprocessing phase obeys the above restriction of producing a polynomially-sized result, then the polynomial hierarchy collapses to its second level. The details of this result can be found in other articles [28], [31], but are unnecessary for the aim of this article; what matters is that, as already discussed, such a collapse is currently considered very unlikely by the computational complexity community.

We show a technique to derive a nucomp reduction from a (regular) poly-time reduction. More precisely, we present sufficient conditions allowing for a polynomial-time reduction to imply the existence of a nucomp reduction [31].

Let us assume that we know a polynomial reduction from problem $A$ to problem $B$, and we want to prove the nucomp-hardness of $B$. This can be accomplished by first proving some conditions on $A$, and then one condition on the reduction from $A$ to $B$.

Definition 5: A classification function for a problem $A$ is a polynomial time function Class from instances of $A$ to nonnegative integers, such that $\operatorname{Class}(y) \leq\|y\|$.

Typically, the classification function computes the number of "objects" the instance $y$ is build upon. For example, if $A$ is a problem on graphs, $\operatorname{Class}(y)$ could be the number of nodes of the graph $y$. If $A$ is a problem on propositional formulas, Class (y) could be the number of variables in the formula $y$.

Definition 6: A representative function for a problem $A$ is a polynomial time function Repr from nonnegative integers to instances of $A$ such that for every integer $n \geq 0$ it is the case that $\operatorname{Class}(\operatorname{Repr}(n))=n$ and $\|\operatorname{Repr}(n)\|$ is bounded
by a polynomial in $n$.
The representative function simply computes an element of a class. As an example, for problems of graphs the representative of the class $n$ may be the graph with $n$ nodes and no edge.

Definition 7: An extension function for a problem $A$ is a polynomial time function Ext from instances of $A$ and nonnegative integers to instances of $A$ such that, for each $y$ and $n \geq \operatorname{Class}(y)$, the instance $y^{\prime}=\operatorname{Ext}(y, n)$ satisfies the following conditions:

1) $y \in A$ if and only if $y^{\prime} \in A$;
2) $\operatorname{Class}\left(y^{\prime}\right)=n$.

An extension function increases the class of an instance without changing its membership to the language.

Let us give some intuition about these functions. Usually, an instance of a problem is composed of a set of objects combined in some way. For problems on Boolean formulas, we have a set of variables combined to form a formula. For graph problems, we have a set of nodes, and the graph is indeed a set of edges, which are pairs of nodes. The classification function gives the number of objects in an instance. The representative function thus gives an instance composed of the given number of objects. This instance should be in some way "symmetric", in the sense that its elements should be interchangeable (this is because the representative function must be determined only from the number of objects.) Possible results of the representative function can be the set of all clauses of three literals over a given alphabet, the complete graph over a set of nodes, the graph with no edges, etc.

Let, as an example, $A$ be the problem of propositional satisfiability. $\operatorname{Class}(F)$ could be the number of variables in formula $F$ and $\operatorname{Repr}(n)$ the set of all clauses of three literals over an alphabet of $n$ variables. A possible extension function simply adds a suitable number of tautological clauses.

These three functions are related to the problem $A$ only, and involve neither the specific problem $B$ we want to prove hard nor the specific reduction used. We now define a condition over the poly-time reduction from $A$ to $B$. Since $B$ is a problem of pairs, we can define a reduction from $A$ to $B$ as a pair of polynomial functions $\langle r, h\rangle$ such that $x \in A$ if and only if $\langle r(x), h(x)\rangle \in B$.

Definition 8: Given a problem $A$ with associated Class, Repr and Ext functions, a problem of pairs $B$, and a polynomial reduction $\langle r, h\rangle$ from $A$ to $B$, the condition of representative equivalence holds if, for any instance $y$ of $A$, the following holds:

$$
\begin{aligned}
& \langle r(y), h(y)\rangle \in B \quad \text { iff } \\
& \quad\langle r(\operatorname{Repr}(\operatorname{Class}(y)), h(y)\rangle \in B
\end{aligned}
$$

Representative equivalence requires an extension function Ext to exists, even if it is not mentioned in the "iff" condition. It can be shown that the existence of such a reduction from a PSPACE-hard problem $A$ to a problem $B$ proves that the latter is $\| \curvearrowleft$ PSPACE-hard [31].

## IV. Planning

A preliminary step to prove the claim about model checking in temporal logic is to show that a certain restriction of the planning problem STRIPS is $1 ん \leadsto$ PSPACE-hard.

Theorem 1: The problem of plan-existence in STRIPS $_{3}$ is $\| \rightsquigarrow$ PSPACE-hard if the varying part of an instance is the initial state.

Proof: We show a reduction from STRIPS ${ }_{2}$ to STRIPS $_{3}$ satisfying the condition of representative equivalence. It would had been formally more elegant to reduce STRIPS $_{2}$ to STRIPS $_{2}$ or to reduce $\mathrm{STRIPS}_{3}$ to $\mathrm{STRIPS}_{3}$, but to satisfy the condition of representative equivalence we need to introduce a precondition to each operator, thereby turning a STRIPS ${ }_{2}$ instance into a STRIPS $_{3}$ one.

As previously mentioned, the problem of plan existence for STRIPS $_{2}$ is PSPACE-hard. Its required three functions are:

Classification:

$$
\operatorname{Class}\left(\left\langle\left\{p_{1}, \ldots, p_{n}\right\}, \mathcal{O}, \mathcal{I}, \mathcal{G}\right\rangle\right)=n
$$

Representative:
$\operatorname{Repr}(n)=$
$\left\langle\left\{p_{1}, \ldots, p_{n}\right\}, O_{n}^{2 / 2}, \emptyset,\left\langle\left\{p_{1}\right\}, \emptyset\right\rangle\right\rangle$,
where $O_{n}^{2 / 2}$ is the set of all possible operators of at most two preconditions and two postconditions of $\left\{p_{1}, \ldots, p_{n}\right\}$ Extension:
$\operatorname{Ext}\left(\left\langle\left\{p_{1}, \ldots, p_{m}\right\}, \mathcal{O}, \mathcal{I}, \mathcal{G}\right\rangle, n\right)=$
$\left\langle\left\{p_{1}, \ldots, p_{m}, p_{m+1}, \ldots, p_{n}\right\}, \mathcal{O}, \mathcal{I}, \mathcal{G}\right\rangle$

The reduction produces an instance of STRIPS $_{3}$ by suitably modifying a STRIPS $2_{2}$ instance. In particular, new conditions are created and inserted in the initial state, and each operator is modified individually. For each operator $o=\langle\alpha, \beta, \gamma, \delta\rangle$ of the original instance, a condition $p_{o}$ is added to the set of conditions and to the initial state, and $o$ is changed into $o^{\prime}=\left\langle\alpha \cup\left\{p_{o}\right\}, \beta, \gamma, \delta\right\rangle ;$ this is a valid STRIPS ${ }_{3}$ instance because $\|\alpha\| \leq 2$ and $\|\beta\| \leq 2$ since the original instance is of STRIPS $_{2}$, therefore $\left\|\alpha \cup\left\{p_{o}\right\}\right\| \leq 3$. The goal is not changed. In this new instance, no operator modifies $p_{o}$, which therefore always remains true. As a result, each operator $o^{\prime}$ is executable if and only if $o$ is. Therefore, a plan exists for this new instance if and only if one exists for the original instance.

We prove that this reduction satisfies the condition of representative equivalence. Let $y$ be an instance of STRIPS $2_{2}$ and $\langle r(y), h(y)\rangle$ its corresponding STRIPS $_{3}$ instance, where $h(y)$ is the initial state and $r(y)$ the rest of the instance. We show that $\langle r(y), h(y)\rangle$ has the same plans of $\langle r(z), h(y)\rangle$, where $z=\operatorname{Repr}(\operatorname{Class}(y))$.

The two instances $y$ and $z$ have the same conditions and goal, but differ on the initial state and operators. In particular, $z$ has every operator with two preconditions and two postconditions, while $y$ in general may contain only some of them. The corresponding instances $\langle r(y), h(y)\rangle$ and $\langle r(z), h(y)\rangle$ therefore only differ for their operators, since the initial state $h(y)$ is the same in them.

In particular, $r(y)$ contains an operator $o^{\prime}=$ $\left\langle\alpha \cup\left\{p_{o}\right\}, \beta, \gamma, \delta\right\rangle$ for every operator $o=$ $\langle\alpha, \beta, \gamma, \delta\rangle$ of $y$ whereas $r(z)$ contains one such operator $o^{\prime}$ for each possible operator $o$ with two preconditions and two postconditions. However, $h(y)$ contains $p_{o}$ only if $o$ is an operator of $y$. As a result, the condition $p_{o}$ for an operator $o$ that is in $z$ but not in $y$ is not in the initial state of $\langle r(z), h(y)\rangle$. Since no operator has it as a postcondition, the corresponding operator $o^{\prime}$ is never executable. As a consequence, the operators in $\langle r(z), h(y)\rangle$ that are not in $\langle r(y), h(y)\rangle$ are not part of any plan of the former instance.

In order to complete the proof, the representation of conditions and operators has to be specified. Indeed, we have to make sure that replacing $r(y)$ with $r(z)$ in $\langle r(y), h(y)\rangle$ does not
create a mismatch between the representation of conditions and operators in $y$ and $z$.

The chosen representation has alphabet $\{0, \ldots, 9,(),,\langle\rangle, c$,$\} . A condition like p_{34}$ is represented simply by the two-character string 34. Similarly, the operator that has $p_{34}$ and $p_{1}$ as positive preconditions, no negative precondition or postcondition and $p_{123}$ as the positive postcondition, is represented by the string $\langle(34 c 1)()(123)()\rangle$. In such a string, ( represents what is commonly written as an open curly brace $\{$; in the same way, ) means the closed curly brace and $c$ stands for a comma.

A new condition $p_{o}$ created by the reduction is represented by the same string that represents $o$, the distinction between condition and operator being clear from the context. For instance, the example operator of the previous paragraph generates the new condition $p_{o}$ represented by the same string $\langle(34 c 1)()(123)()\rangle$ and the new operator $o^{\prime}$ represented by the string $\langle(34 c 1 c\langle(34 c 1)()(123)()\rangle)()(123)()\rangle$.

There is no recursion in this expression: while $o^{\prime}$ contains the string representing $o$, no condition $p_{o^{\prime}}$ is ever created.

The introduction of the new condition and the change to the operator do not superpolynomially increase size. Indeed, for each operator the new condition has the same size of the original operator, and the new operator is twice the size of the original operator.

Since STRIPS $_{k}$ limits preconditions to be at most $k$ and postconditions to be two, $\mathrm{STRIPS}_{3}$ is a restriction of $\mathrm{STRIPS}_{k}$ for every $k \geq 3$ and of unbounded STRIPS. As a result, the $\| \leadsto$ PSPACEhardness of STRIPS $3_{3}$ implies that all these problems are $\| \longmapsto$ PSPACE-hard as well.

In the opposite way, since STRIPS is in PSPACE, it is in $\mathbb{H} \leftrightarrow$ PSPACE as well, as well as all its restrictions. Together with the above hardness result, this proves that STRIPS and all $\mathrm{STRIPS}_{k}$ problems with $k \geq 3$ are $\mid \vdash \leadsto$ PSPACEcomplete.

## V. Interleaved asynchronous COMPOSITION

The main aim of this section is to show that the initial state problem of model checking is
$\| \curvearrowleft$ PSPACE-hard when the model is represented as an asynchronous interleaved composition of modules. This is achieved by reducing STRIPS $_{3}$, proved hard for that class in the previous section, to that problem.

Theorem 2: The initial state problem is $\| \curvearrowleft$ PSPACE-complete, if the model is represented as an asynchronous interleaved composition of modules and the varying part of the problem is the initial state.

Proof: We show a reduction from $\mathrm{STRIPS}_{3}$. In this reduction, the initial state is translated into the initial state, and the rest of the planning instance is translated into the model and the formula of the initial state problem.

An operator of a planning instance specifies conditions over the states before and after its execution. This can be directly translated into the formula of a model checking module. In particular, this formula is true if the previous state makes the operator executable, the effects of the operators are true in the next state and all other conditions maintain their truth value through the transition.

Let $\left\langle\left\{p_{1}, \ldots, p_{n}\right\}, \mathcal{O}, \mathcal{I},\left\langle\left\{p_{1}\right\}, \emptyset\right\rangle\right\rangle$ be an instance of STRIPS $_{3}$. For each operator $o_{i}=$ $\left\langle\alpha_{i}, \beta_{i}, \gamma_{i}, \delta_{i}\right\rangle$ in $\mathcal{O}$, we define a module $M_{i}=$ $\left(V_{i}^{L} \cup V^{S}\right.$, true, $\left.\varrho_{i}\right)$ where $V_{i}^{L}=\left\{n_{i}\right\}, V^{S}=$ $\left\{p_{1}, \ldots, p_{n}\right\}$, and $\varrho_{i}$ is as follows:

$$
\begin{aligned}
& \varrho_{i}=\left(n_{i} \not \equiv n_{i}^{\prime}\right) \wedge \bigwedge_{p_{j} \in \alpha_{i}} p_{j} \wedge \\
& \bigwedge_{p_{j} \in \gamma_{i}} p_{j}^{\prime} \wedge \\
& p_{j} \in \beta_{i} \\
& \bigwedge_{p_{j} \in \delta_{i}} \neg p_{j}^{\prime} \wedge \bigwedge_{p_{j} \notin \delta_{i} \cup \gamma_{i}}\left(p_{j}^{\prime} \equiv p_{j}\right)
\end{aligned}
$$

Each module represents the execution of the corresponding operator. Indeed, this formula can only be satisfied if the precondition of the operator is true in the previous state, the postcondition is true in the next state, and all other conditions maintain their truth values.

There is exactly one variable $n_{i}$ for each module. The subformula $n_{i} \not \equiv n_{i}^{\prime}$ ensures that if a module is "active", the others are not. Indeed, the transition function $\varrho$ of the composition is defined in (1) as the disjunction of a subformula for each module, and each subformula forces the local variables of the other modules to maintain their value. As a result, if the subformula corresponding to an operator is true, then the local
variables of all other modules will be forced to maintain their values.

The interleaved composition of the modules $\varrho_{i}$ is a model that allows, at each step, only the changes resulting from executing exactly one operator of the $\mathrm{STRIPS}_{3}$ instance: the variables $n_{i}$ and their subformulae ensure that the translation is what results from applying a single operators and not two or more at the same time while the rest of the formulae $M_{i}$ encode the behavior of an operator each. As a result, the existence of a plan equates the existence of a run of the model that starts from the given initial state and satisfies $p_{1}$ in some time point. This condition is the same as $E F p_{1}$ being true in the given initial state of the model.

Therefore, this is a translation from the problem of planning into the problem of checking whether the model has a run, starting from a given initial state, that satisfies a given formula. In this translation, the formula is fixed, the initial state is translated into the initial state and the rest of the instance is translated into the rest of the instance. This is therefore a nucomp reduction.

Technically, a nucomp reduction is a triple of functions $f_{1}, f_{2}$ and $g$ that operate on the fixed part of the planning problem $\left\{p_{1}, \ldots, p_{n}\right\}$, $\mathcal{O},\left\langle\left\{p_{1}\right\}, \emptyset\right\rangle$, the varying part of the planning problem $\mathcal{I}$ and its size $\|\mathcal{I}\|$. In particular:

$$
\begin{aligned}
& f_{1}\left(\left\langle\left\{p_{1}, \ldots, p_{n}\right\}, \mathcal{O},\left\langle\left\{p_{1}\right\}, \emptyset\right\rangle\right\rangle,\|\mathcal{I}\|\right) \\
& \quad \text { is the pair composed of the interleaved } \\
& \quad \text { asynchronous composition of the } M_{i} \text { 's } \\
& \quad \text { above and of the formula } E F p_{1} ; \\
& f_{2}\left(\left\langle\left\{p_{1}, \ldots, p_{n}\right\}, \mathcal{O},\left\langle\left\{p_{1}\right\}, \emptyset\right\rangle\right\rangle,\|\mathcal{I}\|\right) \\
& \quad \text { is } E F p_{1} ; \\
& g(x, \mathcal{I}) \\
& \quad \text { is } \mathcal{I} \text { for every } x .
\end{aligned}
$$

In this specific case $f_{2}$ is not really needed; yet, the definition of nucomp reduction dictates that one has to be provided. The choice of $E F p_{1}$ for its value is arbitrary.

We now formally prove that the reduction works as required. We assume that the planning instances always contain a "nop" operator, that is, $\langle\emptyset, \emptyset, \emptyset, \emptyset\rangle$; this is necessary to ensure that transitions are still possible in the model after the goal is reached. We denote by $N$ the set of all variables $n_{i}$.

1. correspondence between operators and modules

Let $s$ and $o_{i}$ be respectively a state and an operator of the $\mathrm{STRIPS}_{3}$ instance, and let $s^{\prime}=$ $r\left(s, o_{i}\right)$. We prove that both $\left\langle s \cup N_{i} \cup\left\{n_{i}\right\}, s^{\prime} \cup N_{i}\right\rangle$ and $\left\langle s \cup N_{i}, s^{\prime} \cup N_{i} \cup\left\{n_{i}\right\}\right\rangle$ are pairs of previous/next states allowed by the model, for every $N_{i} \subseteq N \backslash\left\{n_{i}\right\}$. We also prove the converse: if a pair of states is allowed by the model, then it is either in the form $\left\langle s \cup N_{i} \cup\left\{n_{i}\right\}, s^{\prime} \cup N_{i}\right\rangle$ or $\left\langle s \cup N_{i}, s^{\prime} \cup N_{i} \cup\left\{n_{i}\right\}\right\rangle$, where $s^{\prime}=r\left(s, o_{i}\right)$ and $N_{i}$ is a subset of $N \backslash\left\{n_{i}\right\}$.

Assume that $s^{\prime}=r\left(s, o_{i}\right)$ and let $o_{i}=$ $\langle\alpha, \beta, \gamma, \delta\rangle$. Since $r\left(s, o_{i}\right)$ is not undefined, this operator is executable in $s$. As a result, the formula $\bigwedge_{p_{i} \in \alpha} p_{i} \wedge \bigwedge_{p_{i} \in \beta} \neg p_{i}$ is true in the state $s \cup N_{i} \cup\left\{n_{i}\right\}$. Since $s^{\prime}$ differs from $s$ only by the addition of $\gamma$ and deletion of $\delta$, the formula $\bigwedge_{p_{i} \in \gamma} p_{i}^{\prime} \wedge \bigwedge_{p_{i} \in \delta} \neg p_{i}^{\prime} \wedge \bigwedge_{p_{i} \notin \delta \cup \gamma}\left(p_{i}^{\prime} \equiv p_{i}\right)$ holds as well in the resulting state $s^{\prime} \cup N_{i}$. The same holds for the pair $\left\langle s \cup N_{i}, s^{\prime} \cup N_{i} \cup\left\{n_{i}\right\}\right\rangle$.

This proves that $\varrho_{i}$ is satisfied by all considered pairs. Since the values of $n_{j}$ for $j \neq i$ is the same in the state before and after the transition, and these are the local variables of the other modules, the $i$-th disjunct of $\varrho$ (see equation 1 ) is satisfied, which means that $\varrho$ itself is satisfied as well.

Let us now prove the converse. Let $s, s^{\prime}$ be a pair of previous/next state allowed by the model. Since $\varrho$ is true, (at least) one of its disjuncts has to be satisfied. Let it be the $i$-th one. We prove that $s^{\prime} \backslash N=r\left(s \backslash N, o_{i}\right)$.

Since the $i$-th subformula of $\varrho$ is satisfied, all variables $N$ have the same value in $s$ and $s^{\prime}$ but $n_{i}$, which has opposite values. Furthermore, $\varrho_{i}$ is satisfied by the values of variables in $P$. This means that all variables in $\alpha$ are true and all those in $\beta$ are false in $s$, that the variables in $\gamma$ are true and those in $\delta$ are false in $s^{\prime}$, and that all other variables maintain their values between $s$ and $s^{\prime}$. This proves that $s^{\prime} \backslash N=r\left(s \backslash N, o_{i}\right)$.

## 2. Correspondence between sequences

We proved that the execution of an operator corresponds to a transition allowed by the model and vice versa. By using the same initial state, the result of applying a sequence of operators is an allowed multi-step transition according to the model, if one disregards the values of the variables $N_{i}$.

As a result, the existence of a plan implies that $E F p_{1}$ holds in the initial state of the model. Since plan existence is $\| \leadsto$ PSPACE-complete, it follows that the initial state problem is $\| \curvearrowright$ PSPACE-hard. Membership to the same class follows from the initial state problem being in PSPACE.

The result of the preprocessing phase can be considered as a representation of the set of states satisfying the initial state problem. As a consequence, the above theorem has the following result as a corollary.

Corollary 1: The polynomial hierarchy collapses if, given a formula and a model represented by the asynchronous interleaved composition of modules, there always exists a polynomially sized data structure that allows checking whether a state is a solution of the initial state problem in polynomial time.

This result has a practical implication on algorithms using the labeling technique for solving model checking. The labeling technique works by determining the set of states satisfying the subformulas of the formula, starting from the literals and proceeding in a bottom-up manner. Since the number of states is exponential in the number of variables, BDDs are used to represent these sets. The procedure ends when we generate a BDD representing the set of initial states satisfying the formula. This is exactly a data structure that allows checking whether a state is an initial state satisfying the formula in polynomial time. Therefore, the above corollary applies to this case.

Corollary 2: Unless the polynomial hierarchy collapses to its second level, the BDD representation of the initial states satisfying a formula and a model defined as an asynchronous interleaved composition of modules is, in the worst case, superpolynomial in the size of the formula and model.

This result theoretically confirms the empirical observations about the size increase of BDDs in model checking. The previous corollary however qualifies it as not being specific to BDDs, as the same holds for any other formalism with similar features. In fact, the same result also applies to RBCs, MTBDDs, SDDs, and ADDs.

## VI. Synchronous composition

When modules are composed synchronously, all their formulas $\varrho_{i}$ apply to every transition. Formally, the difference from the interleaved asynchronous composition is the way the formulas $\varrho_{i}$ are composed to form $\varrho$.

We show a translation from asynchronous to synchronous composition that does not involve the initial state. This reduction adds a shared variable $a_{i}$ to each module. A new module $\varrho_{0}^{a}$ is created, and each module $\varrho_{i}$ is translated into $\varrho_{i}^{a}$ as follows:

$$
\varrho_{i}^{a}=a_{i} \rightarrow\left(\varrho_{i} \wedge \bigwedge_{\substack{j=1, \ldots, k \\ j \neq i}} V_{j}^{L} \equiv V_{j}^{L^{\prime}}\right)
$$

The new module $\varrho_{0}^{a}$ enforces exactly one $a_{i}$ to be true at each time step:

$$
\varrho_{0}^{a}=\bigvee_{i=1, \ldots, k}\left(a_{i} \wedge \bigwedge_{\substack{j=1, \ldots, k \\ j \neq i}} \neg a_{j}\right)
$$

Intuitively, this reduction works because, at any time point, at least one of the variables $a_{i}$ has to be true, but no more then one. If $a_{i}$ is false, then $\varrho_{i}^{a}$ is satisfied. If $a_{i}$ is true, then $\varrho_{i}^{a}$ becomes equivalent to the $i$-th disjunct of $\varrho$, the composed formula of the asynchronous composition, as defined in (1). This means that $\varrho$ is true if and only if the pair of states satisfies the corresponding formula for the interleaved asynchronous composition.

Theorem 3: Disregarding the variables $a_{i}$, the models that are the asynchronous interleaved composition of the $\varrho_{1}, \ldots, \varrho_{k}$ and the synchronous composition of $\varrho_{0}^{a}, \varrho_{1}^{a} \ldots, \varrho_{k}^{a}$ are the same.

Proof: We show that $\varrho^{a}$, the synchronous composition of $\varrho_{0}^{a}, \varrho_{1}^{a} \ldots, \varrho_{k}^{a}$, is essentially the same as $\varrho$, the asynchronous interleaved composition of the $\varrho_{1}, \ldots, \varrho_{k}$, the only difference being the values of the new variables $a_{i}$.

$$
\begin{aligned}
\varrho^{a}= & \bigwedge_{i=0, \ldots, k} \varrho_{i}^{a} \\
= & \bigvee_{i=1, \ldots, k}\left(a_{i} \wedge \bigwedge_{\substack{j=1, \ldots, k \\
j \neq i}} \neg a_{j}\right) \wedge \\
& \bigwedge_{i=1, \ldots, k} a_{i} \rightarrow\left(\varrho_{i} \wedge \bigwedge_{\substack{j=1, \ldots, k \\
j \neq i}} V_{j}^{L} \equiv V_{j}^{L^{\prime}}\right) \\
= & \bigvee_{i=1, \ldots, k}\left(a_{i} \wedge \bigwedge_{\substack{j=1, \ldots, k \\
j \neq i}} \neg a_{j} \wedge\right. \\
& \left.\bigwedge_{h=1, \ldots, k} a_{h} \rightarrow\left(\varrho_{h} \wedge \bigwedge_{\substack{j=1, \ldots, k \\
j \neq h}} V_{j}^{L} \equiv V_{j}^{L^{\prime}}\right)\right)
\end{aligned}
$$

If $h \neq i$ the formula $a_{h} \rightarrow \ldots$ is entailed by $\neg a_{h}$. As a result, $\varrho^{a}$ is also equivalent to:

$$
\begin{aligned}
\varrho^{a}= & \bigvee_{i=1, \ldots, k}\left(a_{i} \wedge \bigwedge_{\substack{j=1, \ldots, k \\
j \neq i}} \neg a_{j} \wedge\right. \\
& \left.a_{i} \rightarrow\left(\varrho_{i} \wedge \bigwedge_{\substack{j=1, \ldots, k \\
j \neq i}} V_{j}^{L} \equiv V_{j}^{L^{\prime}}\right)\right)
\end{aligned}
$$

This formula can be further simplified:

$$
\begin{aligned}
\varrho^{a}= & \bigvee_{\substack{i=1, \ldots, k}}\left(a_{i} \wedge \bigwedge_{\substack{j=1, \ldots, k \\
j \neq i}} \neg a_{j} \wedge\right. \\
& \left.\left(\varrho_{i} \wedge \bigwedge_{\substack{j=1, \ldots, k \\
j \neq i}} V_{j}^{L} \equiv V_{j}^{L^{\prime}}\right)\right)
\end{aligned}
$$

Apart from the variables $a_{i}$, this formula is equivalent to the one defining the allowed transitions of the interleaved asynchronous composition of modules $\varrho_{i}$. Formally, the variables $a_{i}$ can be removed by performing the operation of forgetting [32]; the resulting formula is equivalent to (1).

From the above theorem we can derive the class of the initial state problem when the model is a synchronous composition of modules.

Corollary 3: The initial state problem is $\| \curvearrowleft$ PSPACE-complete if the initial state is the varying part of the instances and the model is represented as a synchronous composition of modules.

As a result, the statement of Corollary 1 holds even in the case of synchronous composition of modules: unless the polynomial hierarchy collapses, the set of the initial states of a model satisfying a property cannot in general be represented by a data structure that is polynomially sized and allows checking a state in polynomial time. As in the case of asynchronous composition of modules, this results concerns BDDs but also BRBCs, MTBDDs, SDDs, and ADDs.

## VII. DISCUSSION AND RELATED WORK

This article presents results about preprocessing in Temporal Logic Model Checking. These results extend, refine and improve over a previous work by the same authors [9] where the fixed part is the formula and the varying one is the model or vice versa. In particular, the present article strengthens these results by placing the initial state only in the varying part of the problem, so that the formula and most of the model are fixed. This result also appeared in a technical report [33], where the formula is however not assumed to be a composition of modules.

The results in the present article also improve over those about the exponential growth of BDDs with respect to a particular problem (e.g. integer multiplication [34]) and about the size growth of other decision diagrams with respect to particular problems [35]. While these results are not conditional to the collapse of the polynomial hierarchy as the ones reported in this paper, they are also less general, as they concern only specific data structures (e.g. OBDDs) and specific problems (e.g. integer multiplication).

Also related is the computational analysis of model checking, in particular its parametrized complexity [36]. All the considered problems, where the input is a synchronized product of $k$ components, where $k$ is the parameter, are shown intractable even in the parametrized case. A more detailed comparison of the differences and similarities between parametrized complexity and the classes like $\| \curvearrowleft$ PSPACE is presented by Cadoli, Donini, Liberatore and Schaerf [28].

Other related results are [37]: the complexity of model checking does not decrease under the hypotheses of some structural restrictions (e.g. treewidth) in the input; although a CNF formula of bounded treewidth can be represented by an OBDD of polynomial size, the nice properties of treewidth-bounded CNF formulas are not preserved under existential quantification or unrolling, which is a basic operation of model checking algorithms.

## REFERENCES

[1] E. Clarke, O. Grumberg, and D. Peled, Model Checking. MIT Press, 2000.
[2] I. Beer, S. B. David, D. Geist, R. Gewirtzman, and M. Yoeli, "Methodology and system for practical formal verification of reactive hardware," in Proceedings of the 6th International Conference on Computer-Aided Verification (CAV'94), ser. LNCS, vol. 818. Springer, 1994, pp. 182-193.
[3] G. Holzmann, "The model checker SPIN," IEEE Transactions on Software Engineering, vol. 23(5), pp. 279-295, 1997.
[4] K. McMillan, Symbolic Model Checking. Kluwer Academic, 1993.
[5] A. Cimatti, E. Clarke, E. Giunchiglia, F. Giunchiglia, M. Pistore, M. Roveri, R. Sebastiani, and A. Tacchella, "NuSMV 2: An open-source tool for symbolic model checking," in Proceedings of the 14th International Conference on Computer-Aided Verification (CAV'02), 2002.
[6] M. Kwiatkowska, G. Norman, and D. Parker, "PRISM 4.0: Verification of probabilistic real-time systems," in Proc. 23rd International Conference on Computer Aided Verification ( $C A V^{\prime} 11$ ), ser. LNCS, G. Gopalakrishnan and S. Qadeer, Eds., vol. 6806. Springer, 2011, pp. 585-591.
[7] A. Pnueli, "The temporal logic of programs," in Proceeding of the 18th IEEE Symposium on Foundations of Computer Science (FOCS'77), 1977, pp. 46-57.
[8] P. Schnoebelen, "The complexity of temporal logic model checking." Advances in modal logic, vol. 4, no. 393-436, p. 35, 2002.
[9] A. Ferrara, P. Liberatore, and M. Schaerf, "Model checking and preprocessing," in $A I^{*} I A, 2007$, pp. 48-59.
[10] J. R. Burch, E. M. Clarke, K. L. McMillan, D. L. Dill, and L. J. Hwang, "Symbolic model checking: $10^{20}$ states and beyond," Inf. Comput., vol. 98, no. 2, pp. 142-170, 1992.
[11] R. E. Bryant, "Graph-based algorithms for boolean function manipulation," Computers, IEEE Transactions on, vol. 100, no. 8, pp. 677-691, 1986.
[12] P. Kissmann and J. Hoffmann, "BDD ordering heuristics for classical planning," Journal of Artificial Intelligence Research, pp. 779-804, 2014.
[13] B. Kell, A. Sabharwal, and W.-J. van Hoeve, "BDD-guided clause generation," in Integration of AI and OR Techniques in Constraint Programming. Springer, 2015, pp. 215-230.
[14] S. Edelkamp, P. Kissmann, and A. Torralba, "BDDs strike back (in ai planning)," in Twenty-Ninth AAAI Conference on Artificial Intelligence, 2015.
[15] Y. Breitbart, H. Hunt, and D. Rosenkrantz, "On the size of binary decision diagrams representing boolean functions," Theoretical Computer Science, vol. 145, no. 1, pp. 45-69, 1995.
[16] R. E. Bryant and Y.-A. Chen, "Verification of arithmetic circuits using binary moment diagrams," STTT, vol. 3, no. 2, pp. 137-155, 2001.
[17] P. Abdullah, P. Bjesse, and N. Een, "Symbolic reachability analysis based on SAT-solvers," in Proceedings of the 6th International Conference on Tools and Algorithms for the Construction and Analisys of Systems (TACAS'00), 2000.
[18] E. Clarke, M. Fujita, P. McGeer, K. McMillan, J. Yang, and X. Zhao, "Multi terminal binary decision diagrams: An efficient data structure for matrix representation," in Proceedings of the International Workshop on Logic and Synthesis, 1993, pp. 1-15.
[19] R. Bahar, E. Frohm, C. Gaona, C. Hachtel, G. Macii, and F. Somenzi, "Algebraic decision diagrams and their applications," in Proceedings of the International Conference CAD, 1993, pp. 188-191.
[20] A. Darwiche, "SDD: A new canonical representation of propositional knowledge bases," in IJCAI ProceedingsInternational Joint Conference on Artificial Intelligence, vol. 22, no. 1, 2011, p. 819.
[21] R. Fikes and N. Nilsson, "STRIPS: a new approach to the application of theorem proving to problem solving," Artificial Intelligence, vol. 2, pp. 189-208, 1971.
[22] A. Sistla and E. Clarke, "The complexity of propositional linear temporal logics," Journal of ACM, vol. 32(3), pp. 733$749,1985$.
[23] T. Bylander, "The computational complexity of propositional STRIPS planning," Artificial Intelligence, vol. 69, no. 1-2, pp. 165-204, 1994.
[24] M. R. Garey and D. S. Johnson, Computers and Intractability: A Guide to the Theory of NP-Completeness. San Francisco, Ca: W.H. Freeman and Company, 1979.
[25] D. S. Johnson, "A catalog of complexity classes," in Handbook of Theoretical Computer Science, J. van Leeuwen, Ed. Elsevier Science Publishers (North-Holland), Amsterdam, 1990, vol. A, ch. 2, pp. 67-161.
[26] L. J. Stockmeyer, "The polynomial-time hierarchy," Theoretical Computer Science, vol. 3, pp. 1-22, 1976.
[27] A. R. Klivans and D. Van Melkebeek, "Graph nonisomorphism has subexponential size proofs unless the polynomialtime hierarchy collapses," SIAM Journal on Computing, vol. 31, no. 5, pp. 1501-1526, 2002.
[28] M. Cadoli, F. Donini, P. Liberatore, and M. Schaerf, "Preprocessing of intractable problems," Information and Computation, vol. 176, pp. 89-120, 2002.
[29] P. V. E. Boas, "Machine models and simulations," in Handbook of Theoretical Computer Science, volume A, J. Van Leeuwen, Ed. Elsevier, 2014, pp. 1-66.
[30] J. M. Robson, "N by N checkers is exptime complete," SIAM Journal on Computing, vol. 13, no. 2, pp. 252-267, 1984.
[31] P. Liberatore, "Monotonic reductions, representative equivalence, and compilation of intractable problems," Journal of $A C M$, vol. 48, no. 6, pp. 1091-1125, 2001.
[32] J. Lang, P. Liberatore, and P. Marquis, "Propositional independence - formula-variable independence and forgetting," Journal of Artificial Intelligence Research, vol. 18, pp. 391443, 2003.
[33] A. Ferrara, P. Liberatore, and M. Schaerf, "On the size of data structures used in symbolic model checking," Computing Research Repository (CoRR), vol. abs/1012.3018, 2010. [Online]. Available: http://arxiv.org/abs/1012.3018
[34] R. Bryant, "On the complexity of VLSI implementations and graph representations of boolean functions with application
to integer multiplication," IEEE Transactions on Computers, vol. 40, pp. 205-213, 1991.
[35] R. Drechsler and D. Sieling, "Binary decision diagrams in theory and practice," International Journal on Software Tools for Technology Transfer (STTT), vol. 3, no. 2, pp. 112136, May 2001.
[36] S. Demri, F. Laroussinie, and P. Schnoebelen, "A parametric analysis of the state explosion problem in model checking," in STACS, 2002, pp. 620-631.
[37] A. Ferrara, G. Pan, and M. Y. Vardi, "Treewidth in verification: Local vs. global," in LPAR, 2005, pp. 489-503.

